Low-power 2-input NAND gate Rev. 7 — 23 April 2019

**Product data sheet** 

### 1. General description

The 74AUP1G00 provides the single 2-input NAND function.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption;  $I_{CC} = 0.9 \mu A$  (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- IOFF circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C



# 3. Ordering information

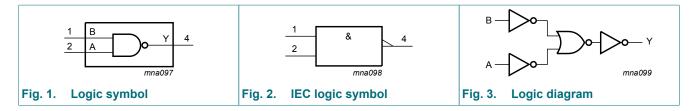
Type number	Package							
	Temperature range	Name	Description	Version				
74AUP1G00GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1				
74AUP1G00GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886				
74AUP1G00GF	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1 × 0.5 mm	SOT891				
74AUP1G00GN	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 × 1.0 × 0.35 mm	SOT1115				
74AUP1G00GS	-40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 × 1.0 × 0.35 mm	SOT1202				
74AUP1G00GX	-40 °C to +125 °C	X2SON5	X2SON5: plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 × 0.8 × 0.35 mm	SOT1226				

### 4. Marking

Table 2. Marking	
Type number	Marking code [1]
74AUP1G00GW	рА
74AUP1G00GM	рА
74AUP1G00GF	рА
74AUP1G00GN	рА
74AUP1G00GS	рА
74AUP1G00GX	pA

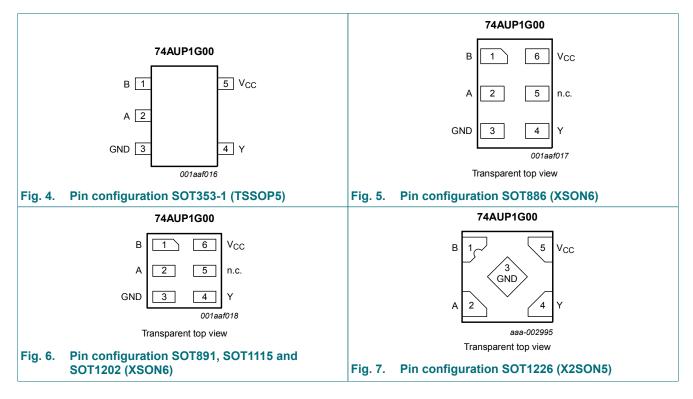
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

## 5. Functional diagram



## 6. Pinning information





### 6.2. Pin description

Symbol	Pin		Description
	TSSOP5 and X2SON5	XSON6	
В	1	1	data input
A	2	2	data input
GND	3	3	ground (0 V)
Y	4	4	data output
n.c.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

### Table 3. Pin description

## 7. Functional description

#### Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

Input		Output
Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CC</sub>	supply voltage			-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>I</sub> < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V		-50	-	mA
Vo	output voltage	Active mode	[1]	-0.5	V <sub>CC</sub> + 0.5	V
		Power-down mode	[1]	-0.5	+4.6	V
I <sub>O</sub>	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±20	mA
I <sub>CC</sub>	supply current			-	+50	mA
I <sub>GND</sub>	ground current			-50	-	mA
T <sub>stg</sub>	storage temperature			-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb}$ = -40 °C to +125 °C	[2]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For TSSOP5 packages: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.

For XSON6 and X2SON5 packages: above 118 °C the value of Ptot derates linearly with 7.8 mW/K.

### 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
V <sub>O</sub>	output voltage	Active mode	0	V <sub>CC</sub>	V
		Power-down mode; $V_{CC}$ = 0 V	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V

# **10. Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	25 °C					_
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.75V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.11	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.32	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	2.05	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.9	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.72	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.6	-	-	V
V <sub>OL</sub>	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.31	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.31	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.44	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.31	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.1	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.2	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 V; I_{O} = 0 A; V_{CC} = 3.3 V [1]$	-	-	40	μA
CI	input capacitance	$V_{CC}$ = 0 V to 3.6 V; V <sub>I</sub> = GND or V <sub>CC</sub>	-	0.8	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF

5 / 19

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = -	40 °C to +85 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.65V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.35V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	-	0.9	V
V <sub>OH</sub>	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$				
	voltage	$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.1	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.7V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	1.03	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.30	-	-	V
		I <sub>O</sub> = -2.3 mA; V <sub>CC</sub> = 2.3 V	1.97	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.85	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.67	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.55	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.3V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.33	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.45	V
l <sub>l</sub>	input leakage current	$V_{I}$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.5	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA
I <sub>CC</sub>	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V} $ [1]	-	-	50	μA
T <sub>amb</sub> = -	40 °C to +125 °C		I		1	1
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	0.75V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	0.70V <sub>CC</sub>	-	-	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	0.25V <sub>CC</sub>	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	0.30V <sub>CC</sub>	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	_	-	0.9	V

74AUP1G00

Product data sheet

### Low-power 2-input NAND gate

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>OH</sub>	HIGH-level output	V <sub>I</sub> = V <sub>IH</sub> or V <sub>IL</sub>				
	voltage	$I_{O}$ = -20 µA; $V_{CC}$ = 0.8 V to 3.6 V	V <sub>CC</sub> - 0.11	-	-	V
		I <sub>O</sub> = -1.1 mA; V <sub>CC</sub> = 1.1 V	0.6V <sub>CC</sub>	-	-	V
		I <sub>O</sub> = -1.7 mA; V <sub>CC</sub> = 1.4 V	0.93	-	-	V
		I <sub>O</sub> = -1.9 mA; V <sub>CC</sub> = 1.65 V	1.17	-	-	V
		$I_0$ = -2.3 mA; $V_{CC}$ = 2.3 V	1.77	-	-	V
		I <sub>O</sub> = -3.1 mA; V <sub>CC</sub> = 2.3 V	1.67	-	-	V
		I <sub>O</sub> = -2.7 mA; V <sub>CC</sub> = 3.0 V	2.40	-	-	V
		I <sub>O</sub> = -4.0 mA; V <sub>CC</sub> = 3.0 V	2.30	-	-	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = 20 µA; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	0.33V <sub>CC</sub>	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		I <sub>O</sub> = 2.3 mA; V <sub>CC</sub> = 2.3 V	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
I <sub>I</sub>	input leakage current	$V_1$ = GND to 3.6 V; $V_{CC}$ = 0 V to 3.6 V	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	$V_1$ or $V_0$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.75	μA
ΔI <sub>OFF</sub>	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I <sub>CC</sub>	supply current	$V_I = GND \text{ or } V_{CC}; I_O = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
ΔI <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V} $ [1]	-	-	75	μA

[1] One input at  $V_{CC}$  - 0.6 V, other input at  $V_{CC}$  or GND.

# **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9

Symbol	Parameter	Conditions	Min	Typ [1]	Мах	Unit
T <sub>amb</sub> = 2	5 °C; C <sub>L</sub> = 5 pF					
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u> [2]				
		V <sub>CC</sub> = 0.8 V	-	17.5	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.5	5.3	11.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.0	3.8	6.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.6	3.1	5.3	ns
		$V_{CC}$ = 2.3 V to 2.7 V	1.3	2.5	4.0	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	2.2	3.6	ns

### Low-power 2-input NAND gate

Symbol	Parameter	Conditions	Min	Тур [1]	Мах	Unit
T <sub>amb</sub> = 2	25 °C; C <sub>L</sub> = 10 pF	, ,				_
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u>	[2]			
		V <sub>CC</sub> = 0.8 V	-	21.0	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.4	6.1	13.0	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.4	4.4	7.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	3.7	6.2	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.4	3.0	4.7	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.3	2.8	4.3	ns
T <sub>amb</sub> = 2	25 °C; C <sub>L</sub> = 15 pF	, ,				_
t <sub>pd</sub>	propagation delay	A, B to Y; see Fig. 8	[2]			
		V <sub>CC</sub> = 0.8 V	-	24.5	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.4	6.9	14.8	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.8	5.0	8.9	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	4.1	7.0	ns
		V <sub>CC</sub> = 2.3 V to 2.7 V	1.7	3.5	5.3	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.6	3.2	4.9	ns
T <sub>amb</sub> = 2	25 °C; C <sub>L</sub> = 30 pF	, ,				_
t <sub>pd</sub>	propagation delay	A, B to Y; see Fig. 8	[2]			
		V <sub>CC</sub> = 0.8 V	-	34.8	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.6	9.2	20.1	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	3.0	6.5	11.8	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.6	5.4	9.3	ns
		$V_{\rm CC}$ = 2.3 V to 2.7 V	2.4	4.6	7.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.3	4.3	6.5	ns
T <sub>amb</sub> = 2	25 °C	, ,				_
C <sub>PD</sub>	power dissipation	$f = 1 MHz; V_I = GND to V_{CC}$	[3]			
	capacitance	V <sub>CC</sub> = 0.8 V	-	2.6	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	2.8	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	2.9	-	pF
		V <sub>CC</sub> = 1.65 V to 1.95 V	-	3.1	-	pF
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	3.6	-	pF
		V <sub>CC</sub> = 3.0 V to 3.6 V	-	4.2	-	pF

[1] All typical values are measured at nominal V<sub>CC</sub>.

[1] Fin typical values are included up in the end of t

 $f_i$  = input frequency in MHz;

 $f_o$  = output frequency in MHz;

 $C_L$  = output load capacitance in pF;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$  = sum of the outputs.

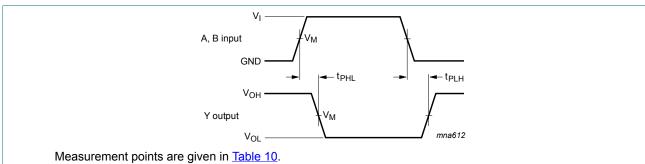
### Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 9

Symbol	Parameter	Conditions	-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Max	Min	Max	
C <sub>L</sub> = 5 p	F			<u> </u>			_
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u> [1]					
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.1	12.2	2.1	13.5	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	1.8	7.8	1.8	8.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.4	6.2	1.4	6.9	ns
		$V_{CC}$ = 2.3 V to 2.7 V	1.1	4.7	1.1	5.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.0	4.2	1.0	4.7	ns
C <sub>L</sub> = 10	pF						
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u> [1]					
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.2	14.4	2.2	15.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.2	9.2	2.2	10.2	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	1.9	7.3	1.9	8.1	ns
		$V_{CC}$ = 2.3 V to 2.7 V	1.3	5.6	1.3	6.2	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.2	4.9	1.2	5.4	ns
C <sub>L</sub> = 15	pF						_
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u> [1]					
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.1	16.5	3.1	18.2	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.5	10.5	2.5	11.6	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.0	8.3	2.0	9.2	ns
		$V_{CC}$ = 2.3 V to 2.7 V	1.5	6.4	1.5	7.1	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	1.4	5.7	1.4	6.3	ns
C <sub>L</sub> = 30	pF					1	
t <sub>pd</sub>	propagation delay	A, B to Y; see <u>Fig. 8</u> [1]					
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.1	22.6	4.1	24.9	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	2.9	14.0	2.9	15.4	ns
		V <sub>CC</sub> = 1.65 V to 1.95 V	2.3	11.1	2.3	12.3	ns
		$V_{CC}$ = 2.3 V to 2.7 V	2.1	8.5	2.1	9.4	ns
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.1	7.6	2.1	8.4	ns

[1]  $t_{pd}$  is the same as  $t_{PLH}$  and  $t_{PHL}$ .

### 11.1. Waveforms and test circuit

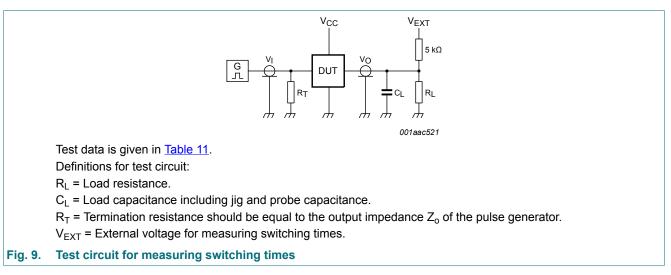


Logic levels: V<sub>OL</sub> and V<sub>OH</sub> are typical output voltage drop that occur with the output load.

### Fig. 8. The data input (A or B) to output (Y) propagation delays

### Table 10. Measurement points

Supply voltage	Input	Output		
V <sub>cc</sub>	V <sub>M</sub>	VI	$t_r = t_f$	V <sub>M</sub>
0.8 V to 3.6 V	0.5 × V <sub>CC</sub>	V <sub>CC</sub>	≤ 3.0 ns	$0.5 \times V_{CC}$



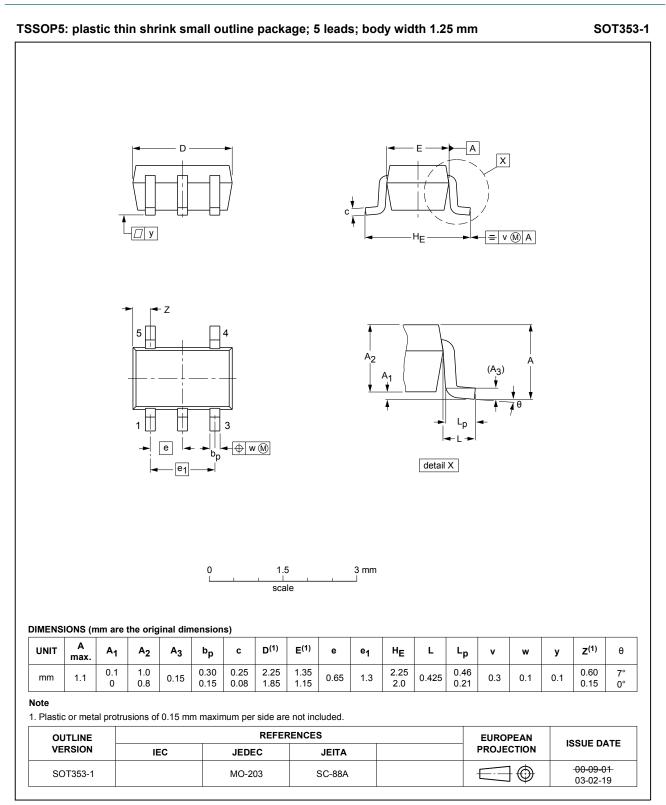
#### Table 11. Test data

Supply voltage	Load		V <sub>EXT</sub>			
V <sub>cc</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 × V <sub>CC</sub>	

[1] For measuring enable and disable times  $R_L = 5 k\Omega$ .

For measuring propagation delays, setup and hold times and pulse width  $R_L$  = 1 M $\Omega$ .

## 12. Package outline



#### Fig. 10. Package outline SOT353-1 (TSSOP5)

74AUP1G00

All information provided in this document is subject to legal disclaimers

### Low-power 2-input NAND gate

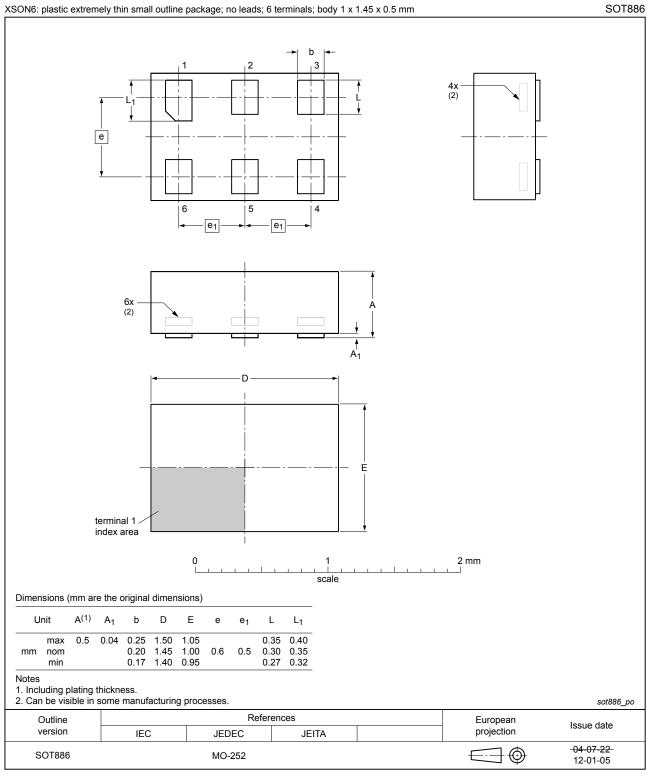


Fig. 11. Package outline SOT886 (XSON6)

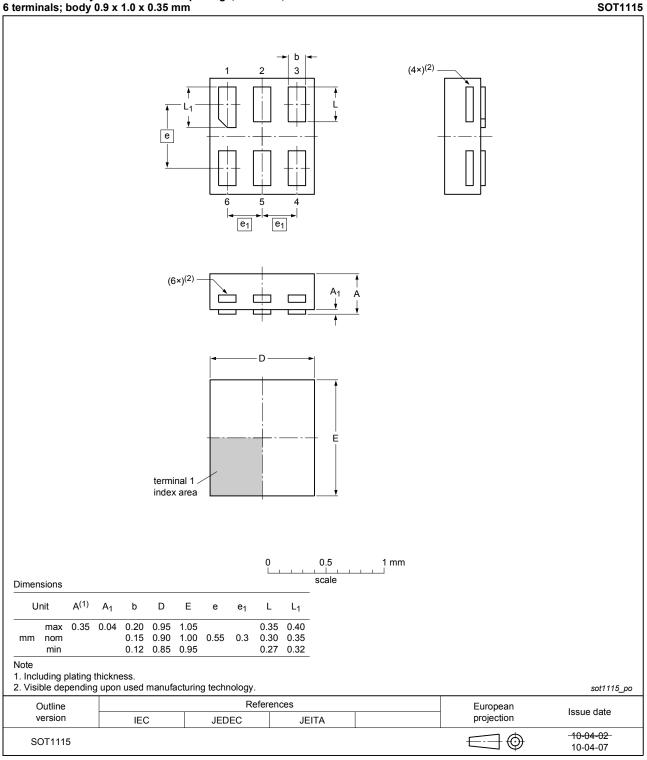
### Low-power 2-input NAND gate

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm							SOT					
			e	- L <sub>1</sub> <u>L</u> 							4x (1)	
			6 (1	× !)								
			terr	- ninal 1 - ex area			- D					
	SIONS (I					ons)	 	<u> </u>	1 scale		2 mm	
UNIT	A max	A <sub>1</sub> max	<b>b</b> 0.20	<b>D</b> 1.05	<b>E</b> 1.05	e	e <sub>1</sub>	L 0.35	L <sub>1</sub> 0.40			
mm lote	0.5	0.04	0.12	0.95	0.95	0.55	0.35	0.00	0.32			
. Can b	be visible	e in sor	me manı	ufacturir	ng proce	sses.						
OUTLINE VERSION		_		-0			REFER				EUROPEAN PROJECTION	ISSUE DATE
	OT891		IE	C	JEDEC			JEITA				<del>-05-04-06</del>
												07-05-15

Fig. 12. Package outline SOT891 (XSON6)

#### Low-power 2-input NAND gate

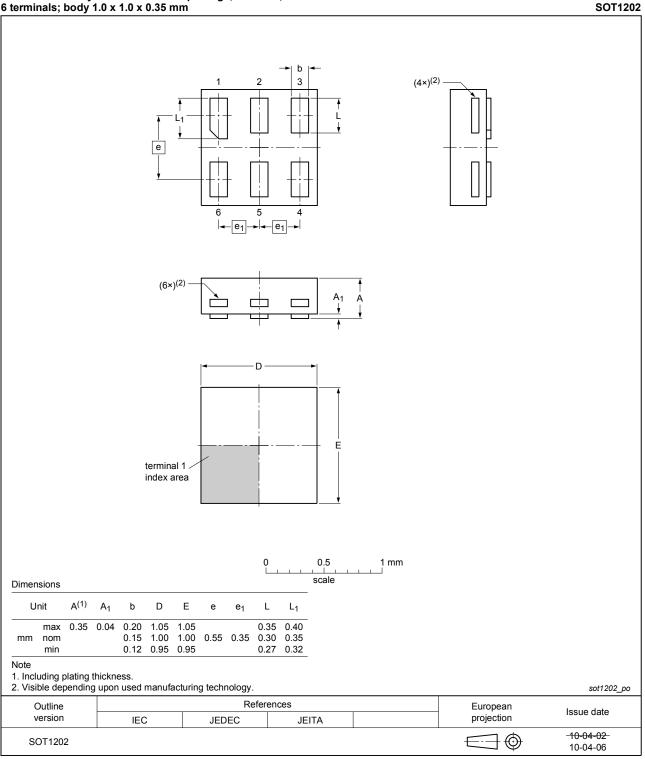
#### XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm





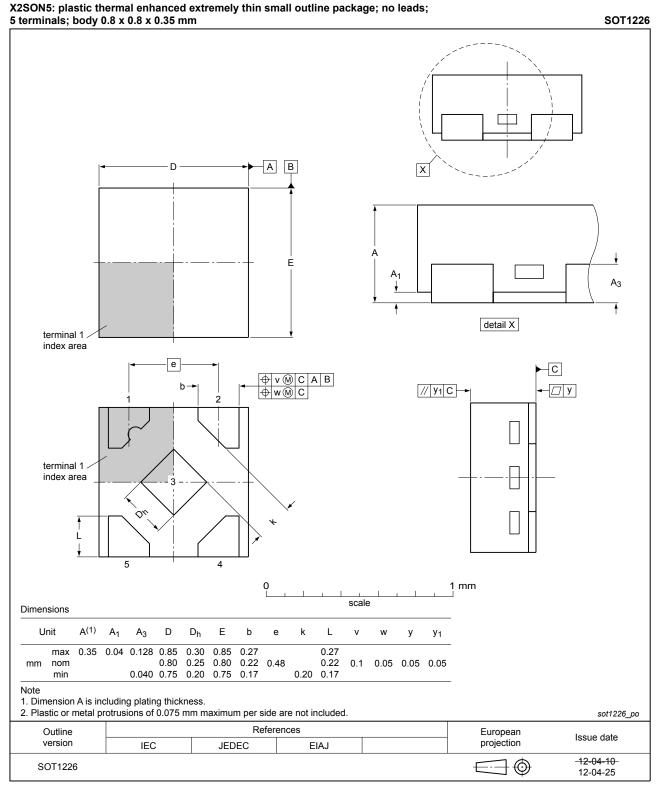
#### Low-power 2-input NAND gate

#### XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm





#### Low-power 2-input NAND gate





74AUP1G00

## 13. Abbreviations

Acronym	Description
CDM	Charged Device Model
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model

## 14. Revision history

#### Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes					
74AUP1G00 v.7	20190423	Product data sheet	-	74AUP1G00 v.6					
Modifications:	of Nexperia • Legal texts								
74AUP1G00 v.6	20120627	Product data sheet	-	74AUP1G00 v.5					
Modifications:	Added type	Added type number 74AUP1G00GX (SOT1226).							
74AUP1G00 v.5	20120316	Product data sheet	-	74AUP1G00 v.4					
Modifications:	Package o	Package outline drawing of SOT886 (Fig. 11) modified.							
74AUP1G00 v.4	20111115	Product data sheet	-	74AUP1G00 v.3					
Modifications:	Legal page	Legal pages updated.							
74AUP1G00 v.3	20101007	Product data sheet	-	74AUP1G00 v.2					
74AUP1G00 v.2	20060629	Product data sheet	-	74AUP1G00 v.1					
74AUP1G00 v.1	20050711	Product data sheet	-	-					

## 15. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

#### **Definitions**

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### **Disclaimers**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal

injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at <u>http://www.nexperia.com/profile/terms</u>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

74AUP1G00

All information provided in this document is subject to legal disclaimers.

© Nexperia B.V. 2019. All rights reserved

## **Contents**

1. General description	1
2. Features and benefits	1
3. Ordering information	2
4. Marking	2
5. Functional diagram	2
6. Pinning information	3
6.1. Pinning	3
6.2. Pin description	3
7. Functional description	4
8. Limiting values	4
9. Recommended operating conditions	4
10. Static characteristics	5
11. Dynamic characteristics	7
11.1. Waveforms and test circuit	10
12. Package outline	11
13. Abbreviations	17
14. Revision history	17
15. Legal information	18

© Nexperia B.V. 2019. All rights reserved

For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 23 April 2019

74AUP1G00