



5V/150mA Fixed-Voltage Ultra Low LDO

SOT-223

Pin Definition:

- 1. Input
- 2. Ground (tab)
- 3. Output

General Description

The TS4264GCW50 is a monolithic integrated low-drop fixed voltage regulator which can supply loads up to 150mA. It is functional compatible to the TS4264GCW50 but has a reduced quiescent current of typ. 40µA. The TS4264GCW50 is especially designed for all applications which are require very low quiescent currents. This ULDO is designed to supply microprocessor systems under the severe condition of automotive applications and is therefore equipped with additional protection against overload, short-circuit and over temperature. Of course the TS4264GCW50 can be used in all other applications, wherever a stabilized voltage is required.

An input voltage in the range of $5.5V \sim 45V$ is regulated to $V_{OUT} = 5V$ with an accuracy of $\pm 3\%$. An accuracy of $\pm 2\%$ is kept for a load current range up to 50mA. The device operates in the temperature range of $T_J = -40 \sim 150$ °C.

Features

- Fixed Output Voltage 5V
- Output Voltage Tolerance ±3%
- 150mA Current Capability
- Ultra Low Quiescent Current 40uA (Typ.)
- Over Temperature Protection
- Short-Circuit Proof
- Reverse Polarity Proof
- Wide Temperature Polarity Range
- Suitable for use in Automotive Electronics

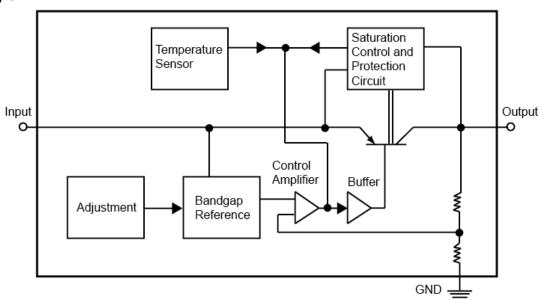
Ordering Information

Part No.	Package	Packing		
TS4264GCW50 RP	SOT-223	2.5Kpcs / 13" Reel		

Pin Definition and Function

Pin	Symbol	Function			
1	Input	Block to ground directly on IC with ceramic capacitor			
2	Ground	Ground			
3	Output	Block to ground with 10uF capacitor, ESR $< 4\Omega$			

Block Diagram



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Absolute Maximum Rating

Parameter	Symbol	Limit \	Values	Unit	Notes
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Input Voltage	V _{IN}	-42	45	V	
Input Voltage (Operating Range)	V _{IN (OPR)}	5.5	45	V	
Input Current	I _{IN}				Internally Limited
Output Voltage	V _{OUT}	-0.3	32	V	
Output Current	I _{OUT}				Internally Limited
Ground Current	I _{GND}	50		mA	
Junction Temperature	TJ		150	°C	
Junction Temperature (Operating Range)	T _{J (OPR)}	-40	150	°C	
Storage Temperature	T _{STG}	-50	150	°C	

Thermal Performance

Parameter	Symbol	Limit \	/alues	Unit	Notes
- arameter	Symbol	Min.	Max.	Oiiit	
Thermal Resistance Junction-Ambient	$R\Theta_{JA}$		80	°C/W	
Thermal Resistance Junction-Pin	$R\Theta_{JP}$		17	°C/W	

Note: Measured to pin 2 (tab)

Electrical Characteristics V_{IN} =13.5V, -40 \leq T_J \leq +150, unless otherwise specified.

Parameter	Symbol	Limit Values			Unit	Notes	
	Min. Typ. Max.		Oilit	Notes			
Output Voltage	V _{OUT}	4.85	5.0	5.15	V	$6V \le V_{IN} \le 21V$, $5mA \le Io \le 100mA$	
Output Voltage	V _{OUT}	4.90	5.0	5.10	V	6V ≤ V _{IN} ≤ 16V, 5mA ≤ Io ≤ 50mA	
Output Current Limit	I _{OUT}	150	200	500	mA		
Current Consumption	IQ		40	60	uA	lo= 100uA, T _J ≤ 85°C	
			40	70		lo= 100uA	
			1.7	4	mA	Io= 50mA	
Dropout Voltage (Note)	V_{DROP}		0.22	0.5	V	lo= 100mA	
Load Regulation	REG _{LOAD}		50	90	mV	5mA ≤ Io ≤ 100mA, V _{IN} = 13.5V	
Line Regulation	REG _{LINE}		5	30	mV	6V ≤ V _{IN} ≤ 28V, Io=1mA	
Ripple Rejection	PSRR		68		dB	f = 100Hz, V _R = 0.5V _{PP}	
Output Capacitor	C _{OUT}	10			uF	ESR ≤ 4Ω @ 10kHz	

Note: Dropout voltage = $V_{IN} - V_{OUT}$

(Measured where V_{OUT} has dropped 100mV from the nominal value obtained at V_{IN} = 13.5V)





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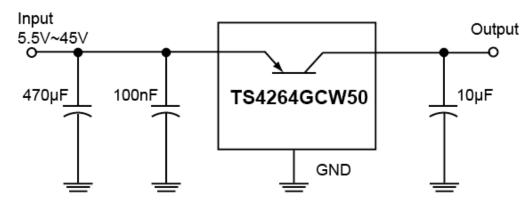
Application Information

In the TS4264GCW50 the output voltage is divided and compared to an internal reference of 2.5V typical. The regulation loop controls the output to achieve an output voltage of 5V with an accuracy of ±3% at an input voltage range of 5.5V~45V.

Circuit Description

The control amplifier compares a reference voltage, which is kept highly precise by resistance adjustment, to a voltage that is proportional to the output voltage and drives the base of the series transistor via a buffer. Saturation control, working as a function of load current, prevents any over-saturation of the power element. The IC is additionally protected against overload, over temperature and reverse polarity

Typical Application Circuit



<u>Dimensioning Information on External Components</u>

The input capacitor C_{IN} is necessary for compensating line influences. Using a resistor of approx. 1Ω in series with C_{IN} , the oscillating of input inductivity and input capacitance can be clamped. The output capacitor C_{OUT} is necessary for the stability of the regulating circuit. Stability is guaranteed at values $C_{\text{OUT}} \ge 10 \text{uF}$ and an ESR $\le 4\Omega$ within the operating temperature range.

The application circuit shows additional electrolytic input capacitor of 470uF is added in order to buffer supply line influences. This capacitor is recommended, if the device is sourced via long supply lines of several meters.

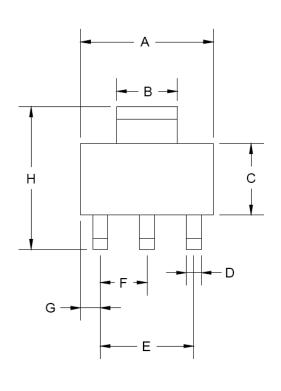
The TS4264GCW50 can supply up to 150mA. However for protection for high input voltage above 25V, the output current is reduced (SOA protection).

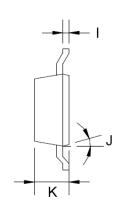




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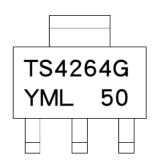
SOT-223 Mechanical Drawing





227 222 711 771 2121						
SOT-223 DIMENSION						
DIM -	MILLIM	ETERS	INCHES			
	MIN	MIN MAX		MAX		
Α	6.350	6.850	0.250	0.270		
В	2.900	3.100	0.114	0.122		
С	3.450	3.750	0.136	0.148		
D	0.595	0.635	0.023	0.025		
Е	4.550	4.650	0.179	0.183		
F	2.250	2.350	0.088	0.093		
G	0.835	1.035	0.032	0.041		
Н	6.700	7.300	0.263	0.287		
	0.250	0.355	0.010	0.014		
J	10°	16°	10°	16°		
K	1.550	1.800	0.061	0.071		

Marking Diagram



Y = Year Code

M = Month Code

(A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep,

J=Oct, K=Nov, L=Dec)

L = Lot Code

50 = 5V Fixed Output Voltage



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