650 V SiC MPS™ Diode

Silicon Carbide Schottky Diode

Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- Superior Figure of Merit Q_C/I_F
- 3000 V Isolation with Low Thermal Resistance
- 175 °C Maximum Operating Temperature
- Temperature Independent Fast Switching
- Positive Temperature Coefficient of V_F
- AEC-Q101 Qualified and PPAP Capable

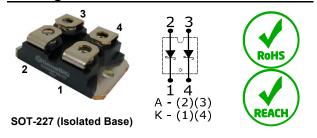
Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current



V _{RRM}	=	650 V
I _{F (Tc = 100°C)}	=	136 A *
Qc	=	160 nC *

Package



Applications

- Boost Diode in Power Factor Correction (PFC)
- Switched Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Motor Drives
- Freewheeling / Anti-parallel Diode in Inverters
- Solar Inverters
- Electric Vehicles (EV) & DC Fast Charging
- Induction Heating & Welding

Absolute Maximum Ratings (At T_c = 25 °C Unless Otherwise Stated)

Parameter Sym		Conditions	Values	Unit	
Repetitive Peak Reverse Voltage (Per Leg)	V _{RRM}		650	V	
Continuous Forward Current (Per Leg / Per Device)		T _C = 25 °C, D = 1	104 / 208	А	
	I _F	T _C = 100 °C, D = 1	68 / 136		
		T _C = 128 °C, D = 1	50 / 100		
Non-Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	I _{F,SM}	T _C = 25 °C, t _P = 10 ms	400	^	
		T _C = 150 °C, t _P = 10 ms	320	A	
Repetitive Peak Forward Surge Current, Half Sine Wave (Per Leg)	I _{F,RM}	T _C = 25 °C, t _P = 10 ms	200	•	
		T _C = 150 °C, t _P = 10 ms	134	A	
Non-Repetitive Peak Forward Surge Current (Per Leg)	I _{F,max}	T _C = 25 °C, t _P = 10 μs	2000	А	
i ² t Value (Per Leg)	∫i² dt	T _C = 25 °C, t _P = 10 ms	800	A ² s	
Non-Repetitive Avalanche Energy (Per Leg)	E _{AS}	L = 0.6 mH, I _{AS} = 50 A	725	mJ	
Diode Ruggedness (Per Leg)	dV/dt	V _R = 0 ~ 520 V	100	V/ns	
Power Dissipation (Per Leg / Per Device)	P _{tot}	T _C = 25 °C	263 / 526	W	
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	

* Per Device

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Electrical Characteristics (Per Leg)

Parameter	Cumhal	Conditions		Values			11
	Symbol			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V	I _F = 50 A, T _j = 25 °C			1.5	1.8	V
	V _F	I _F = 50 A, T _j = 175 °C			1.8	2.2	
Reverse Current	I	V _R = 650 V, T _j = 25 °C			1	10	
	I _R	V _R = 650 V, T _j = 175 °C			15	150	μA
Total Capacitive Charge	0	V _R = 200			57		~ C
	Q _C	I _F ≤ I _{F,MAX} dI _F /dt = 200 A/µs	V _R = 400 V		80		nC
Switching Time	+	T _j = 175 °C	V _R = 200 V		< 10		20
	t _s	V _R = 40			< 10		ns
Total Capacitance	С	V _R = 1 V, f = 1 MHz			2585		ъĘ
	C	V _R = 400 V, f = 1 MHz			230		pF

Thermal / Package Characteristics

Deveryor	Cumb al	Oanditiana	Values			11	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Thermal Resistance, Junction – Case (Per Leg)	R _{thJC}			0.57		°C/W	
Weight	W _T			28		g	
Mounting Torque	Тм	Screws to Heatsink			1.5	Nime	
		Terminal Connection (M4)			1.3	Nm	
Isolation Voltage (RMS)	V _{ISO}	t = 1 s (50 / 60 Hz)	3000				
		t = 60 s (50 / 60 Hz)	2500			V	
Creepage Distance on Surface	d _{Ctt}	Terminal to Terminal	10.5				
	d _{Ctb}	Terminal to Backside	8.5			mm	
Striking Distance Through Air	d _{Stt}	Terminal to Terminal	3.2				
	d _{Stb}	Terminal to Backside	6.8			mm	



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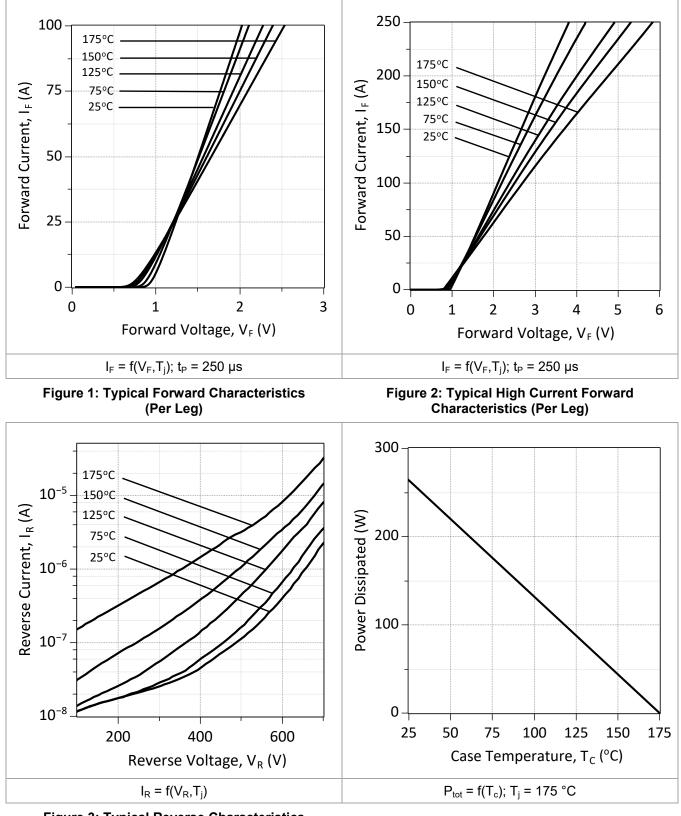
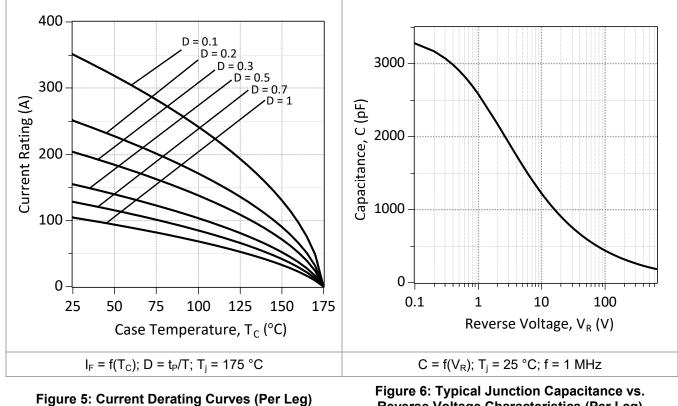


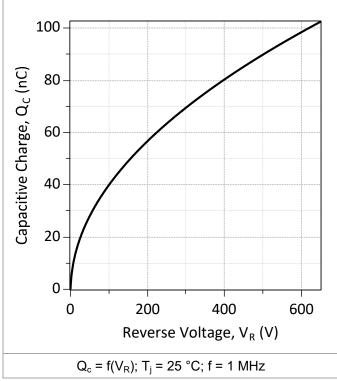


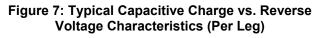
Figure 4: Power Derating Curve (Per Leg)

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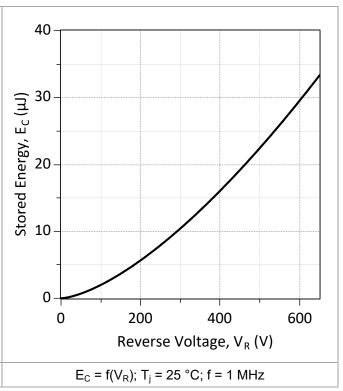








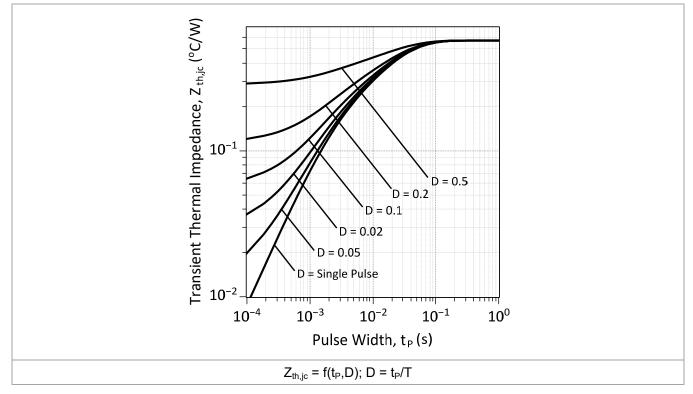
Reverse Voltage Characteristics (Per Leg)



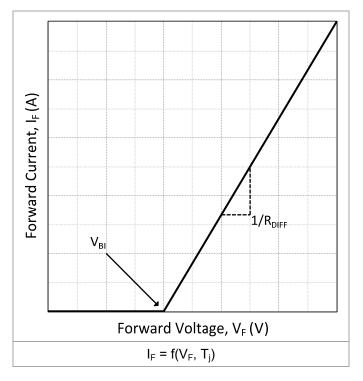


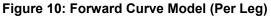












 $I_{F} = (V_{F} - V_{BI})/R_{DIFF} (A)$

Built-In Voltage (V_{BI}):

$$V_{BI}(T_j) = m^*T_j + n (V),$$

m = -1.26e-03, n = 1.01

Differential Resistance (R_{DIFF}):

$$R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c(\Omega);$$

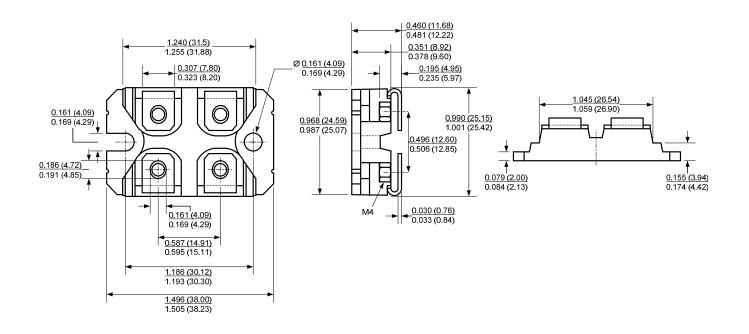
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Package Dimensions

SOT-227

Package Outline



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



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RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS 2), as adopted by EU member states on January 2, 2013 and amended on March 31, 2015 by EU Directive 2015/863. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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