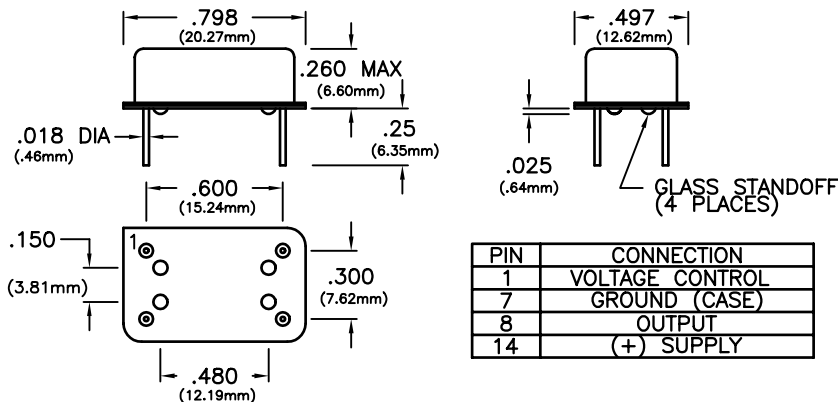




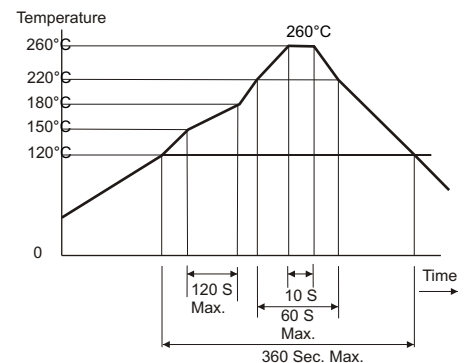
14 PIN DIP ACMOS VCO

SPECIFICATIONS		AV53R1AG		AV53R2G			
Center Frequency (Fo)		12MHz to 153.846MHz		13.8MHz to 139.130MHz			
Frequency Stability		±1%					
Temperature Range		0°C to +70°C					
Supply Voltage		+5Vdc ±5%					
Supply Current		50mA Maximum, 20mA Typical					
Waveform		CMOS Squarewave, TTL Compatible					
Load		50pF					
Output	Voltage	4.5V Minimum					
	Voh	4.5V Minimum					
	Vol	0.5V Maximum					
	Current	-24mA					
	loh	-24mA					
	lol	24mA					
Duty Cycle		50/50 ±20% Max, ±10% Typ measured at 50% level					
Rise/Fall Time		3nS Maximum					
SSB Phase Noise		@1KHz		-80dBc/Hz			
(Typical)		@10KHz		-100dBc/Hz			
		@25KHz		-105dBc/Hz			
		@100KHz		-115dBc/Hz			
Frequency Control							
Control Voltage		0.0 to 5.0V Positive Transfer Characteristics					
Center Frequency		2V to 3V					
Input Impedance		>50K Ohms at 10KHz					
Modulation Bandwidth		<15KHz					
Deviation @ 25°C		AV53R1AG		AV53R2G			
(Referenced to Fo)		Min	Typ	Max	Min	Typ	Max
0V Pin 1		-2.0%	-3.0%	-4.0%	-10%	-12%	-15%
5V Pin 1		+2.0%	+3.0%	+4.0%	+10%	+12%	+15%

Package Outline and Pin Connections



Solder Profile



RoHS Compliant / Lead Free

Specifications subject to change without notice