

# C4D10120D

## Silicon Carbide Schottky Diode

### Z-REC<sup>®</sup> RECTIFIER

$V_{RRM}$	=	1200 V
$I_F (T_c=135^\circ\text{C})$	=	18 A**
$Q_c$	=	54 nC**

### Features

- 1.2kV Schottky Rectifier
- Zero Reverse Recovery Current
- High-Frequency Operation
- Temperature-Independent Switching
- Extremely Fast Switching

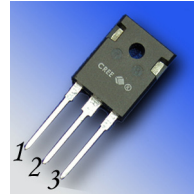
### Benefits

- Replace Bipolar with Unipolar Rectifiers
- Essentially No Switching Losses
- Higher Efficiency
- Reduction of Heat Sink Requirements
- Parallel Devices Without Thermal Runaway

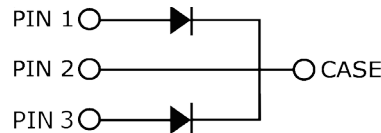
### Applications

- Switch Mode Power Supplies
- Power Factor Correction
- Motor Drives

### Package



TO-247-3



Part Number	Package	Marking
C4D10120D	TO-247-3	C4D10120

### Maximum Ratings (T<sub>c</sub>=25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{RRM}$	Repetitive Peak Reverse Voltage	1200	V		
$V_{RSM}$	Surge Peak Reverse Voltage	1300	V		
$V_R$	DC Peak Reverse Voltage	1200	V		
$I_F$	Continuous Forward Current (Per Leg/Device)	19/38 9/18 5/10	A	T <sub>c</sub> =25°C T <sub>c</sub> =135°C T <sub>c</sub> =160°C	
$I_{FRM}$	Repetitive Peak Forward Surge Current	26* 18*	A	T <sub>c</sub> =25°C, t <sub>p</sub> =10 ms, Half Sine Pulse T <sub>c</sub> =110°C, t <sub>p</sub> =10 ms, Half Sine Pulse	
$I_{FSM}$	Non-Repetitive Forward Surge Current	46* 36*	A	T <sub>c</sub> =25°C, t <sub>p</sub> =10 ms, Half Sine Pulse T <sub>c</sub> =110°C, t <sub>p</sub> =10 ms, Half Sine Pulse	
$I_{F,Max}$	Non-Repetitive Peak Forward Current	400* 320*	A	T <sub>c</sub> =25°C, t <sub>p</sub> =10 μs, Pulse T <sub>c</sub> =110°C, t <sub>p</sub> =10 μs, Pulse	
$P_{tot}$	Power Dissipation(Per Leg/Device)	93/187 40/81	W	T <sub>c</sub> =25°C T <sub>c</sub> =110°C	
$T_J$	Operating Junction Range	-55 to +175	°C		
$T_{stg}$	Storage Temperature Range	-55 to +135	°C		
	TO-247 Mounting Torque	1 8.8	Nm lbf-in	M3 Screw 6-32 Screw	

\* Per Leg, \*\* Per Device

### Electrical Characteristics (Per Leg)

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_F$	Forward Voltage	1.4 1.9	1.8 3	V	$I_F = 5\text{ A}$ $T_J = 25^\circ\text{C}$ $I_F = 5\text{ A}$ $T_J = 175^\circ\text{C}$	
$I_R$	Reverse Current	20 40	150 300	$\mu\text{A}$	$V_R = 1200\text{ V}$ $T_J = 25^\circ\text{C}$ $V_R = 1200\text{ V}$ $T_J = 175^\circ\text{C}$	
$Q_C$	Total Capacitive Charge	27		nC	$V_R = 800\text{ V}$ , $I_F = 5\text{ A}$ $di/dt = 200\text{ A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$	
C	Total Capacitance	390 27 20		pF	$V_R = 0\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ $V_R = 400\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ $V_R = 800\text{ V}$ , $T_J = 25^\circ\text{C}$ , $f = 1\text{ MHz}$	

Note: This is a majority carrier diode, so there is no reverse recovery charge.

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	1.6* 0.8**		$^\circ\text{C}/\text{W}$		

\* Per Leg, \*\* Per Device

### Typical Performance (Per Leg)

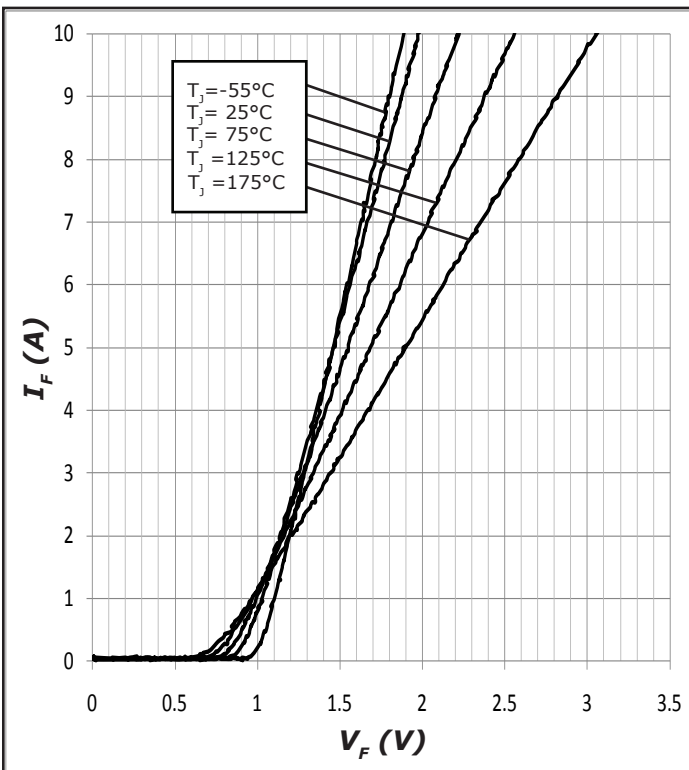


Figure 1. Forward Characteristics

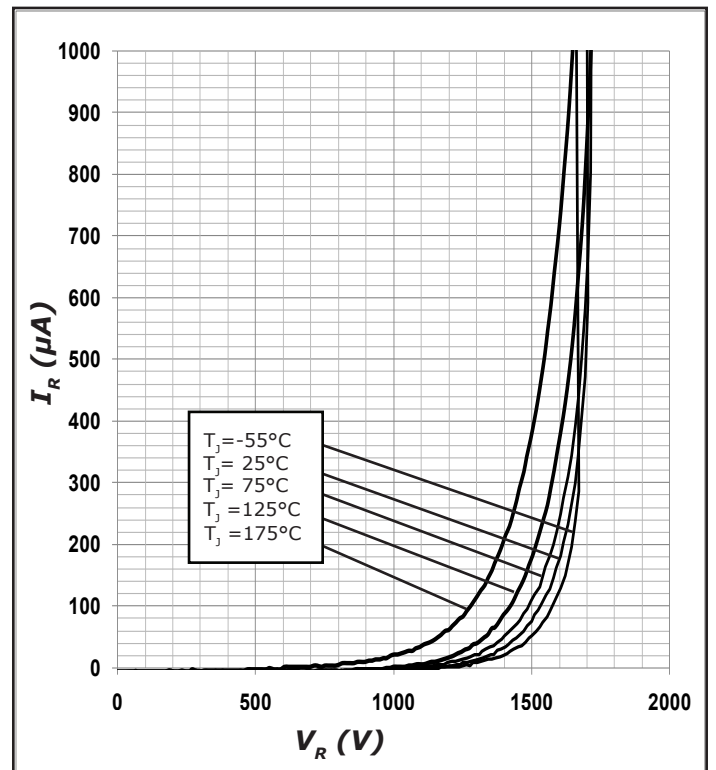


Figure 2. Reverse Characteristics

### Typical Performance (Per Leg)

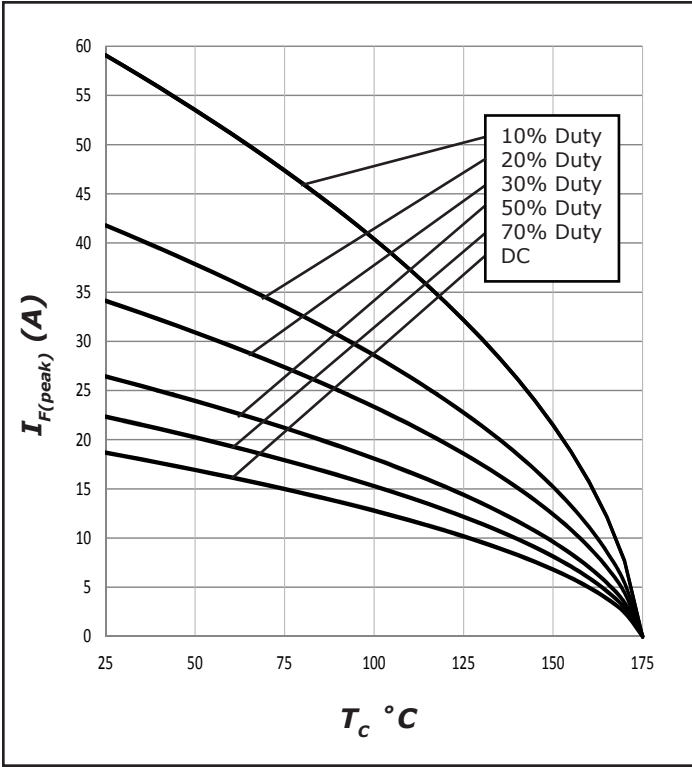


Figure 3. Current Derating

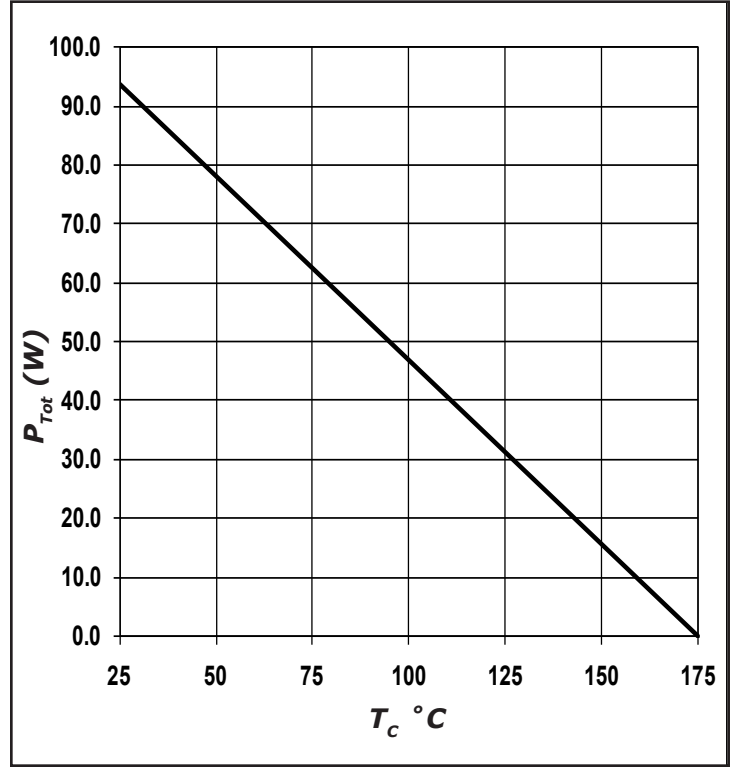


Figure 4. Power Derating

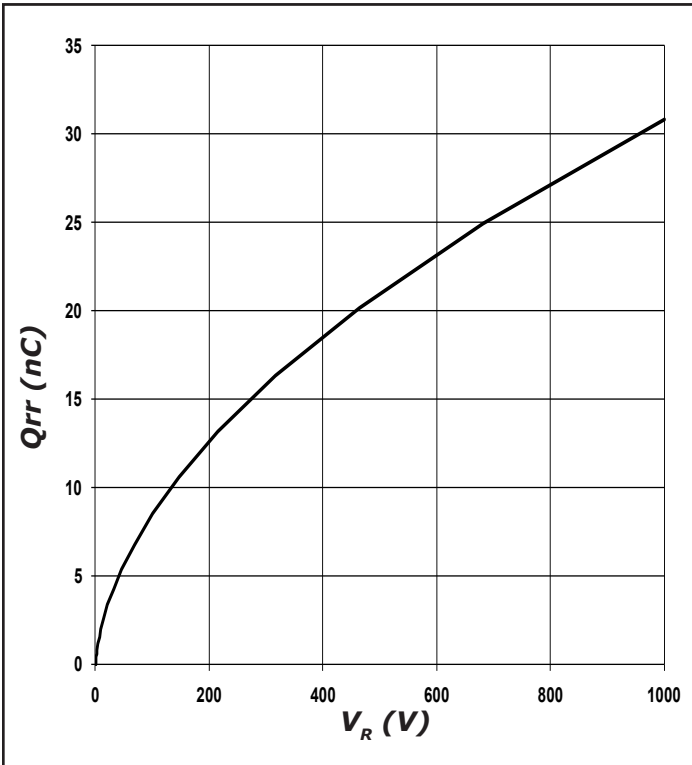


Figure 5. Recovery Charge vs. Reverse Voltage

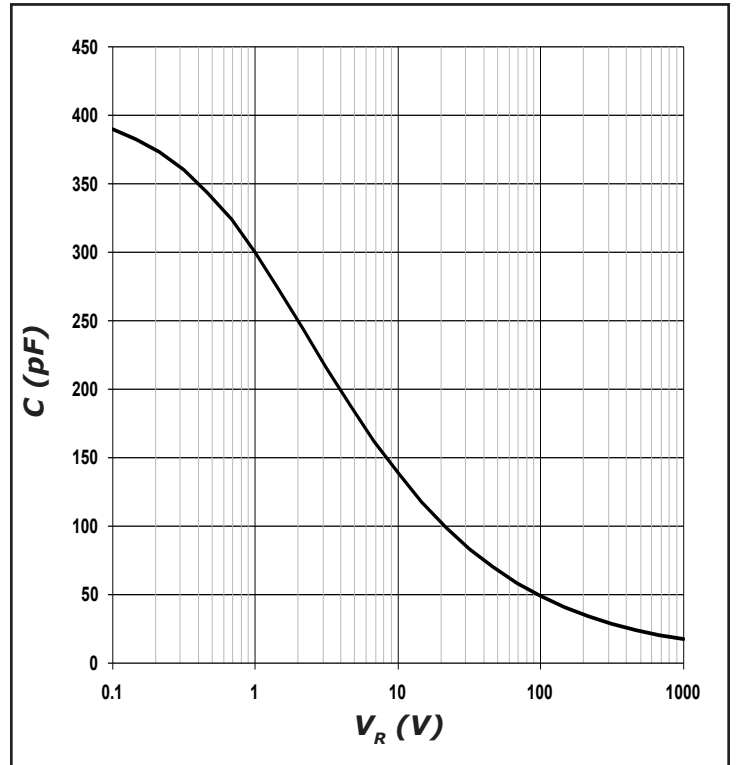


Figure 6. Capacitance vs. Reverse Voltage

Typical Performance

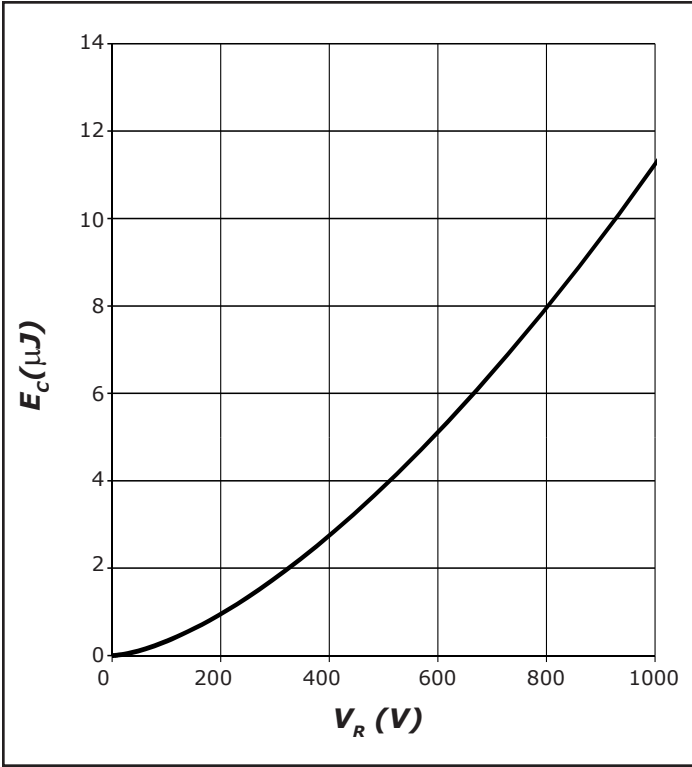


Figure 7. Typical Capacitance Stored Energy, per leg

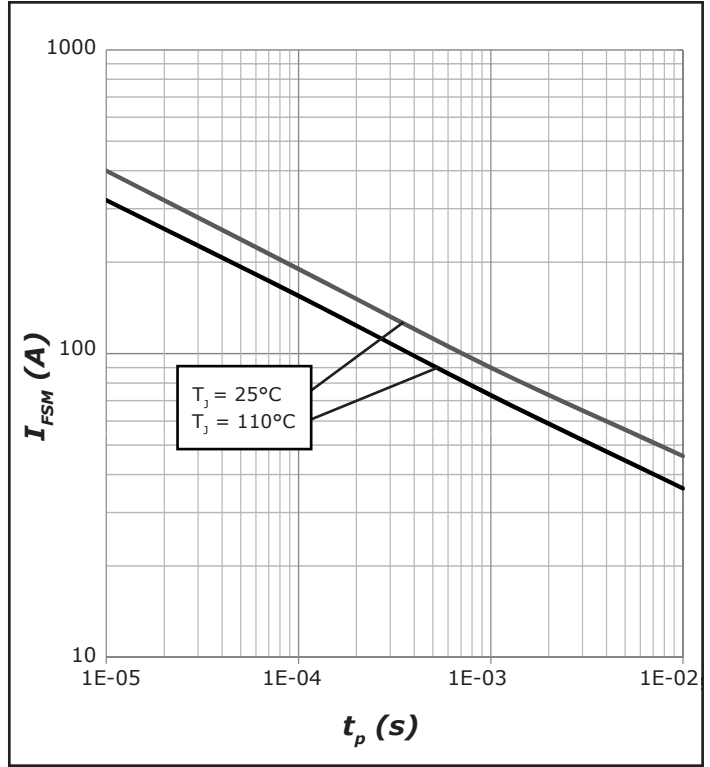


Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform), per leg

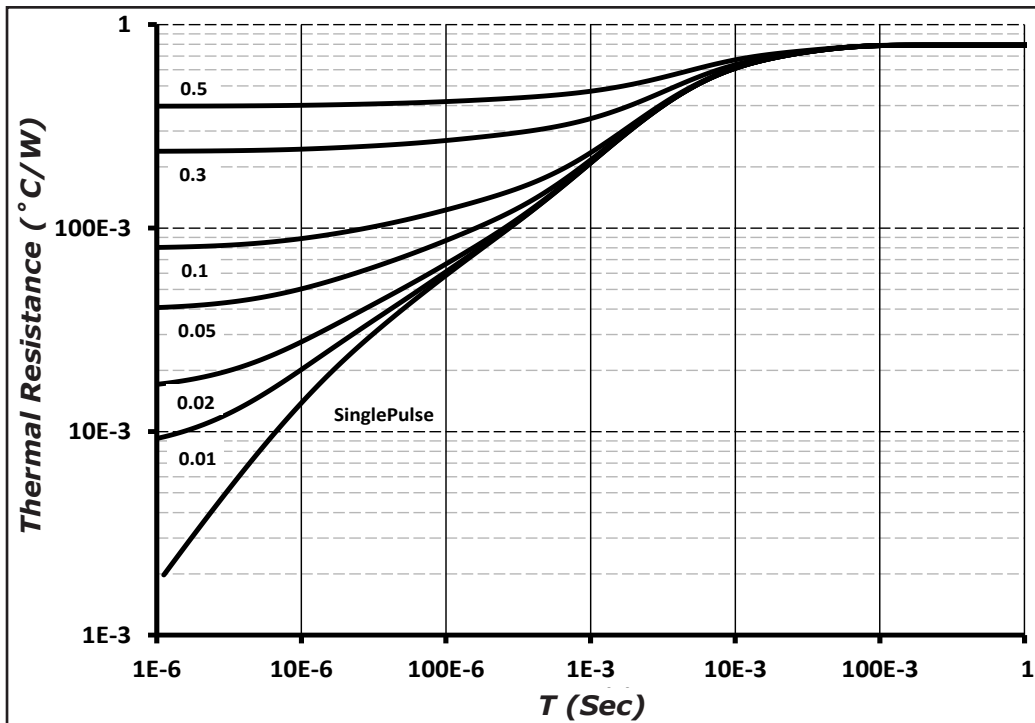
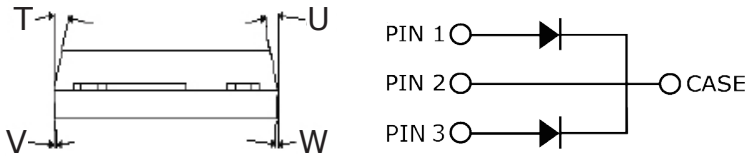
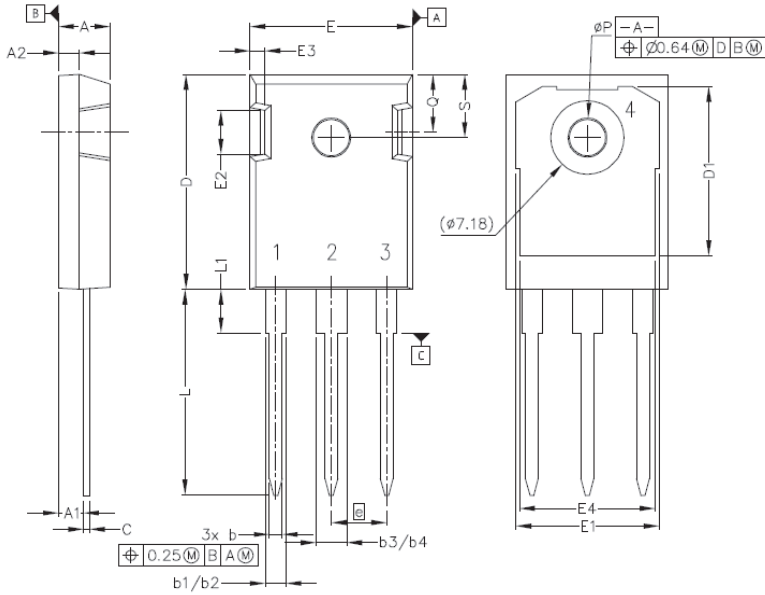


Figure 9. Device Transient Thermal Impedance

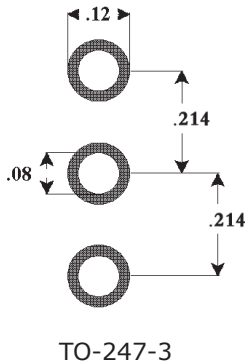
## Package Dimensions

### Package TO-247-3



POS	Inches		Millimeters	
	Min	Max	Min	Max
A	.190	.205	4.83	5.21
A1	.090	.100	2.29	2.54
A2	.075	.085	1.91	2.16
b	.042	.052	1.07	1.33
b1	.075	.095	1.91	2.41
b2	.075	.085	1.91	2.16
b3	.113	.133	2.87	3.38
b4	.113	.123	2.87	3.13
c	.022	.027	0.55	0.68
D	.819	.831	20.80	21.10
D1	.640	.695	16.25	17.65
D2	.037	.049	0.95	1.25
E	.620	.635	15.75	16.13
E1	.516	.557	13.10	14.15
E2	.145	.201	3.68	5.10
E3	.039	.075	1.00	1.90
E4	.487	.529	12.38	13.43
e	.214 BSC		5.44 BSC	
N	3		3	
L	.780	.800	19.81	20.32
L1	.161	.173	4.10	4.40
ØP	.138	.144	3.51	3.65
Q	.216	.236	5.49	6.00
S	.238	.248	6.04	6.30
T	9°	11°	9°	11°
U	9°	11°	9°	11°
V	2°	8°	2°	8°
W	2°	8°	2°	8°

## Recommended Solder Pad Layout

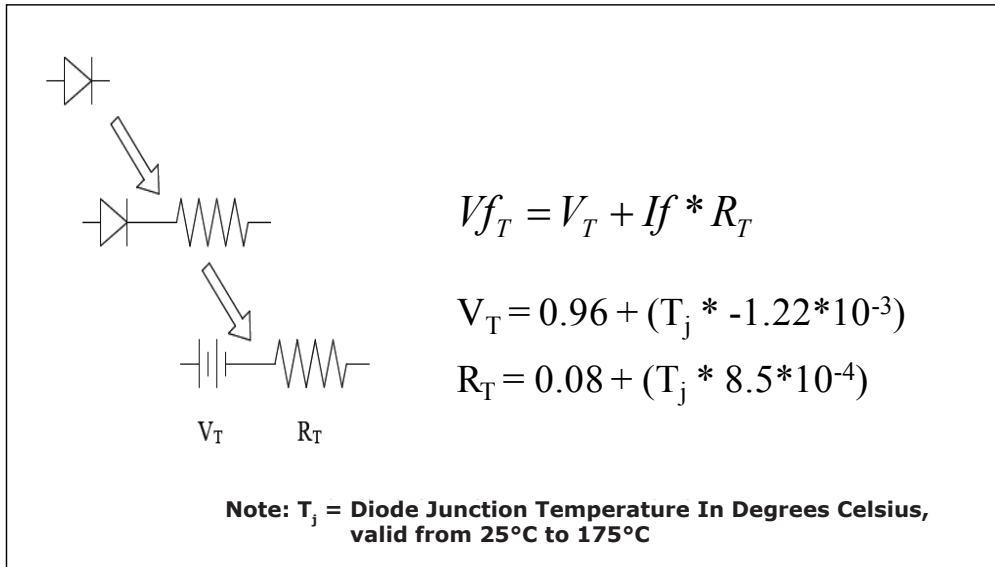


Part Number	Package	Marking
C4D10120D	TO-247-3	C4D10120

Note: Recommended soldering profiles can be found in the applications note here: [http://www.cree.com/power\\_app\\_notes/soldering](http://www.cree.com/power_app_notes/soldering)



## Diode Model



## Notes

- RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Cree representative or from the Product Documentation sections of [www.cree.com](http://www.cree.com).

- REACH Compliance**

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

- This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.