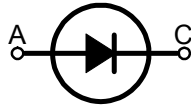
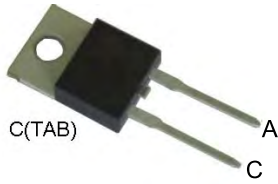


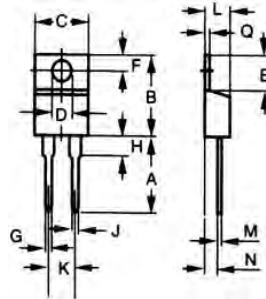
MBR5150 thru MBR5200

High T_{jm} Low IRRM Schottky Barrier Diodes



A=Anode, C=Cathode, TAB=Cathode

Dimensions TO-220AC



Dim.	Inches		Millimeter	
	Min.	Max.	Min.	Max.
A	0.500	0.580	12.70	14.73
B	0.560	0.650	14.23	16.51
C	0.380	0.420	9.66	10.66
D	0.139	0.161	3.54	4.08
E	2.300	0.420	5.85	6.85
F	0.100	0.135	2.54	3.42
G	0.045	0.070	1.15	1.77
H	-	0.250	-	6.35
J	0.025	0.035	0.64	0.89
K	0.190	0.210	4.83	5.33
L	0.140	0.190	3.56	4.82
M	0.015	0.022	0.38	0.56
N	0.080	0.115	2.04	2.49
Q	0.025	0.055	0.64	1.39

	V _{RRM}	V _{RMS}	V _{DC}
	V	V	V
MBR5150	150	105	150
MBR5200	200	140	200

Symbol	Characteristics	Maximum Ratings	Unit
I _{AV}	Maximum Average Forward Rectified Current @T _c =120°C	5	A
I _{FSM}	Peak Forward Surge Current 8.3ms Single Half-Sine-Wave Superimposed On Rated Load (JEDEC METHOD)	120	A
dv/dt	Voltage Rate Of Change (Rated V _R)	10000	V/us
V _F	Maximum Forward Voltage (Note 1) I _F =5A @T _J =25°C I _F =5A @T _J =125°C I _F =10A @T _J =25°C I _F =10A @T _J =125°C	0.95 - 0.95 0.80	V
I _R	Maximum DC Reverse Current At Rated DC Blocking Voltage @T _J =25°C @T _J =125°C	0.05 15	mA
R _{θJC}	Typical Thermal Resistance (Note 2)	4.5	°C/W
T _J	Operating Temperature Range	-55 to +150	°C
T _{stg}	Storage Temperature Range	-55 to +150	°C

NOTES: 1. 300us Pulse Width, Duty Cycle 2%.
2. Thermal Resistance Junction To Case.
3. Measured At 1.0MHz And Applied Reverse Voltage Of 4.0V DC.

FEATURES

- * Metal of silicon rectifier, majority carrier conduction
- * Guard ring for transient protection
- * Low power loss, high efficiency
- * High current capability, low V_F
- * High surge capacity
- * For use in low voltage, high frequency inverters, free whelling, and polarity protection applications
- * RoHS compliant

MECHANICAL DATA

- * Case: TO-220AC molded plastic
- * Polarity: As marked on the body
- * Weight: 2 grams
- * Mounting position: Any

Sirectifier®

MBR5150 thru MBR5200

High T_{jm} Low IRRM Schottky Barrier Diodes

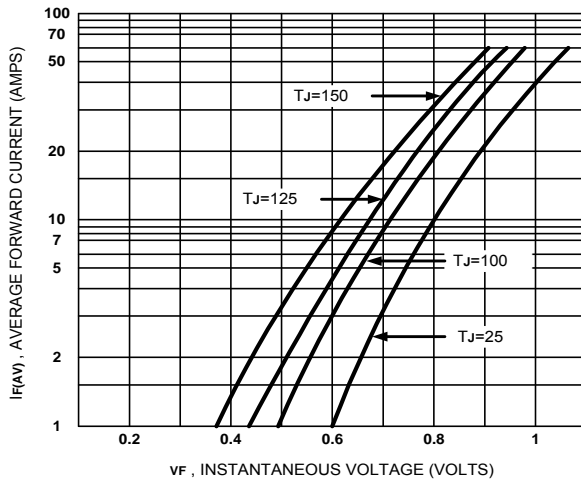


Figure 1. Typical Forward Voltage (Per Leg)

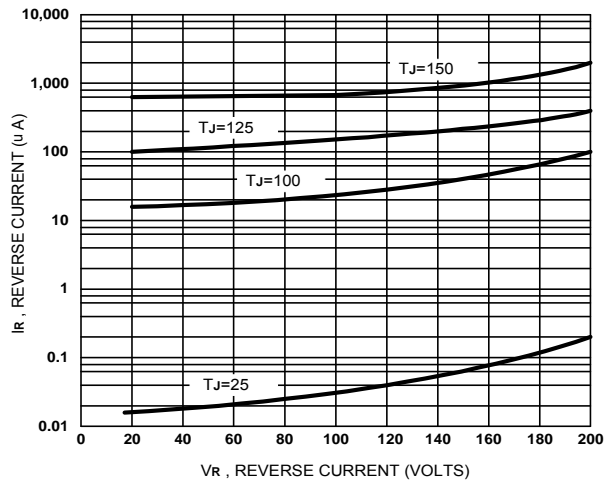


Figure 2. Typical Reverse Current (Per Leg)

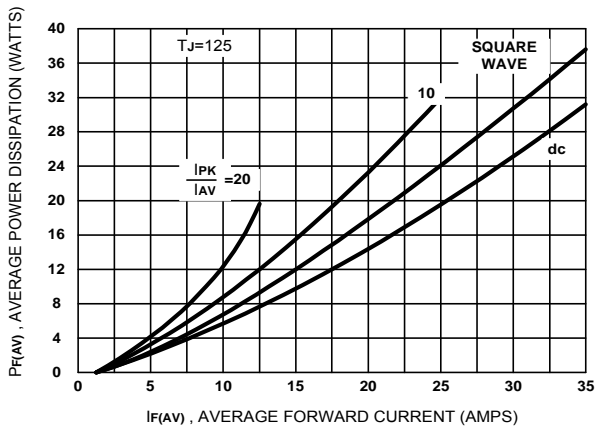


Figure 3. Forward Power Dissipation

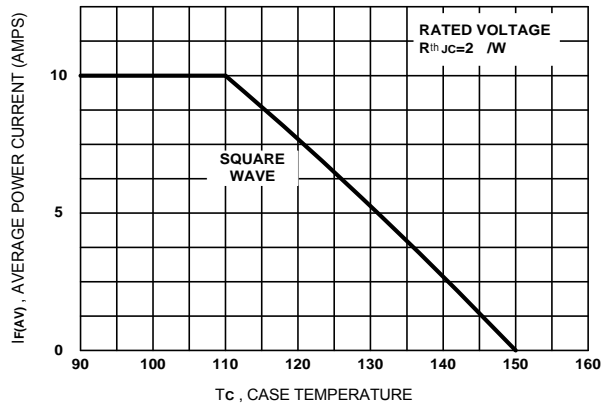


Figure 4. Forward Current Derating, Case

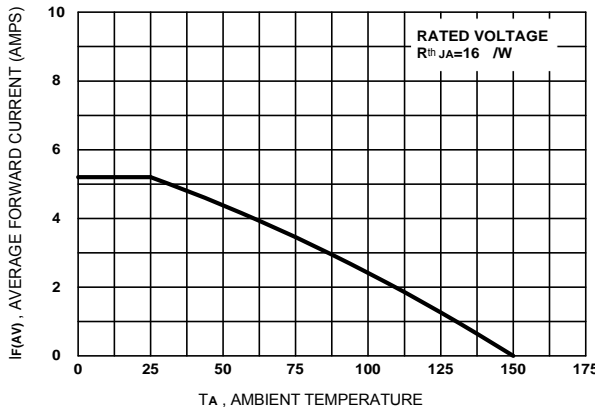


Figure 5. Current Derating, Ambient

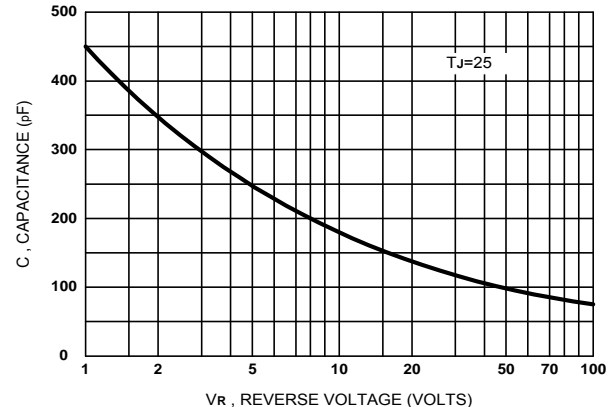


Figure 6. Typical Capacitance (Per Leg)

