# Nch 600V 35A Power MOSFET

$V_{DSS}$	600V
R <sub>DS(on)</sub> (Max.)	0.102Ω
I <sub>D</sub>	±35A
P <sub>D</sub>	102W

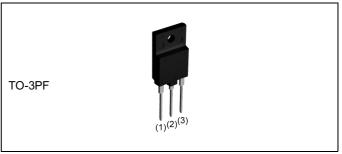
## Features

- 1) Low on-resistance.
- 2) Ultra fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free lead plating; RoHS compliant

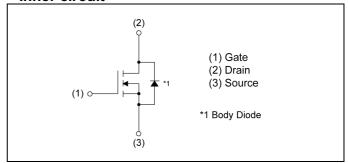
# Application

Switching

# Outline



# •Inner circuit



Packaging specifications

- 1 dokaging opcomoducione						
	Packing	Tube				
	Reel size (mm)	-				
Time	Tape width (mm)	-				
Type	Basic ordering unit (pcs)	360				
	Taping code	C8				
	Marking	R6035KNZ				

# ullet Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25°C)		I <sub>D</sub> *1	±35	Α
Pulsed drain current		I <sub>DP</sub> *2	±105	Α
Cata Sauma valtaga	static	V	±20	V
Gate - Source voltage	AC(f>1Hz)	$V_{GSS}$	±30	V
Avalanche current, single pulse		I <sub>AS</sub>	6.6	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	796	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	102	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage tempera	ature range	T <sub>stg</sub>	-55 to +150	°C

# ●Thermal resistance

Downwortow	Cymah al	Values			l lmit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *4	-	-	1.2	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	40	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# • Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(DD)DCC} = V_{CC} = UV_{DC} = 1$		600	-	1	V
		V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V				
Zero gate voltage drain current	I <sub>DSS</sub>	$T_j = 25^{\circ}C$	-	-	100	μΑ
		$T_j = 125^{\circ}C$	-	-	1000	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	1	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	3	-	5	V
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 18.1A				
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$T_j = 25^{\circ}C$	-	0.092	0.102	Ω
		$T_j = 125^{\circ}C$	-	0.200	-	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	1.0	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymah al	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Transfer Admittance	Y <sub>fs</sub>  *5	$ Y_{fs} ^{*5}$ $V_{DS} = 10V, I_D = 17.5A$		22	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3000	-	,	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 25V	-	2300	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	80	-		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 10V$	-	45	-		
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 17.5A	-	150	-	20	
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 17.4\Omega$	-	90	1	ns	
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	95	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Davamatar	Cymah al	Conditions	Values			11-4
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	72	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 35A	-	20	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	30	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 35A	-	6.6	-	V

<sup>\*1</sup> Limited only by maximum channel temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L $\doteqdot$ 50mH, V<sub>DD</sub>=50V, R<sub>G</sub>=25 $\Omega$ , STARTING T<sub>i</sub>=25°C

<sup>\*4</sup> T<sub>C</sub>=25°C

<sup>\*5</sup> Pulsed

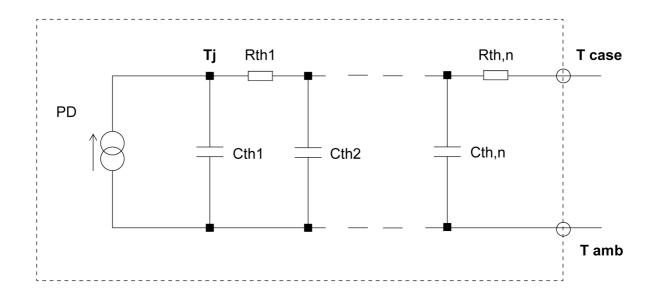
# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Daramatar	Sumb al	Canditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unii
Continuous forward current	I <sub>S</sub> *1	· T <sub>C</sub> = 25°C	1	1	35	Α
Pulse forward current	I <sub>SP</sub> *2	1C - 25 C	-	-	105	Α
Forward voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{S} = 35A$	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *5		-	605	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 35A di/dt = 100A/μs	-	14.5	-	μC
Peak reverse recovery current	I <sub>rrm</sub> *5		-	45	-	А

Typical transient thermal characteristics

Symbol	Value	Unit
R <sub>th1</sub>	0.0683	
R <sub>th2</sub>	0.402	K/W
R <sub>th3</sub>	1.22	

Symbol	Value	Unit
C <sub>th1</sub>	0.00697	
C <sub>th2</sub>	0.0677	Ws/K
C <sub>th3</sub>	1.12	



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Fig.1 Power Dissipation Derating Curve

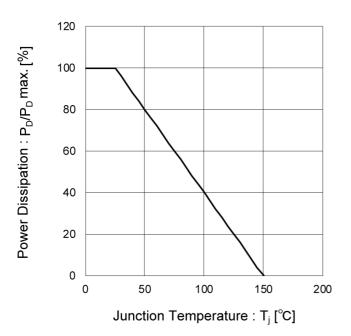


Fig.2 Maximum Safe Operating Area

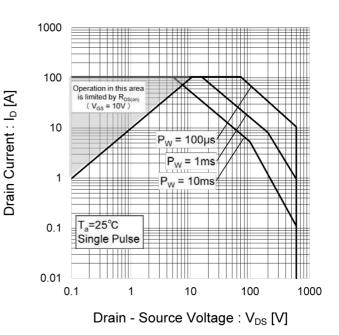


Fig.3 Avalanche Energy Derating
Curve vs. Junction Temperature

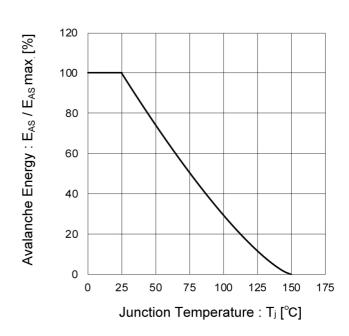


Fig.4 Typical Output Characteristics(I)

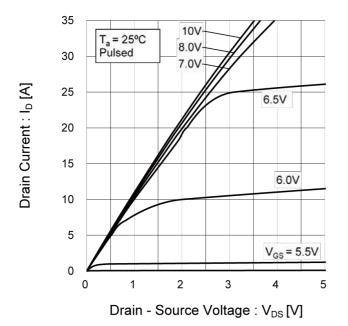
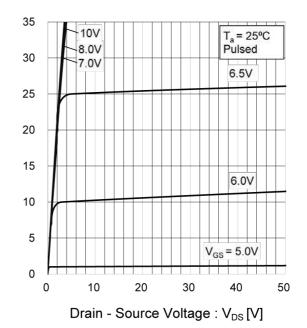


Fig.5 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

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Fig.6 Breakdown Voltage vs.
Junction Temperature

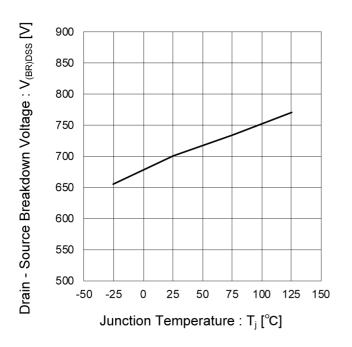


Fig.7 Typical Transfer Characteristics

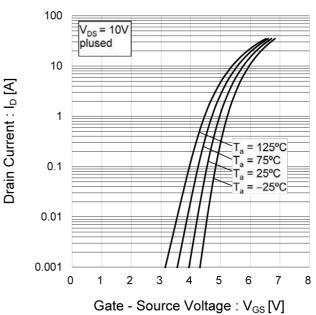


Fig.8 Gate Threshold Voltage vs.
Junction Temperature

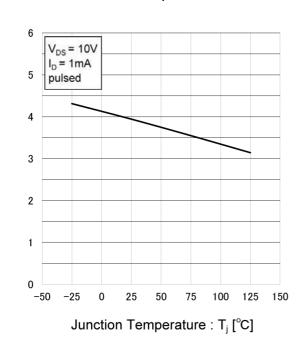
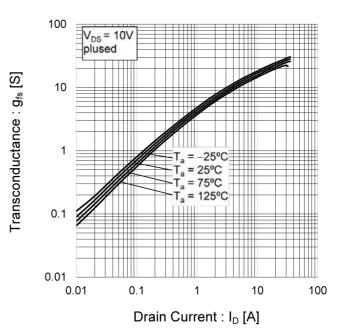


Fig.9 Forward Transfer Admittance vs.

Drain Current



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Fig.10 Static Drain - Source On - State Resistance vs. Gate Source Voltage

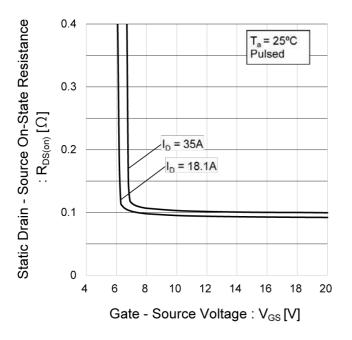


Fig.11 Static Drain - Source On - State Resistance vs. Junction Temperature

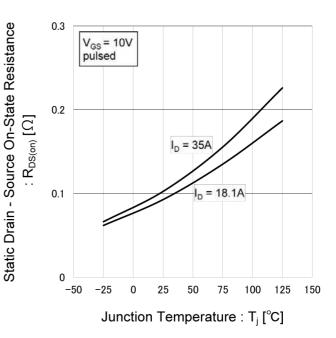


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current(I)

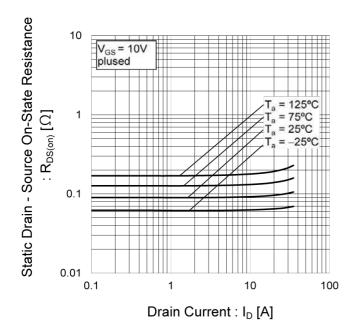
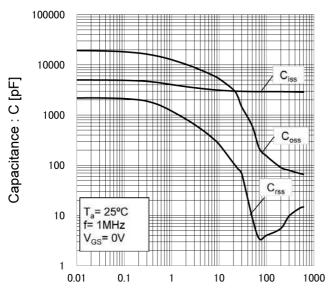


Fig.13 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.14 Switching Characteristics

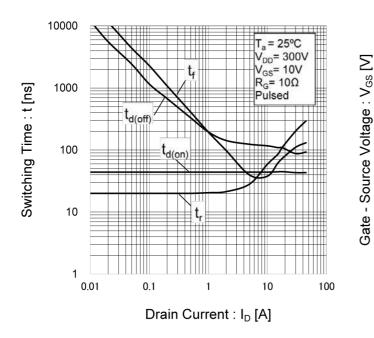


Fig.15 Dynamic Input Characteristics

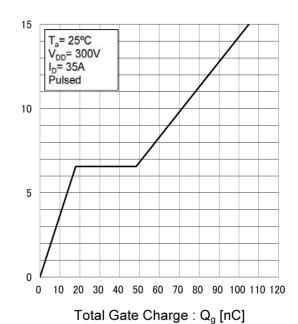


Fig.16 Inverse Diode Forward Current vs. Source - Drain Voltage

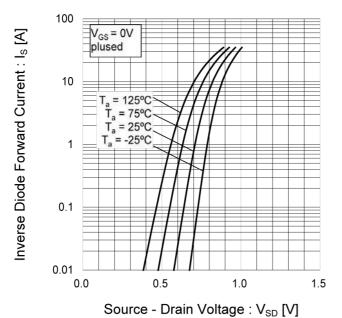
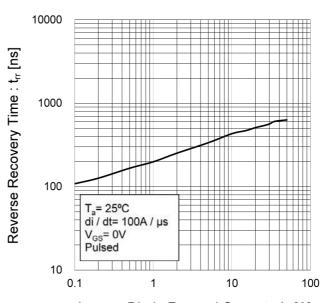


Fig.17 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current: I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

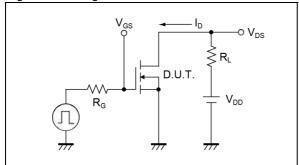


Fig.2-1 Gate Charge Measurement Circuit

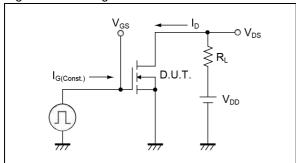


Fig.3-1 Avalanche Measurement Circuit

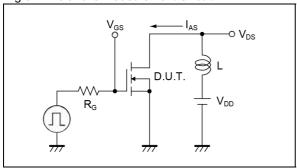


Fig.4-1 dv/dt Measurement Circuit

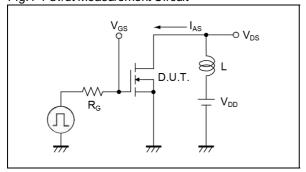


Fig.5-1 dv/dt Measurement Circuit

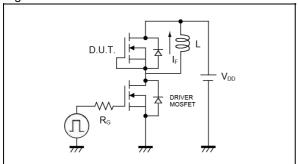


Fig.1-2 Switching Waveforms

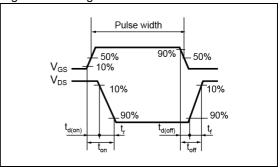


Fig.2-2 Gate Charge Waveform

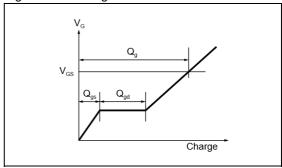


Fig.3-2 Avalanche Waveform

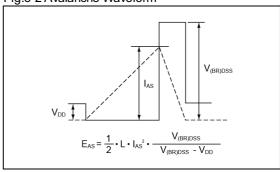


Fig.4-2 dv/dt Waveform

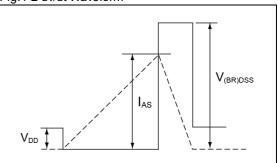
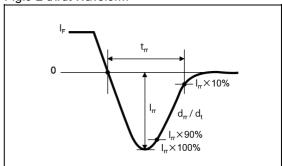
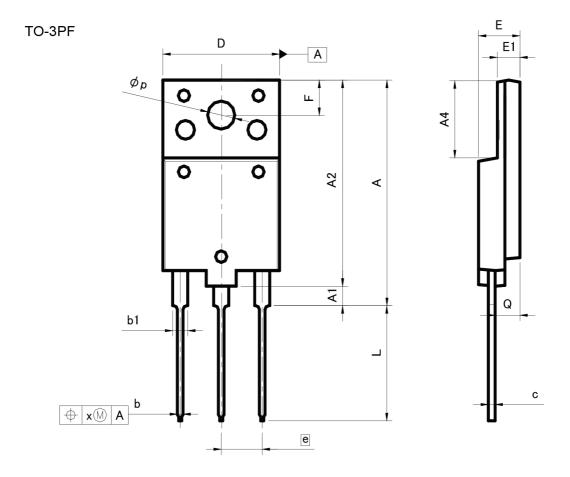


Fig.5-2 dv/dt Waveform



# Dimensions



DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	26.30	26.70	1.035	1.051
A1	2.30	2.70	0.091	0.106
A2	26.30	26.70	1.035	1.051
A4	9.80	10.20	0.386	0.402
b	0.65	0.95	0.026	0.037
b1	1.80	2.20	0.071	0.087
С	0.80	1.10	0.031	0.043
D	15.30	15.70	0.602	0.618
E	5.30	5.70	0.209	0.224
е	5.4	45	0.215	1(-1)
E1	2.80	3.20	0.110	0.126
F	4.30	4.70	0.169	0.185
L	14.60	15.00	0.575	0.591
р	3.40	3.80	0.134	0.150
Q	3.10	3.50	0.122	0.138
х	-	0.50	4.T.)	0.020

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASSIIb	CL A C C TT
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

## **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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