TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74LCX257F,TC74LCX257FT,TC74LCX257FK

Low-Voltage Quad 2-Channel Multiplexer (3-state) with 5-V Tolerant Inputs and Outputs

The TC74LCX257 is a high-performance CMOS multiplexer. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

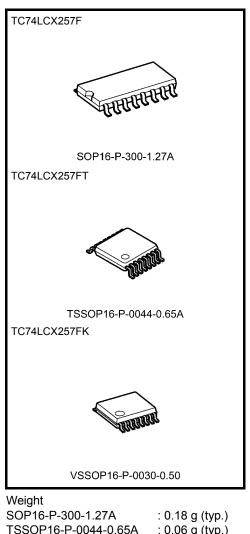
It is composed of four independent 2-channel multiplexers with common select and \overline{OE} .

If \overline{OE} is set low, the outputs are held in a high-impedance state. When SELECT is set low, "A" data inputs are enabled. Conversely, when SELECT is high, "B" data inputs are enabled.

All inputs are equipped with protection circuits against static discharge.

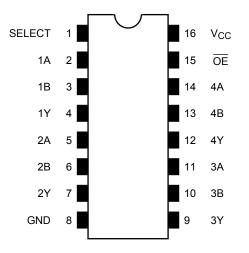
Features

- Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- High-speed operation: $t_{pd} = 6.0$ ns (max) (V_{CC} = 3.0 to 3.6 V) •
- Output current: $|I_{OH}|/I_{OL} = 24 \text{ mA} (\min) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEITA SOP, TSSOP and VSSOP(US)
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 257 type

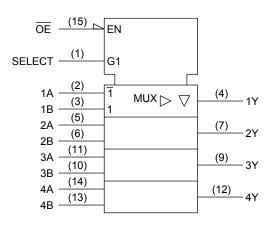


: 0.18 g (typ.)
: 0.06 g (typ.)
: 0.02 g (typ.)

Pin Assignment (top view)



IEC Logic Symbol



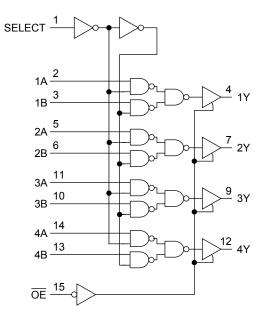
Truth Table

	Outputs			
ŌĒ	SELECT	А	В	Y
Н	Х	Х	Х	Z
L	L	L	Х	L
L	L	Н	Х	Н
L	Н	Х	L	L
L	Н	Х	Н	Н

X: Don't care

Z: High impedance

System Diagram



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 2)	
DC output voltage	V _{OUT}	–0.5 to V _{CC} + 0.5 (Note 3)	V
Input diode current	I _{IK}	-50	mA
Output diode current	I _{OK}	±50 (Note 4)	mA
DC output current	IOUT	±50	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 2: Output in OFF state
- Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND, V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.0 to 3.6	V
Fower supply voltage	VCC	1.5 to 3.6 (Note 2)	v
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	Vout 0 to 5.5 (0 to 5.5 (Note 3)	V
Oulput vollage	VOUT	0 to V _{CC} (Note 4)	v
Output current	Іон/Іог	±24 (Note 5)	mA
Output current	'OH/'OL	±12 (Note 6)	ША
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

- Note 3: Output in OFF state
- Note 4: High or low state
- Note 5: $V_{CC} = 3.0$ to 3.6 V
- Note 6: $V_{CC}=2.7\ to\ 3.0\ V$
- Note 7: $V_{IN} = 0.8$ to 2.0 V, $V_{CC} = 3.0$ V

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Characteris	tics	Symbol	Test Condition		Symbol Test Condition		Test Condition		nbol Test Condition		bol Test Condition		V _{CC} (V)	Min	Max	Unit
	H-level	VIH	_	_	2.7 to 3.6	2.0										
Input voltage	L-level	VIL		_	2.7 to 3.6	_	0.8	V								
				I _{OH} = -100 μA	2.7 to 3.6	V _{CC} -0.2	_									
	H-level	V _{OH}	VIN = VIH or VIL	I _{OH} = -12 mA	2.7	2.2										
				I _{OH} = -18 mA	3.0	2.4										
Output voltage				I _{OH} = -24 mA	3.0	2.2		V								
		V _{OL}		I _{OL} = 100 μA	2.7 to 3.6	_	0.2									
	L laval		V _{OL}	V _{OL} V	., ., .,	I _{OL} = 12 mA	2.7		0.4							
	L-level				VOL	$V_{IN} = V_{IH} \text{ or } V_{IL}$	I _{OL} = 16 mA	3.0	_	0.4						
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55									
Input leakage current		I _{IN}	V _{IN} = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μA								
3-state output OFF st	ate current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5 \text{ V}$		2.7 to 3.6	_	±5.0	μA								
Power-off leakage cu	rrent	IOFF	V _{IN} /V _{OUT} = 5.5 V		0		10.0	μA								
Ouissent sugglu sug			$V_{IN} = V_{CC}$ or GND		2.7 to 3.6		10.0									
Quiescent supply curr	ent	ICC	V _{IN} /V _{OUT} = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μA								
Increase in Icc per input		Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6		500									

AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7	_	6.5	ns
(A, B-Y)	t _{pHL}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	6.0	115
Propagation delay time	t _{pLH}	Figure 1, Figure 2	2.7		8.5	ns
(SELECT-Y)	t _{pHL}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	7.0	115
Output enable time	t _{pZL}	Figure 1, Figure 3	2.7		8.5	ns
	t _{pZH}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	7.0	115
Output disable time	t _{pLZ}	Figure 1, Figure 3	2.7	_	6.0	ns
	t _{pHZ}		$\textbf{3.3}\pm\textbf{0.3}$	1.5	5.5	115
Output to output skew	t _{osLH}	(Note)	2.7	_	_	ns
	t _{osHL}	(NOLE)	$\textbf{3.3}\pm\textbf{0.3}$	_	1.0	115

Note: Parameter guaranteed by design. $(t_{osLH} = |t_{pLHm} - t_{pLHn}|, \ t_{osHL} = |t_{pHLm} - t_{pHLn}|)$

Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5 \text{ ns}$, $C_L = 50 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	V

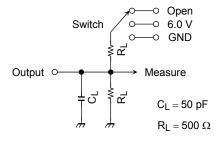
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Input capacitance	CIN	—	3.3	7	pF
Output capacitance	COUT	—	3.3	8	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz (Note	3.3	25	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation: $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

AC Test Circuit



Parameter	Switch
t _{pLH} , t _{pHL}	Open
t _{pLZ} , t _{pZL}	6.0 V
t _{pHZ} , t _{pZH}	GND

Figure 1

AC Waveform

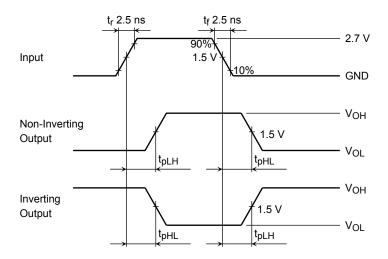


Figure 2 t_{pLH}, t_{pHL}

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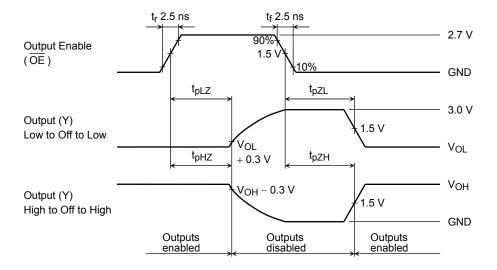


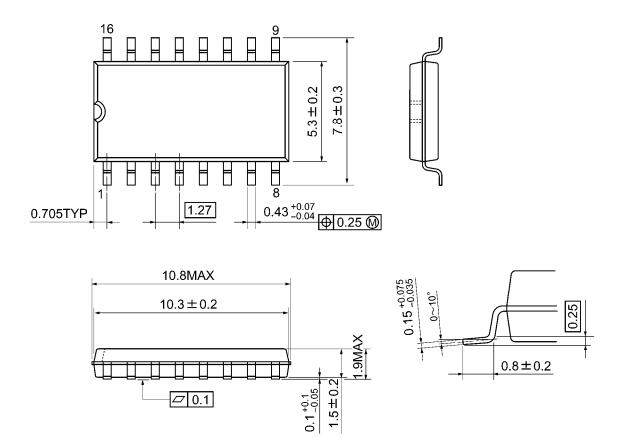
Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$



Package Dimensions

SOP16-P-300-1.27A

Unit: mm

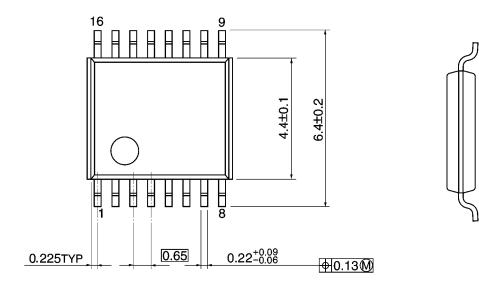


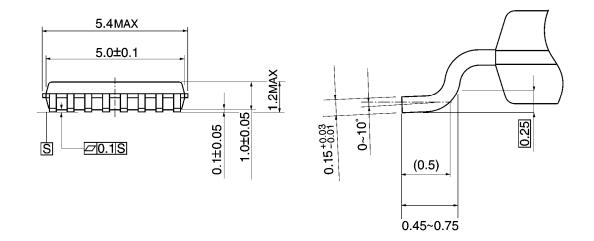
Weight: 0.18 g (typ.)

Package Dimensions

TSSOP16-P-0044-0.65A

Unit: mm





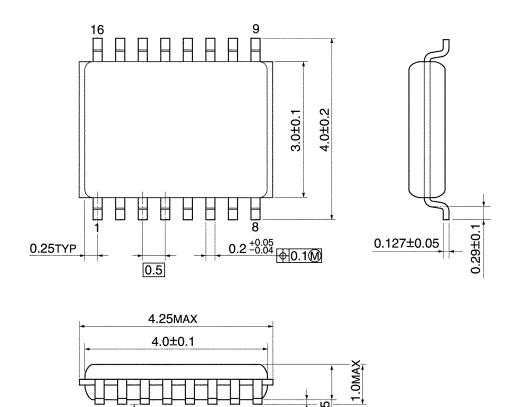
Weight: 0.06 g (typ.)



Package Dimensions

VSSOP16-P-0030-0.50

Unit: mm



Ø.1

0.1±0.05 0.8±0.05



Weight: 0.02 g (typ.)

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