



SFT1345

Power MOSFET

-100V, -11A, 275mΩ, P-Channel Single TP/TP-FA

ON Semiconductor®

<http://onsemi.com>

Features

- ON-resistance $R_{DS(on)} = 210\text{m}\Omega$ (typ.)
- Input Capacitance $C_{iss} = 1020\text{pF}$ (typ.)
- 4V drive
- Halogen free compliance
- Protection diode in

Specifications

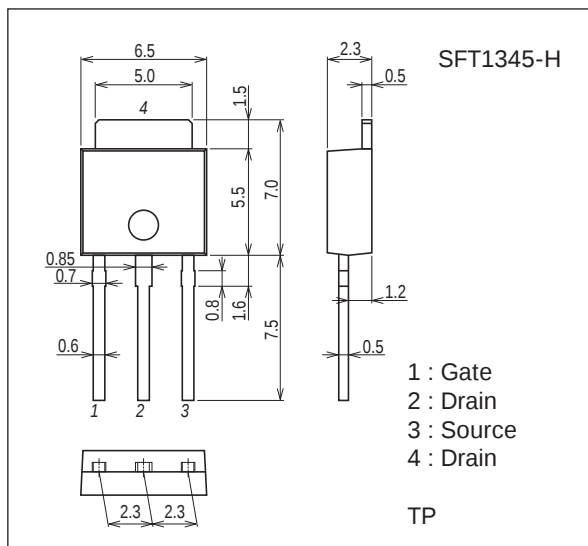
Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DS}		-100	V
Gate-to-Source Voltage	V_{GS}		± 20	V
Drain Current (DC)	I_D		-11	A
Drain Current ($PW \leq 10\mu\text{s}$)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	-44	A
Allowable Power Dissipation	P_D		1.0	W
		$T_c = 25^\circ\text{C}$	35	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

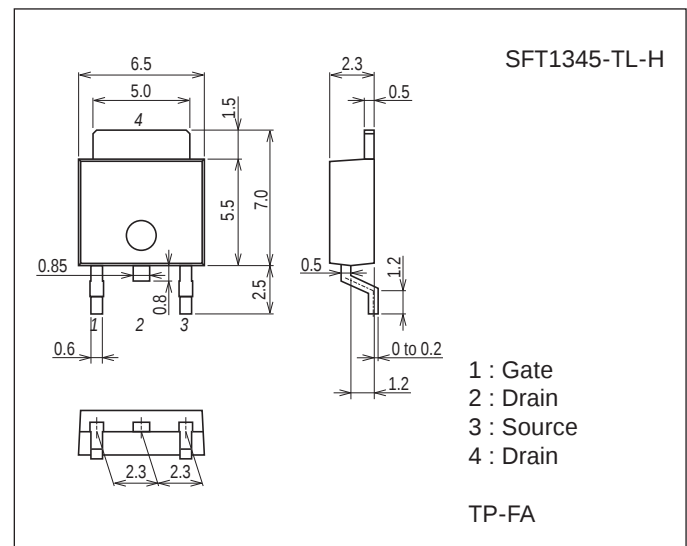
Package Dimensions unit : mm (typ)

7518-004



Package Dimensions unit : mm (typ)

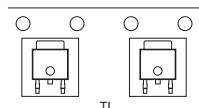
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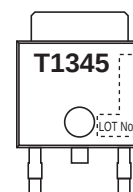
Ordering & Package Information

Device	Package	Shipping	memo
SFT1345-H	TP (SC-64, TO-251)	500pcs./bag	Pb Free and Halogen Free
SFT1345-TL-H	TP-FA (SC-63, TO-252)	700pcs./reel	

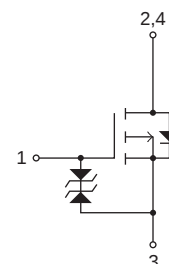
Packing Type (TP-FA) : TL



Marking

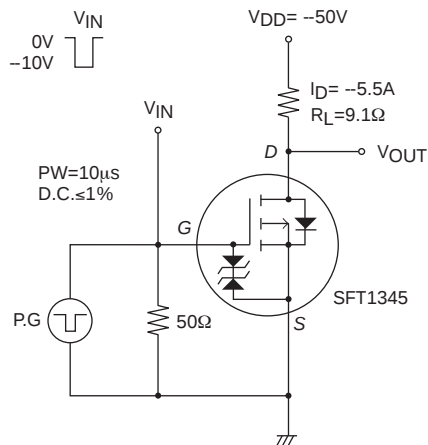


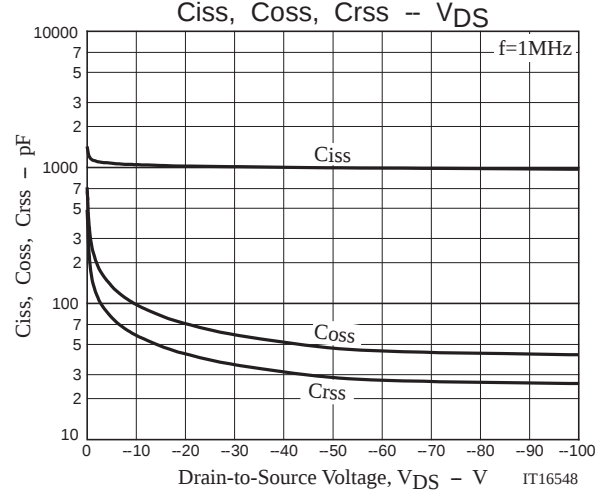
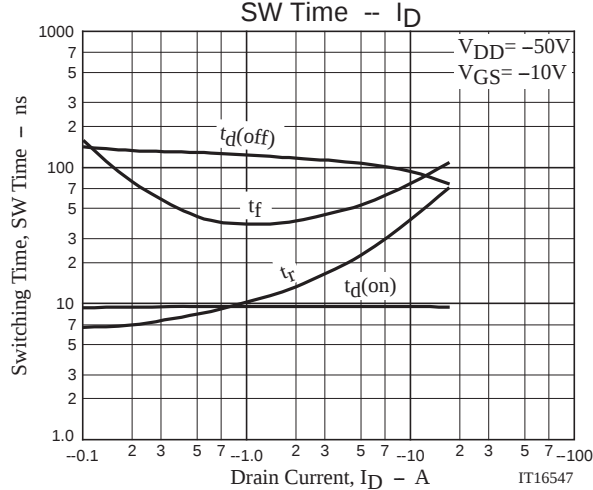
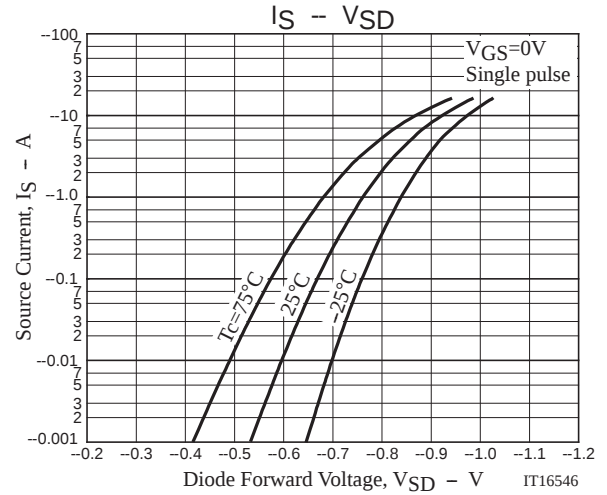
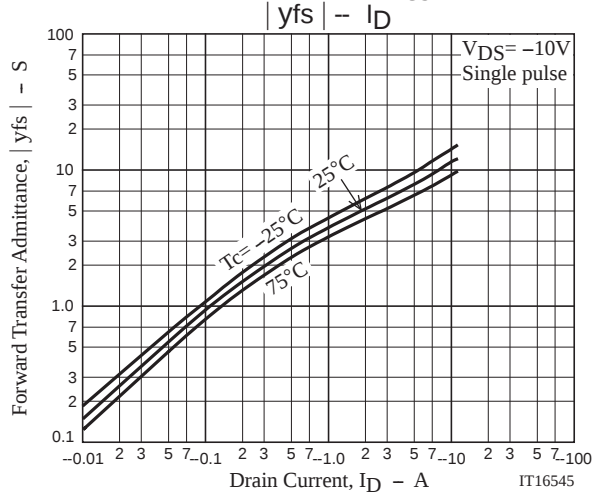
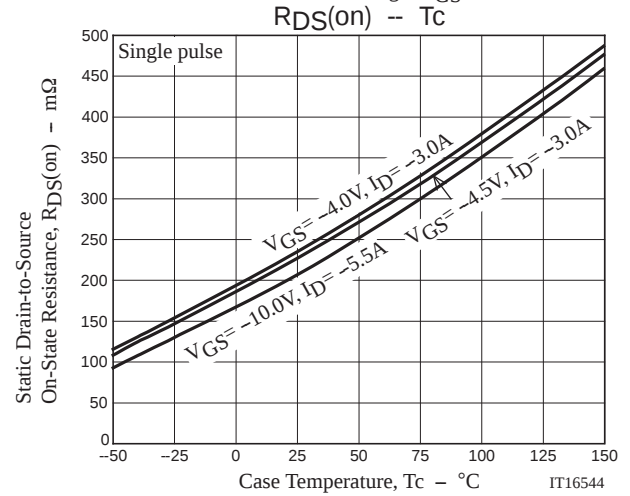
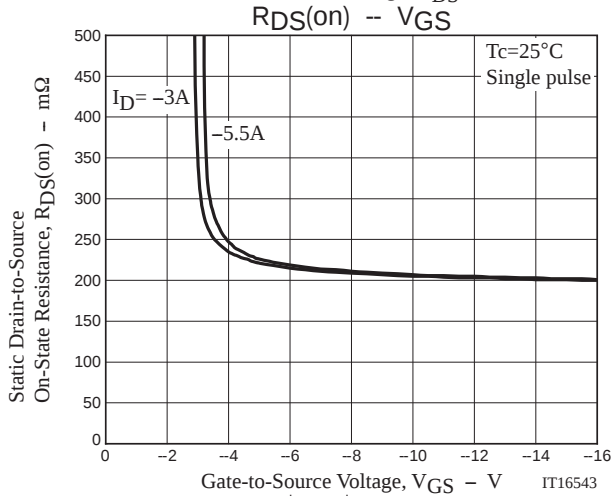
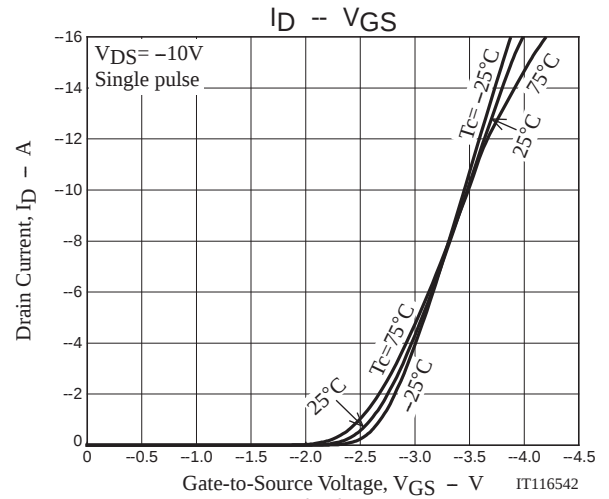
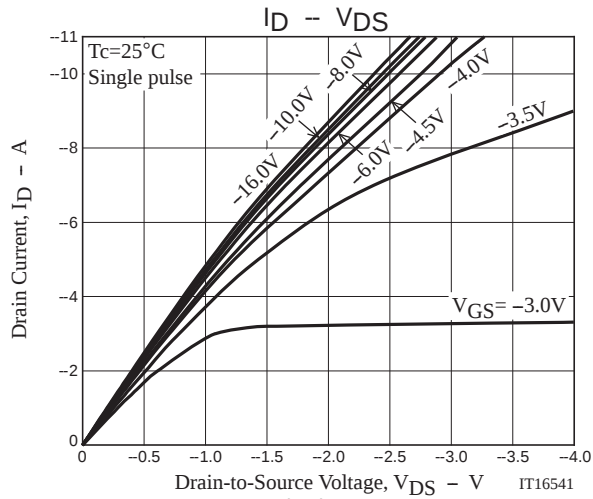
Electrical Connection

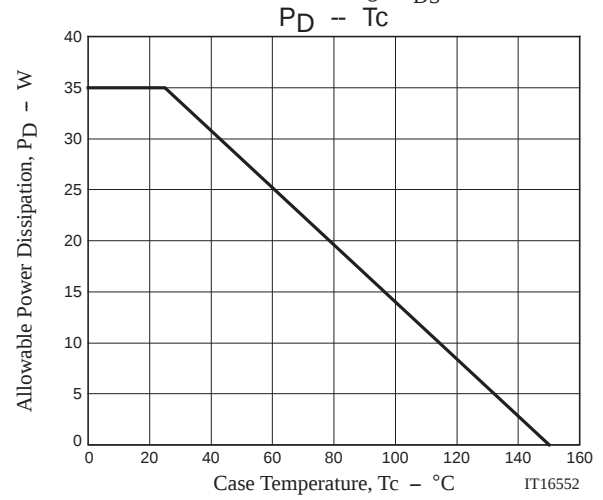
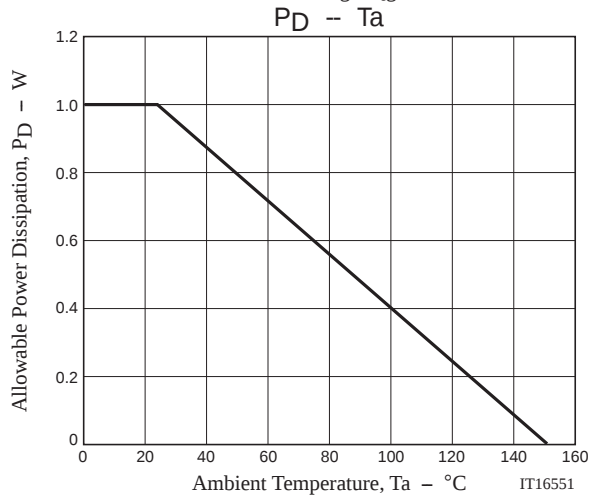
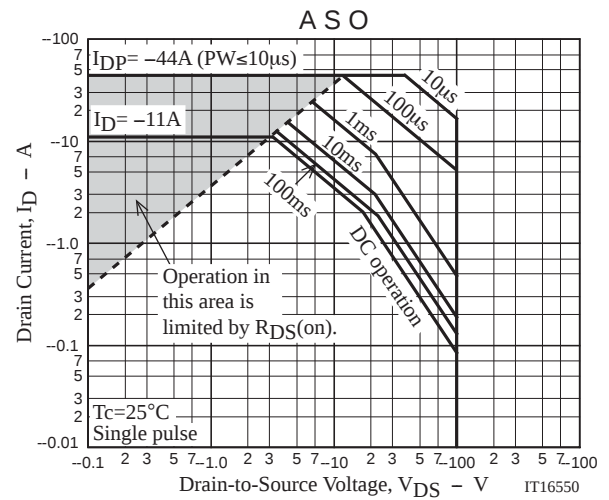
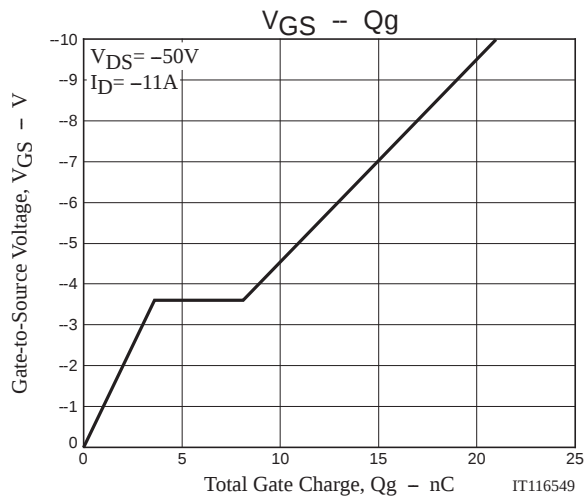


Electrical Characteristics at $T_a=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=-1\text{mA}$, $V_{GS}=0\text{V}$	-100			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-100\text{V}$, $V_{GS}=0\text{V}$			-1	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 16\text{V}$, $V_{DS}=0\text{V}$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=-10\text{V}$, $I_D=-1\text{mA}$	-1.2		-2.6	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=-10\text{V}$, $I_D=-5.5\text{A}$		8.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=-5.5\text{A}$, $V_{GS}=-10\text{V}$		210	275	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=-3\text{A}$, $V_{GS}=-4.5\text{V}$		225	315	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D=-3\text{A}$, $V_{GS}=-4\text{V}$		235	330	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS}=-20\text{V}$, $f=1\text{MHz}$		1020		pF
Output Capacitance	C_{oss}			72		pF
Reverse Transfer Capacitance	C_{rss}			43		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		9.5		ns
Rise Time	t_r			25		ns
Turn-OFF Delay Time	$t_d(off)$			105		ns
Fall Time	t_f			55		ns
Total Gate Charge	Q_g	$V_{DS}=-50\text{V}$, $V_{GS}=-10\text{V}$, $I_D=-11\text{A}$		21		nC
Gate-to-Source Charge	Q_{gs}			3.6		nC
Gate-to-Drain "Miller" Charge	Q_{gd}			4.5		nC
Diode Forward Voltage	V_{SD}	$I_S=-11\text{A}$, $V_{GS}=0\text{V}$		-0.93	-1.5	V

Switching Time Test Circuit





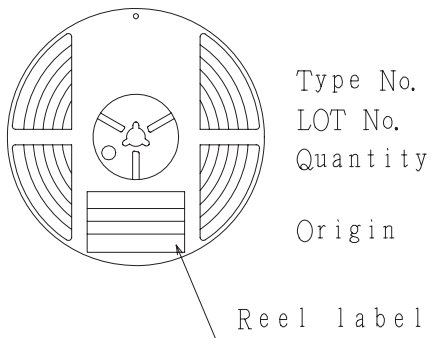
Taping Specification

SFT1345-TL-H

Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
TP-FA	TP	700	2,100	12,600	3 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Packing method



Type No.

LOT No.

Quantity

Origin

Reel label, Inner box label
(unit:mm)

(P) TYPE	000000000
(1) LOT	00
(Q) QTY	0,000 (1) LEAD FREE *
(Z) SPECIAL	*Z0722005310C*
	ASSEMBLY:**** (DIFFUSION:****)

Outer box label

It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

TYPE CODE	
	000000000000
TYPE	00000000
QTY	0,000 PCS (1) LEAD FREE *
LOT	00000000
PACKAGE	00000000
SPECIAL	
	Z0722005310C
	ASSEMBLY:**** (DIFFUSION:****)

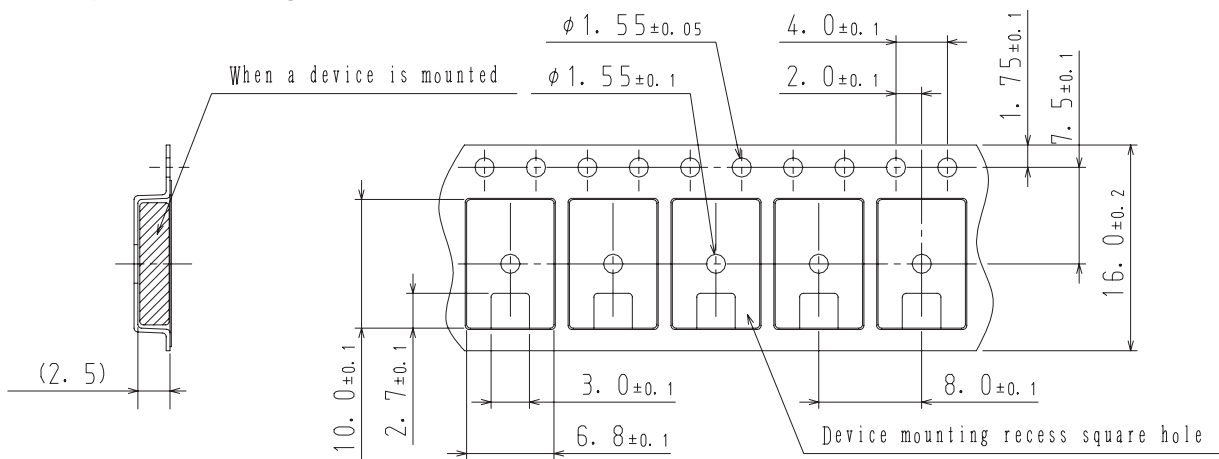
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

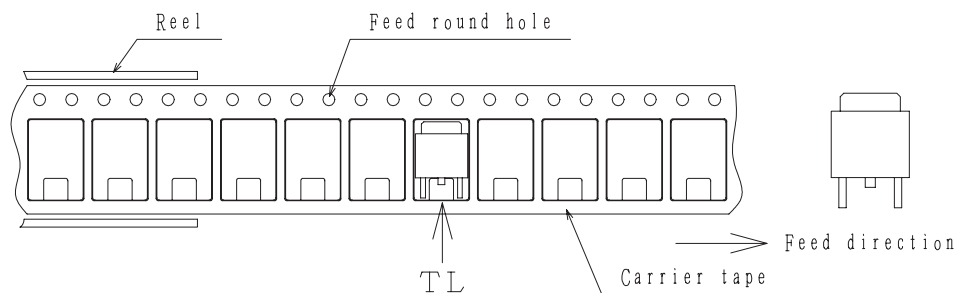
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

Taping configuration

1. Carrier tape size (unit:mm)

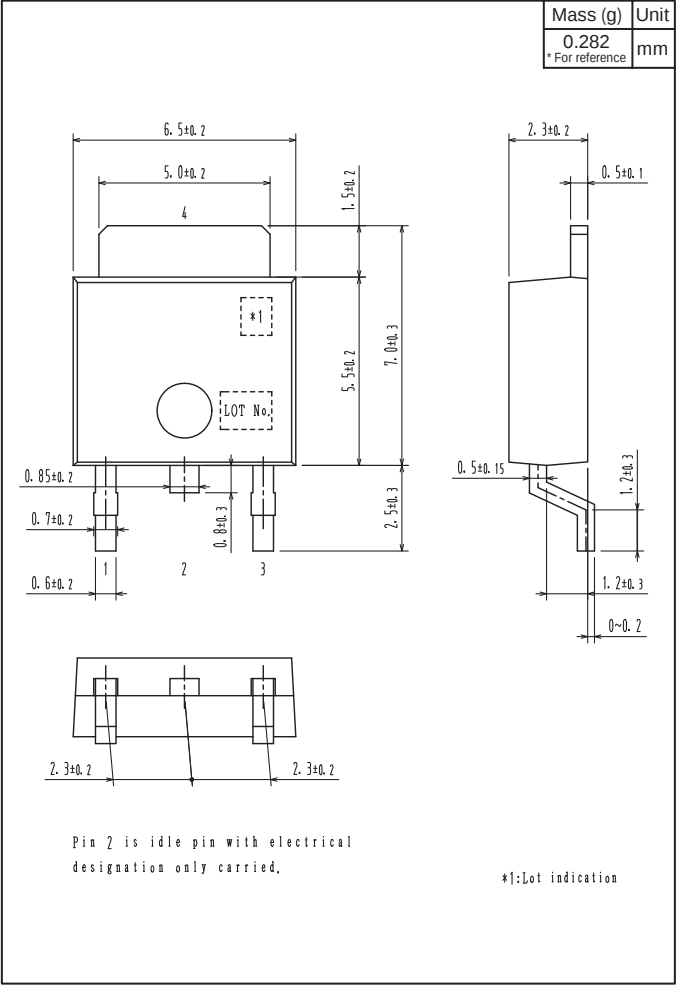


2. Device placement direction

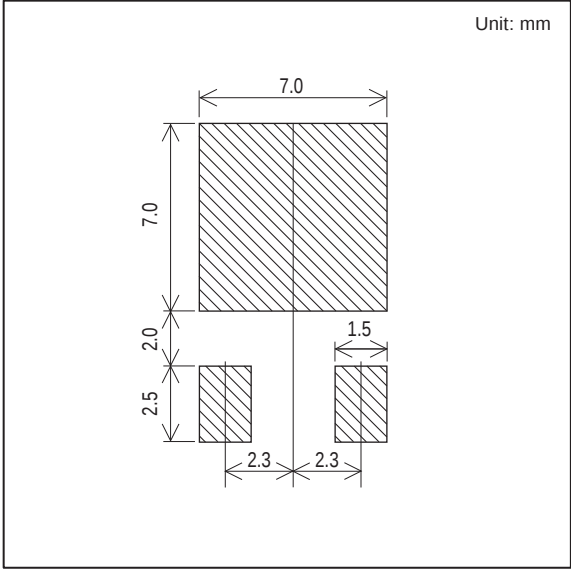


Those with one electrode terminal on the feed hole side.....TL

Outline Drawing
SFT1345-TL-H



Land Pattern Example

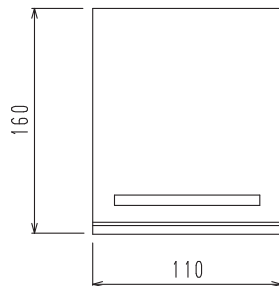
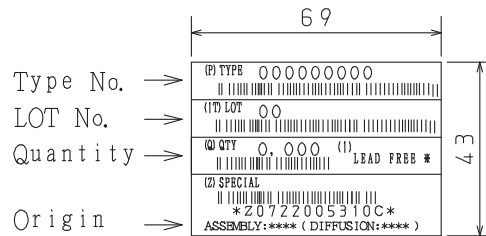


Bag Packing Specification

SFT1345-H

1. Packing Format

Package Name	Maximum Number of devices contained (pcs)			
	Bag	Inner box	Outer box	
TP	500	B-1	A-1	A-2
		10, 000	50, 000	30, 000
		Packing format (Dimensions:mm (external))		
		Inner box	Outer box	
		B-1	A-1	A-2
		445×225×55	470×250×300	470×250×190

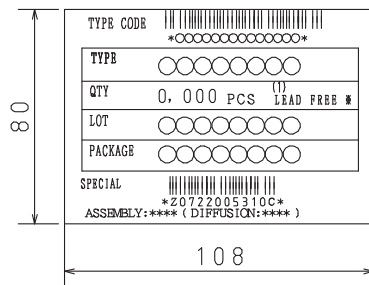
2. Bag dimensions
(unit:mm)3. Bag label, Inner box label
(unit:mm)4. Outer box label
(unit:mm)

It is a label at the time of factory shipments.
The form of a label may change in physical
distribution process.

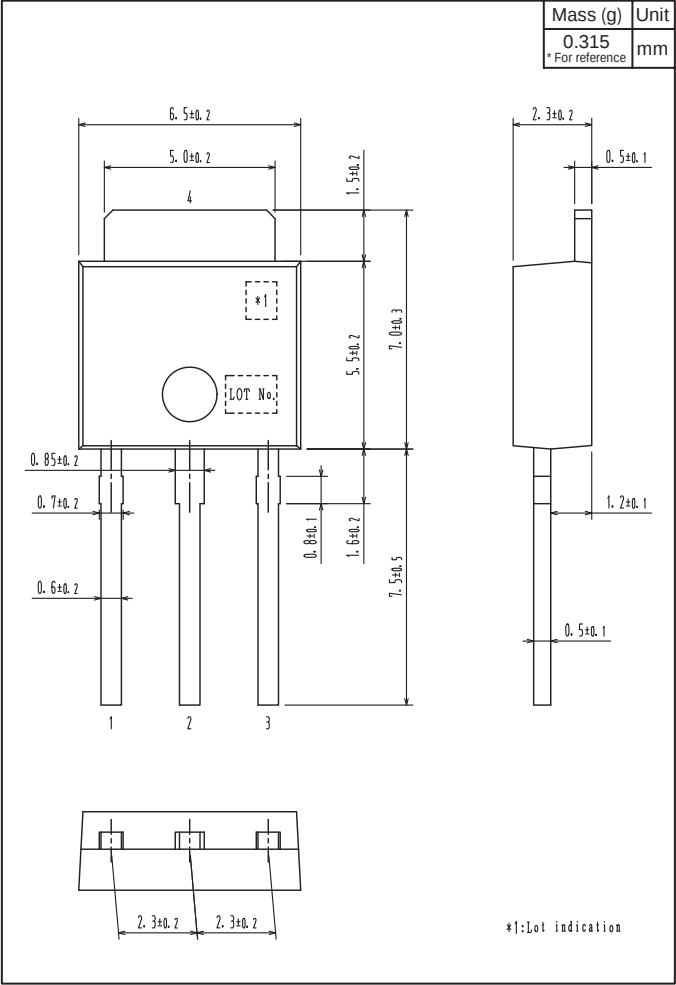
NOTE (1)

The LEAD FREE * description shows that the
surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3



Outline Drawing
SFT1345-H



Note on usage : Since the SFT1345 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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