

Standard Recovery Diodes (Stud Version), 25 A



PRODUCT SUMMARY			
I _{F(AV)}	25 A		
Package	DO-203AA (DO-4)		
Circuit configuration	Single diode		

FEATURES

- High surge current capability
- Stud cathode and stud anode version



- · Wide current range
- Types up to 1200 V V_{RRM}
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- · Battery charges
- Converters
- Power supplies
- Machine tool controls

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		25	A	
I _{F(AV)}	T _C	120	°C	
I _{F(RMS)}		40	A	
I _{FSM}	50 Hz	356	٨	
	60 Hz	373	А	
I2t	50 Hz	636	A ² s	
	60 Hz	580	A-5	
V _{RRM}	Range	100 to 1200	V	
T _J		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	V _{R(BR)} , MINIMUM AVALANCHE VOLTAGE V ⁽¹⁾	I _{RRM} MAXIMUM AT T _J = 175 °C mA
	10	100	150	-	
	20	200	275	-	
	40	400	500	500	
VS-25F(R)	60	600	725	750	12
	80	800	950	950	
	100	1000	1200	1150	
	120	1200	1400	1350	

Note

 $^{^{(1)}}$ Avalanche version only available from V_{RRM} 400 V to 1200 V



FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current at case temperature	I _{F(AV)}	180° conduction, half sine wave		25 120	A °C	
Maximum RMS forward current	I _{F(RMS)}				40	Α
Maximum on-repetitive peak reverse power	P _R ⁽¹⁾	10 µs squa	re pulse, T _J = T _c	ı maximum	10	K/W
		t = 10 ms	No voltage		356	A
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied	Sinusoidal half wave, initial $T_J = T_J$ maximum	373	
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		300	
		t = 8.3 ms	reapplied		314	
	l ² t	t = 10 ms	No voltage		636	A ² s
Mar. 121 fact of all a		t = 8.3 ms	reapplied		580	
Maximum I ² t for fusing		t = 10 ms	100 % V _{RRM} reapplied		450	
		t = 8.3 ms			410	
Maximum I ² √t for fusing	I ² √t	t = 0.1 to 10 ms, no voltage reapplied		6360	A ² √s	
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum		0.80	V	
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		0.90] v	
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum		6.80	mΩ	
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$		5.70	11152	
Maximum forward voltage drop	V_{FM}	$I_{pk} = 78 \text{ A}, T_J = 25 ^{\circ}\text{C}, t_p = 400 \mu \text{s} \text{ rectangular wave}$		1.30	V	

Note

⁽¹⁾ Available only for avalanche version, all other parameters the same as 25F

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction operating temperature range	T_J		- 65 to 175	°C	
Maximum storage temperature range	T _{Stg}		- 65 to 200	-0	
Maximum thermal resistance, junction to case	R_{thJC}	C DC operation 1.5		KAM	
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased	0.5	K/W	
Allowable mounting toward		Not lubricated threads	1.5 + ^{0 - 10} % (13)	N · m (lbf · in)	
Allowable mounting torque		Lubricated threads	1.2 + ^{0 - 10} % (10)	N · m (lbf · in)	
Approximate weight			7	g	
Approximate weight			0.25	oz.	
Case style		See dimensions - link at the end of datasheet DO-203AA (DO-4)		A (DO-4)	

△R _{thJC} CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.28	0.24			
120°	0.39	0.41			
90°	0.50	0.54	$T_J = T_J$ maximum	K/W	
60°	0.73	0.75			
30°	1.20	1.21			

Note

The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC



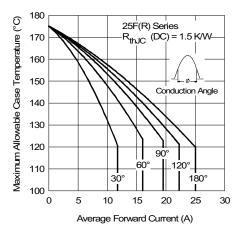


Fig. 1 - Current Ratings Characteristics

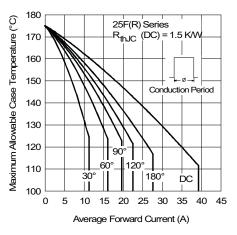


Fig. 2 - Current Ratings Characteristics

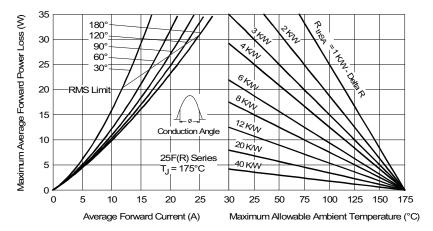


Fig. 3 - Forward Power Loss Characteristics

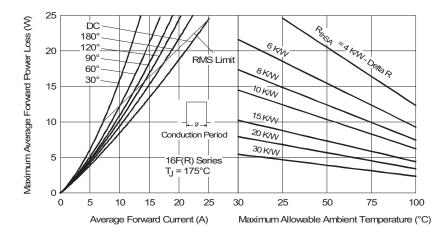


Fig. 4 - Forward Power Loss Characteristics

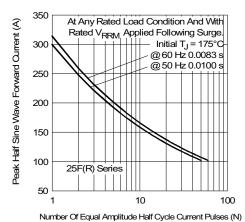


Fig. 5 - Maximum Non-Repetitive Surge Current

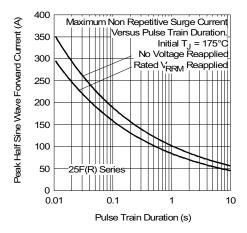


Fig. 6 - Maximum Non-Repetitive Surge Current

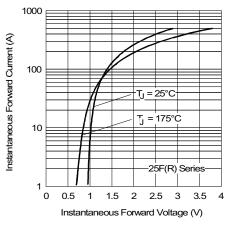


Fig. 7 - Forward Voltage Drop Characteristics

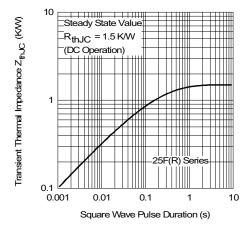


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code

VS-	25	F	R	120	M
1	2	3	4	5	6

- 1 Vishay Semiconductors product
- 2 Current rating: Code = I_{F(AV)}
- 3 F = Standard device
- None = Stud normal polarity (cathode to stud)
 R = Stud reverse polarity (anode to stud)
- 5 Voltage code x 10 = V_{RRM} (see Voltage Ratings table)
- 6 None = Stud base DO-203AA (DO-4) 10-32UNF-2A

M = Stud base DO-203AA (DO-4) M5 X 0.8

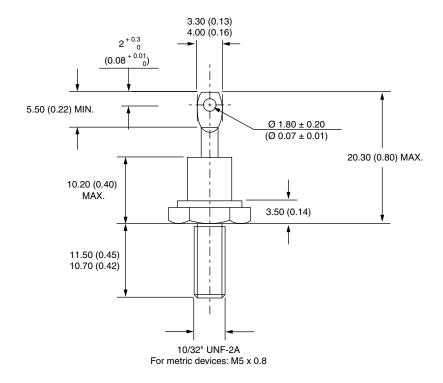
(not available for avalanche diodes)

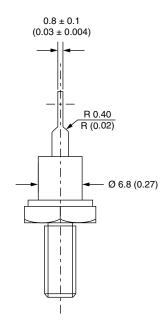
LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95311	

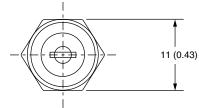


DO-203AA (DO-4)

DIMENSIONS in millimeters (inches)









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