



Features

· 5 dBm Output Level at 98 MHz

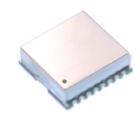
· 2nd Harmonic: < -20 dBc

· Spurious Level: < -68 dBc

· Current Consumption: < 40 mA

Description

The plerowTM PLL synthesizer module was designed for use in wireless and wireline systems in a wide range of frequency from 50 MHz to 6 GHz. ASB's PLL provides exceptionally low spurious and phase noise performance with fast locking time and low current consumption. All products are available in a surface-mount type package.



Specifications

Parameter	Unit	Min.	Typical	Max.
Frequency Range	MHz		98	
Output Power	dBm	3	5	7
Supply Voltage	V		5	
Current Consumption	mA		28	40
2 nd Harmonics	dBc		-25.0	-20.0
Spurious Level	dBc		-70	-68
Reference Frequency	MHz		10	
Reference Input Level	dBm		5	
Phase Noise (C / N)		0	2	5
@ 1 kHz			-88	-82
@ 10 kHz	dBc/Hz		-108	-103
@ 100 kHz			-120	-115
Output Impedance	Ω		50	
Operating Temp. Range	°C	-30	25	80
Package Type & Size	mm	SMT, 19.0W×19.0L×5.8H		

¹⁾ Measurement conditions are as follows: T = 25° C, V_{CC} = 5 V, Freq. = 98MHz, 50 ohm system.

More Information

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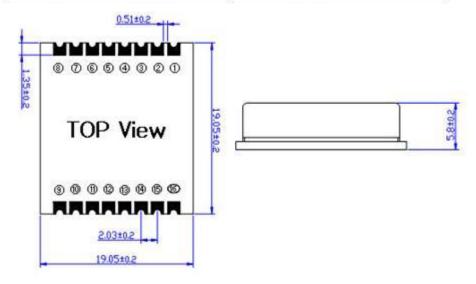
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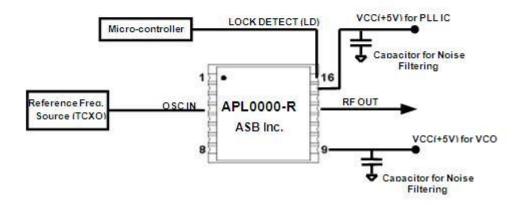


APL0000-R Type

Туре	Dimension
PLL IC + VCO + ROM	19 x 19 x 5.8



Pin Out for PLL					
Pin No.	Application	Pin No.	Application		
1	GROUND	9	VCC (VCO)		
2	GROUND	13	RF OUT		
3	GROUND	15	VCC (PLL)		
4	OSC IN	16	LOCK DETECT		



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