

Medium-high Voltage Multilayer Ceramic Capacitors

HMK325B7104MF-T



■ Features

- Item Summary
100V, X7R, 0.1 μ F, \pm 20%, 1210
- Lifecycle Stage
Mass Production
- Standard packaging quantity (minimum)
Taping Embossed 2000pcs

■ Products characteristics table

CaseSize (EIA/JIS)	1210/3225
Rated Voltage (max)	100V
Capacitance	0.1 μ F
Capacitance Tolerance	\pm 20%
Resistance	100 M Ω · μ F min.
Dielectric Loss Tangent	3.5%MAX
Temperature Characteristics (EIA/JIS)	X7R/-
Temperature Range (EIA/JIS)	-55 to +125 $^{\circ}$ C/-
Capacitance Change (EIA/JIS)	\pm 15/-%
RoHS Compliance	Yes
Halogen Free	Yes
Soldering Method	Reflow

■ External Dimensions

L	3.2mm \pm 0.30
W	2.5mm \pm 0.20
T	1.15mm \pm 0.10
e	0.6mm \pm 0.3

2015.01.12

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
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 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor **TAIYO YUDEN**

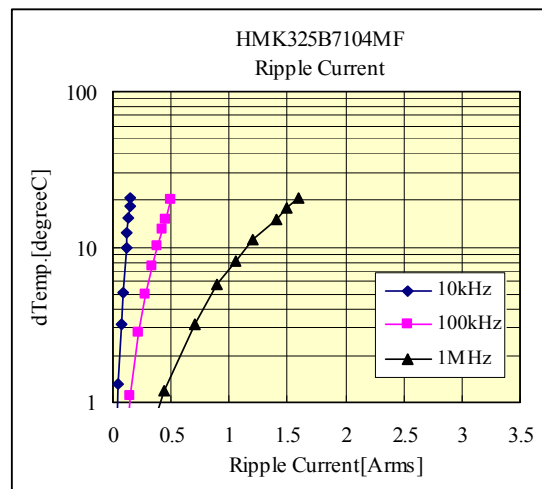
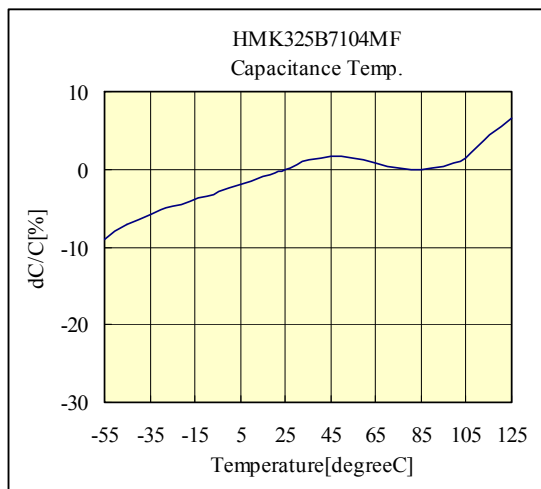
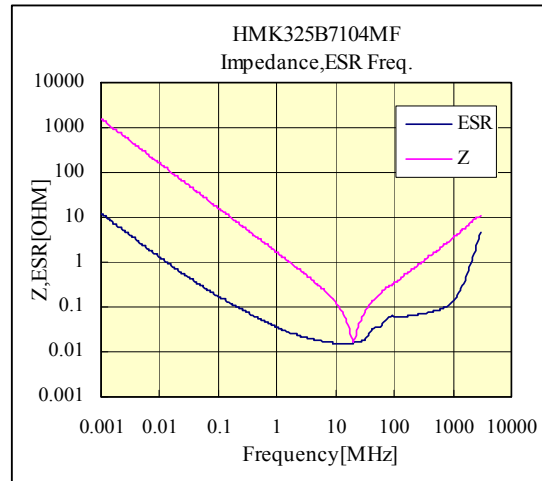
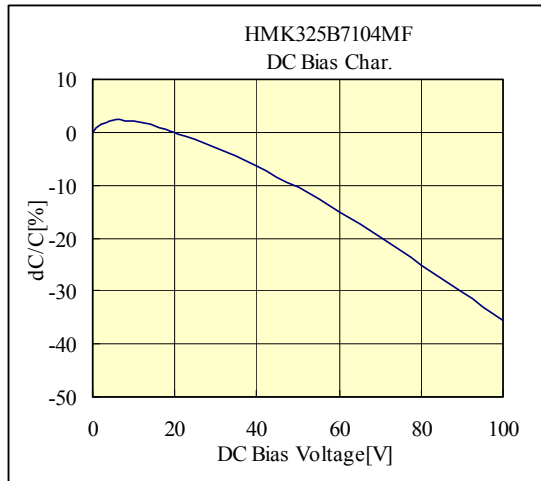
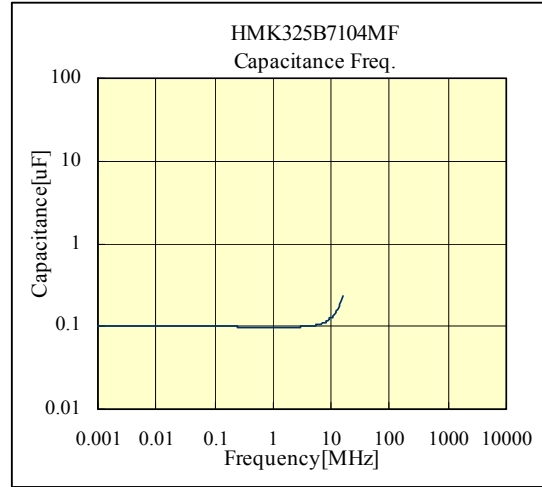
-Electrical Characteristics Data-

13QA01

RoHS compliance product

○HMK325B7104MF

100V 3225 X7R 0.1 μ F +/- 20% t=1.25mmMAX
100V 1210 X7R 0.1 μ F +/- 20% t=0.049inchMAX



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