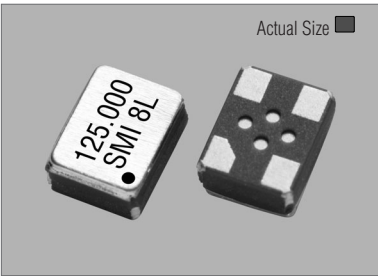


Crystal Clock Oscillators

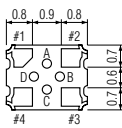
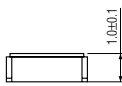
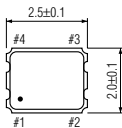
22SMOP (+2.8V, +3.0V or +3.3V PROGRAMMABLE MODELS)

STANDARD SMD CLOCK OSCILLATORS

22SMOP



22SMOP

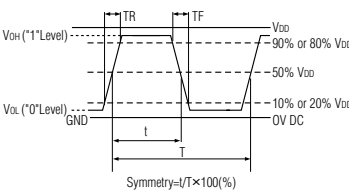


* The terminals (A, B, C & D) not to be connected.

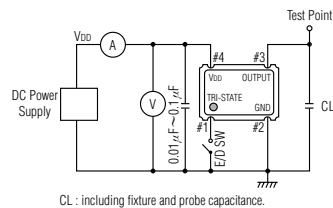
PIN	CONNECTION
1	"L" OPEN or "H"
2	GND
3	Z OUTPUT
4	VDD

Z : high impedance

OUTPUT WAVEFORM



TEST CIRCUIT



STANDARD SPECIFICATIONS

using PLL technology

Item	Specifications
Generic part number	22SMOP ^{※1}
Frequency range	0.5 MHz to 200.0000 MHz
Frequency stability	22SMOP(A) : ± 100 ppm over -20°C to $+75^{\circ}\text{C}$ 22SMOP(B) : ± 50 ppm over -20°C to $+75^{\circ}\text{C}$ 22SMOP(AW) : ± 100 ppm over -40°C to $+85^{\circ}\text{C}$
over all conditions	
Operating Conditions	
Operating temperature	-20°C to $+75^{\circ}\text{C}$ (standard) -40°C to $+85^{\circ}\text{C}$ (W)
Input voltage (VDD)	$+2.8\text{V} \pm 5\%$, $+3.0\text{V} \pm 5\%$ or $+3.3\text{V} \pm 5\%$
Stand-by control voltage (Pin#1)	V _{IH} : 80%VDD min. V _{IL} : 20%VDD max. ^{※2}
Absolute Max. Ratings	
Supply voltage	-0.3V to $+7.0\text{V}$ DC
Storage temperature	-55°C to $+125^{\circ}\text{C}$
Input current	30 mA max. (Pin #1 = Open or V _{IH})
Stand-by current ^{※2}	100 μA max. (Pin #1=V _{IL})
Output (-20°C to $+75^{\circ}\text{C}$)	
Symmetry	45% to 55% at 50%VDD level (0.5MHz to 80.00MHz) 40% to 60% at 50%VDD level (80.00MHz to 200.00MHz)
Rise and fall times (10%VDD to 90%VDD level)	7 ns max. (0.5MHz to 100.000MHz) 4 ns max. (100.000MHz to 200.000MHz)
"0" level	V _{OL} : 10%VDD max.
"1" level	V _{OH} : 90%VDD min.
Load	15 pF max. (CMOS)
Disable delay time	150 ns max.
Enable delay time	10 ms max.
Startup time	10 ms max.
Aging	± 5 ppm max. at $+25^{\circ}\text{C} \pm 3^{\circ}\text{C}$ for first year
Jitter	200 ps : Cycle to cycle 250 ps : Peak to peak
Reflow condition	$+250^{\circ}\text{C} \pm 10^{\circ}\text{C}$ for 10 seconds $+170^{\circ}\text{C} \pm 10^{\circ}\text{C}$ for 1 to 2 minutes (preheating)

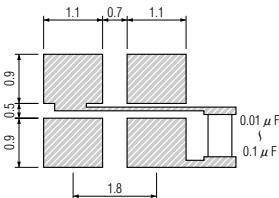
(※1) Final exact part number to be determined with frequency, frequency stability, operating temperature and input voltage.
e.g. 22SMOP(3.3VA) 125.000 MHz.

(※2) Internal crystal oscillation to be halted (Pin #1=V_{IL}).

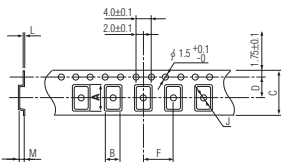
PACKAGE DATA

Item	Package	22SMOP
Lid		Metal
Base		Ceramic
Sealing		AuSn
Terminal		Tungsten (metalized)
Terminal plating		Gold / Nickel (surface) / (under)
RoHS		Compliant (Pb-free)

SOLDERING PATTERN



TAPE SPECIFICATIONS



A	B	C	D	F	J	L	M	Reel Dia.	Qty/Reel
2.8	2.3	8.0	3.5	4.0	1.0	0.25	1.1	180	2000pcs 1000pcs