

### **BULT3N4**

## Medium voltage fast-switching NPN power transistor

#### **Features**

- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

### **Application**

■ Electronic ballast for fluorescent lighting

### **Description**

The device is manufactured using high voltage multi-epitaxial planar technology for high switching speeds and medium voltage capability.

It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is expressly designed for a new solution to be used in compact fluorescent lamps, where it is coupled with the BULT3P3, its complementary PNP transistor.

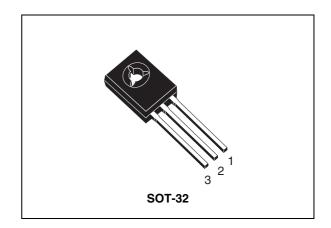


Figure 1. Internal schematic diagram

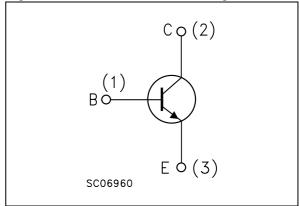


Table 1. Device summary

Order code	Marking	Package	Packing
BULT3N4	BULT3N4	SOT-32	Tube

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# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>BE</sub> = 0)	400	V
V <sub>CEO</sub>	Collector-emitter voltage (I <sub>B</sub> = 0)	200	٧
V <sub>EBO</sub>	Emitter-base voltage $(I_C = 0, I_B = 1.5 \text{ A}, t_p < 100  \mu\text{s}, T_j < 150^{\circ}\text{C})$	V <sub>(BR)EBO</sub>	V
I <sub>C</sub>	Collector current	3	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	6	Α
I <sub>B</sub>	Base current	1.5	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5 ms)	3	Α
P <sub>tot</sub>	Total dissipation at T <sub>c</sub> = 25 °C	32	W
T <sub>stg</sub>	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R <sub>thJC</sub>	Thermal resistance junction-case max	3.9	°C/W

### 2 Electrical characteristics

 $T_{case}$  = 25 °C unless otherwise specified

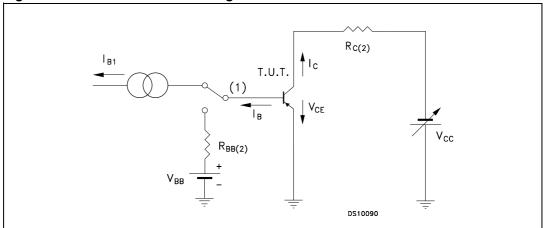
Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector cut-off current (V <sub>BE</sub> = 0)	V <sub>CE</sub> = 400 V V <sub>CE</sub> = 400 V T <sub>C</sub> = 125 °C			0.1 0.5	mA mA
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage $(I_C = 0)$	I <sub>E</sub> = 10 mA	9		18	V
V <sub>CEO(sus)</sub> (1)	Collector-emitter sustaining voltage (I <sub>B</sub> = 0)	I <sub>C</sub> = 10 mA	200			V
V <sub>CE(sat)</sub> (1)	Collector-emitter saturation voltage	$I_C = 0.7 \text{ A}$ $I_B = 0.1 \text{ A}$ $I_C = 1 \text{ A}$ $I_B = 0.2 \text{ A}$			0.4 0.5	V V
V <sub>BE(sat)</sub> (1)	Base-emitter saturation voltage	$I_C = 0.5 \text{ A}$ $I_B = 0.1 \text{ A}$ $I_C = 1 \text{ A}$ $I_B = 0.2 \text{ A}$ $I_C = 2 \text{ A}$ $I_B = 0.4 \text{ A}$			1 1.1 1.3	V V V
h <sub>FE</sub>	DC current gain	$\begin{split} & I_{C} = 10 \text{ mA} & V_{CE} = 5 \text{ V} \\ & I_{C} = 0.75 \text{ A} & V_{CE} = 5 \text{ V} \\ & I_{C} = 2 \text{ A} & V_{CE} = 5 \text{ V} \end{split}$	10 22 4	28	36	
t <sub>r</sub> t <sub>s</sub>	Resistive load Rise time Storage time Fall time	$I_C = 0.7 \text{ A}$ $V_{CC} = 150 \text{ V}$ $I_{B(on)} = -I_{B(off)} = 140 \text{ mA}$ $T_p = 30  \mu\text{s}$		80 1.2 100	1.6 130	ns µs ns
t <sub>s</sub>	Inductive load Storage time Fall time	$\begin{split} I_{C} &= 1 \text{ A} & I_{B(on)} = 100 \text{ mA} \\ V_{BE(off)} &= -5 \text{ V} & R_{BB} = 0 \\ V_{clamp} &= 150 \text{ V L} = 1 \text{ mH} \end{split}$		120 50	200 90	ns ns

<sup>1.</sup> Pulse test: pulse duration  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %

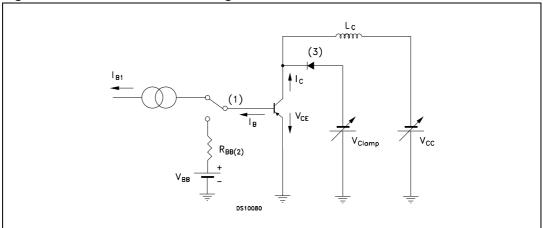
### 2.1 Test circuits

Figure 2. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

Figure 3. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

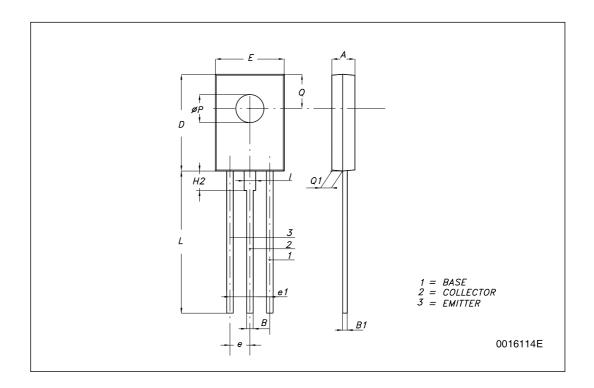
## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.



#### **SOT-32 (TO-126) MECHANICAL DATA**

DIM	mm.			
DIM.	MIN.	TYP	MAX.	
А	2.4		2.9	
В	0.64		0.88	
B1	0.39		0.63	
D	10.5		11.05	
E	7.4		7.8	
е	2.04	2.29	2.54	
e1	4.07	4.58	5.08	
L	15.3		16	
Р	2.9		3.2	
Q		3.8		
Q1	1		1.52	
H2		2.15		
I		1.27		





BULT3N4 Revision history

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes
25-Sep-2009	1	Initial release.



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