TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIV)

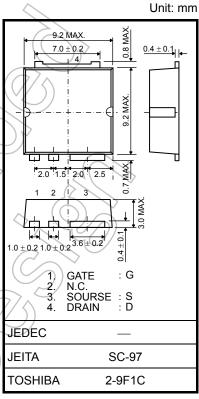
TK70X06K3

Load switch Applications Motor Drive Applications

- Low drain-source ON-resistance: $R_{DS(ON)} = 6.5 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: |Y_{fs}| = 120 S (typ.)
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 60 V)$
- Enhancement mode: V_{th} = 3.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit		
Drain-source voltage		V_{DSS}	60	A		
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	60	У	
Gate-source voltage			V _{GSS}	±20	> v	
Drain current	DC	(Note 1)	I _D	70	A	
	Pulse	(Note 1)	I _{DP}	210		
Drain power dissipation (Tc = 25°C)			P _D <	80	w	
Single pulse avalanche energy (Note 2)		EAS	37	m)		
Avalanche current		IAR	70	Α		
Repetitive avalanche energy (Note 3)		(EAR \	8	mJ		
Channel temperature (Note 4)		Tch	175	2%		
Storage temperature range (Note 4)		√T _{stg}	-55 to 175	∫√°C		



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

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Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.875	°C/W

- Note 1: Ensure that the channel temperature does not exceed 175°C.
- Note 2: $V_{DD} = 25 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), $L = 10 \,\mu\text{H}$, $I_{AR} = 70 \,\text{A}$, $R_G = 25 \,\Omega$
- Note 3: Repetitive rating: pulse width limited by maximum channel temperature.
- Note 4: 175°C refers to AEC-Q101.

This transistor is an electrostatic-sensitive device. Handle with care.

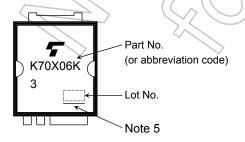
Electrical Characteristics (Ta = 25°C)

Cha	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±1	μА
Drain cut-off curr	ent	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	60	_	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	35	_	_	V
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	3.0))^_	4.0	V
Drain-source ON	-resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 35 A) <u> </u>	6.5	8.0	mΩ
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 35 A	_60	120	_	S
Input capacitance		C _{iss})	2650	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	370	_	pF
Output capacitance		Coss		_	480	_	
Switching time	Rise time	t _r	V _{GS} 10 V I _D = 35 A V _{OUT}	- /	217	<u> </u>	20
	Turn-on time	t _{on}	G € € € 6 € 6 € 6 € 6 € 6 € 6 € 6 € 6 €	_((35) –	
	Fall time	t _f	V _{DD} ≈ 30 V — 19		_	ns	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs		48	_	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD.} ≈ 48 V, V _{GS} ≠ 10 V;) —	62	_	
Gate-source charge		Q _{gs}	$I_D = 70 \text{ A}$	_	34		nC
Gate-drain ("miller") charge		Q _{gd}			28		

Source-Drain Ratings and Characteristics (Note 6) (Ta = 25°C)

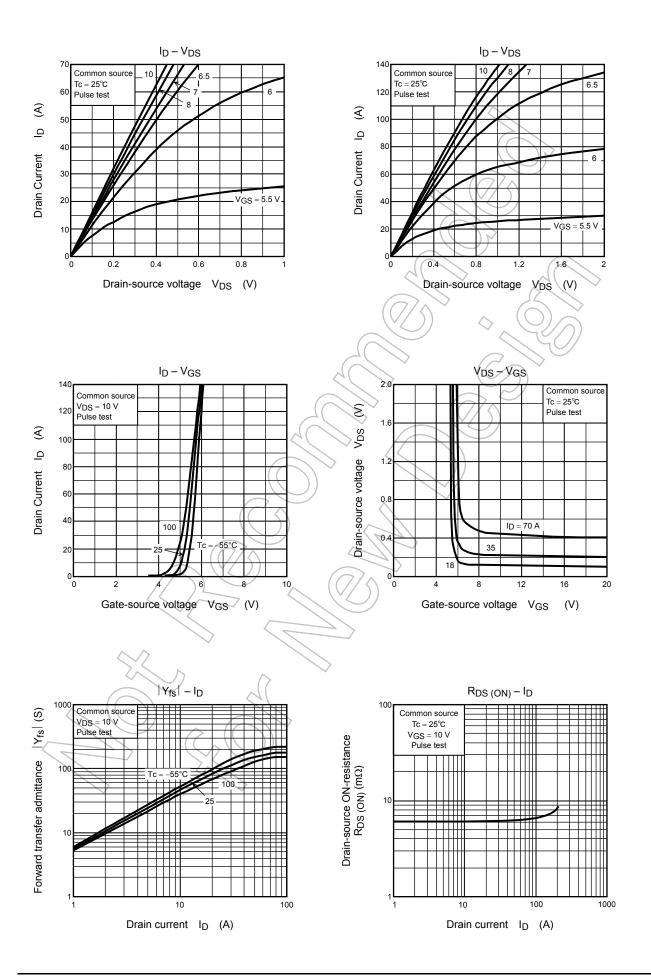
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	77^ -	_	_	70	Α
Pulse drain reverse current (Note 1)	IDRP		_	_	210	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 70 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 70 A, V _{GS} = 0 V,	_	46	_	ns
Reverse recovery charge	Qtt	dl _{DR} /dt = 50 A/μs	_	35		nC

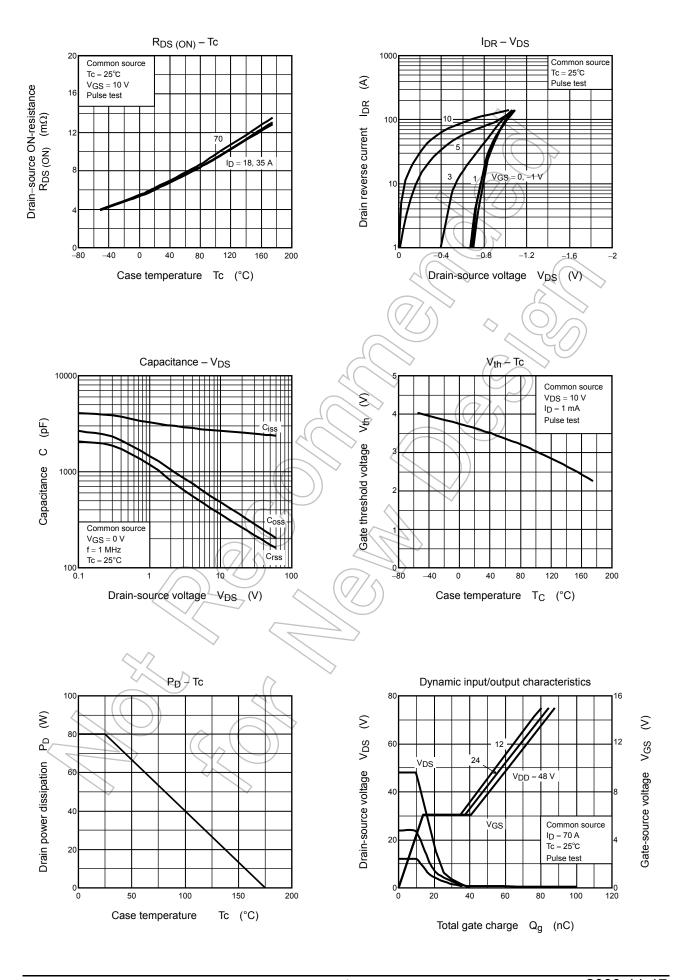
Marking

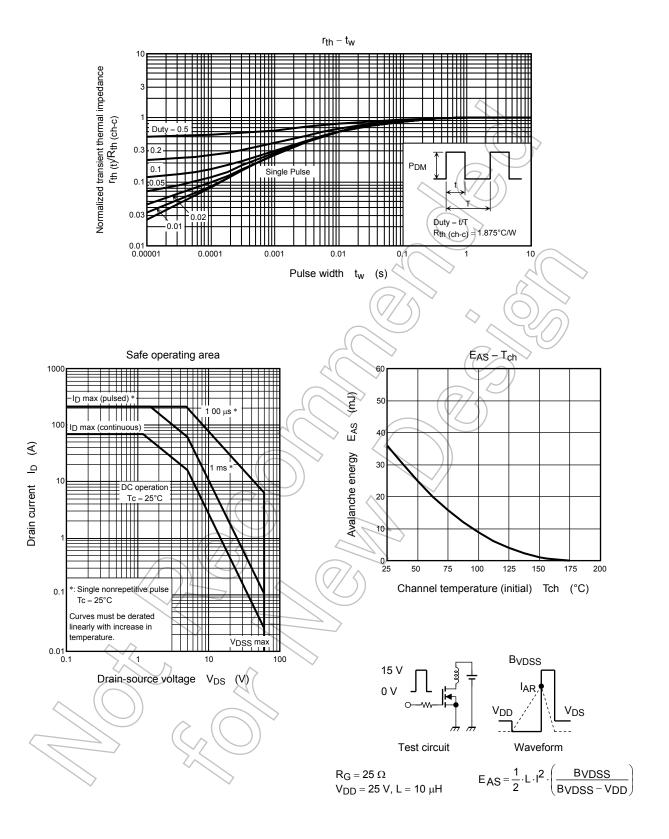


Note 5: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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6