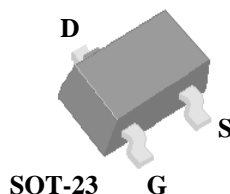
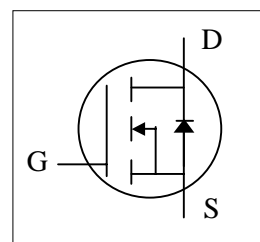


- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device
- ▼ Halogen Free & RoHS Compliant Product



BV_{DSS}	30V
$R_{DS(ON)}$	42m Ω
I_D	4.7A



Description

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The SOT-23 package is widely used for commercial-industrial applications.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current ³ , V_{GS} @ 10V	4.7	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current ³ , V_{GS} @ 10V	3.7	A
I_{DM}	Pulsed Drain Current ¹	20	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation	1.38	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient ³	90	$^\circ\text{C}/\text{W}$

AP2326GN-HF

Electrical Characteristics @ $T_j=25^{\circ}\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=4A$	-	-	42	m Ω
		$V_{GS}=4.5V, I_D=3A$	-	-	60	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1	-	3	V
g_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=3A$	-	9	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30V, V_{GS}=0V$	-	-	10	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_D=3A$	-	4.3	6.9	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=15V$	-	1.1	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=4.5V$	-	2.4	-	nC
$t_{d(on)}$	Turn-on Delay Time ²	$V_{DS}=15V$	-	5.5	-	ns
t_r	Rise Time	$I_D=1A$	-	7.5	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{GS}=10V$	-	12	-	ns
t_f	Fall Time	$R_D=15\Omega$	-	2.5	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	250	400	pF
C_{oss}	Output Capacitance	$V_{DS}=25V$	-	55	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	50	-	pF
R_g	Gate Resistance	$f=1.0\text{MHz}$	-	1.4	-	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=1.2A, V_{GS}=0V$	-	-	1.2	V
t_{rr}	Reverse Recovery Time ²	$I_S=3A, V_{GS}=0V,$	-	14	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100A/\mu s$	-	7	-	nC

Notes:

1. Pulse width limited by Max. junction temperature.
2. Pulse test
3. Surface mounted on 1 in² copper pad of FR4 board ; 270°C/W when mounted on min. copper pad.