

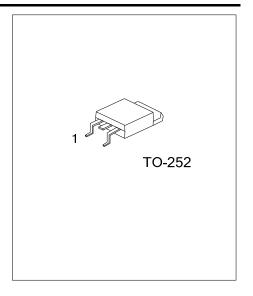
UTT20P04 Preliminary Power MOSFET

-40V, -20A P-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **UTT20P04** is a P-channel Power MOSFET using UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance.

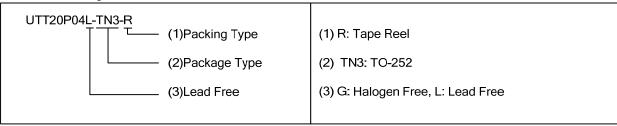
■ FEATURES



ORDERING INFORMATION

Ordering Number		Dealtons	Pin	Assignme	Deaking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
UTT20P04L-TN3-R	UTT20P04G-TN3-R	TO-252	G	D	S	Tape Reel	

Note: Pin Assignment: G: Gate D: Drain S: Sourcs



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 $[*]R_{DS(ON)}$ < 42m Ω @ V_{GS} = -10V, I_{D} = -20A

^{*} High Switching Speed

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-40	V
Gate-Source Voltage		V_{GSS}	±20	V
Drain Current	Continuous	I _D	-20	Α
	Pulsed	I _{DM}	-80	Α
Avalanche Current		I _{AR}	-20	Α
Avalanche Energy Single Pulsed		E _{AS}	36	mJ
Power Dissipation		P _D	50	W
Junction Temperature		TJ	+150	°C
Storage Temperature Range		T _{STG}	-55~+150	°C

- Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 - 2. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 - 3. T_J =25°C, V_{DD} =-25V, L=0.1mH, R_G =25 Ω , I_{AS} =-20A.

■ ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	I _D =-250μA, V _{GS} =0V	-40			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =-40V			1	μ A	
Cata Cauraa Laakaga Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V			100	nA	
Gate-Source Leakage Current	Reverse		V_{GS} =-20V, V_{DS} =0V			-100	nA	
ON CHARACTERISTICS								
Gate Threshold Voltage		$V_{GS(TH)}$	I _D =-250μA	-1		-3	V	
Static Drain-Source On-State R	agiatanaa	R _{DS(ON)}	V _{GS} =-10V, I _D =-20A			42	mΩ	
Static Drain-Source On-State R	esisiance		V_{GS} =-5V, I_D =-8A			55	mΩ	
DYNAMIC PARAMETERS								
Input Capacitance		C_{ISS}			1190		pF	
Output Capacitance		Coss	V _{GS} =0V, V _{DS} =-25V, f=1MHz		185		pF	
Reverse Transfer Capacitance		C_{RSS}			95		pF	
SWITCHING PARAMETERS								
Total Gate Charge		Q_{G}			17		nC	
Gate to Source Charge		Q_{GS}	V_{GS} =-10V, V_{DD} =-30V, I_{D} =-20A		5.5		nC	
Gate to Drain Charge		Q_{GD}			3		nC	
Turn-ON Delay Time		$t_{D(ON)}$			6		ns	
Rise Time		t_R	V_{DD} =-30V, I_{D} =-20A		16		ns	
Turn-OFF Delay Time		$t_{D(OFF)}$	$R_G=25\Omega$, $V_{GS}=-10V$		26		ns	
Fall-Time		t_{F}			10		ns	
SOURCE- DRAIN DIODE RAT	INGS AND C	HARACTERI	STICS					
Maximum Body-Diode Continuous Current		Is				-20	Α	
Maximum Body-Diode Pulsed Current		I _{SM}				-80	Α	
Drain-Source Diode Forward Voltage		V _{SD}	I _S =-20A			-1.2	V	

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