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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR NP60N04KUG

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

The NP60N04KUG is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Channel temperature 175 degree rating
- Super low on-state resistance

 $R_{DS(on)} = 6.1 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, \text{ ID} = 30 \text{ A})$

• Low Ciss: Ciss = 3400 pF TYP.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGS = 0 V)	VDSS	40	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±60	Α
Drain Current (pulse) Note1	D(pulse)	±240	Α
Total Power Dissipation (T _A = 25°C)	PT1	1.8	W
Total Power Dissipation (Tc = 25° C)	Pt2	88	W
Channel Temperature	Tch	175	°C
Storage Temperature	Tstg	–55 to +175	°C
Repetitive Avalanche Current Note2	lar	30	Α
Repetitive Avalanche Energy Note2	Ear	90	mJ

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

2. T_{ch(peak)} \leq 150°C, V_{DD} = 20 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V

THERMAL RESISTANCE

Channel to Case Thermal Resistance	Rth(ch-C)	1.71	°C/W
Channel to Ambient Thermal Resistance	Rth(ch-A)	83.3	°C/W

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ORDERING INFORMATION

PART NUMBER	PACKAGE
NP60N04KUG	TO-263 (MP-25ZK)



(TO-263)

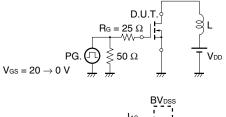
ELECTRICAL CHARACTERISTICS (TA = 25°C)

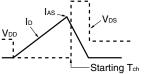
	CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
★ Ze	ero Gate Voltage Drain Current	IDSS	V _{DS} = 40 V, V _{GS} = 0 V			1	μA
Ga	ate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
★ Ga	ate to Source Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} = V _{GS} , I _D = 250 μA	2.0	3.0	4.0	V
Fo	prward Transfer Admittance Note	y _{fs}	V _{DS} = 10 V, I _D = 30 A	12	24		S
Dr	rain to Source On-state Resistance Note	RDS(on)	V _{GS} = 10 V, I _D = 30 A		4.8	6.1	mΩ
In	put Capacitance	Ciss	V _{DS} = 25 V		3400	5100	pF
0	utput Capacitance	Coss	V _{GS} = 0 V		320	480	pF
Re	everse Transfer Capacitance	Crss	f = 1 MHz		210	380	pF
Τι	urn-on Delay Time	td(on)	V _{DD} = 20 V, I _D = 30 A		30	66	ns
Ri	ise Time	tr	V _{GS} = 10 V		52	130	ns
Τι	urn-off Delay Time	t _{d(off)}	R _G = 0 Ω		78	156	ns
Fa	all Time	tr			12	30	ns
Тс	otal Gate Charge	$Q_{\rm G}$	V _{DD} = 32 V		63	95	nC
Ga	ate to Source Charge	Q _{GS}	V _{GS} = 10 V		12		nC
Ga	ate to Drain Charge	\mathbf{Q}_{GD}	I _D = 60 A		20		nC
Вс	ody Diode Forward Voltage ^{Note}	VF(S-D)	I⊧ = 60 A, V _{GS} = 0 V		0.94	1.5	V
Re	everse Recovery Time	trr	I⊧ = 60 A, V _{GS} = 0 V		37		ns
Re	everse Recovery Charge	Qrr	di/dt = 100 A/µs		40		nC

Note Pulsed

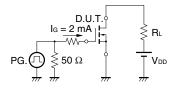
TEST CIRCUIT 1 AVALANCHE CAPABILITY

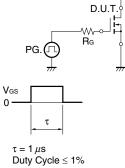
TEST CIRCUIT 2 SWITCHING TIME

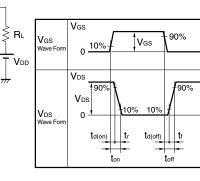




TEST CIRCUIT 3 GATE CHARGE







TOTAL POWER DISSIPATION vs.

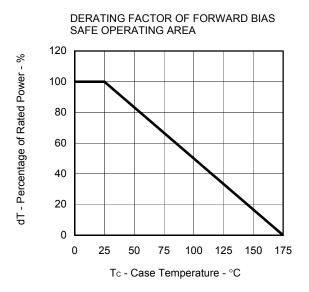
CASE TEMPERATURE

Tc - Case Temperature - °C

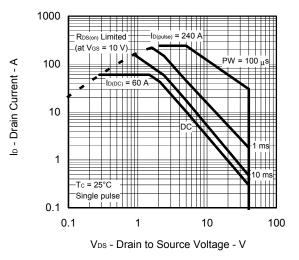
100 125

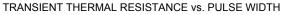
 P_{T} - Total Power Dissipation - W

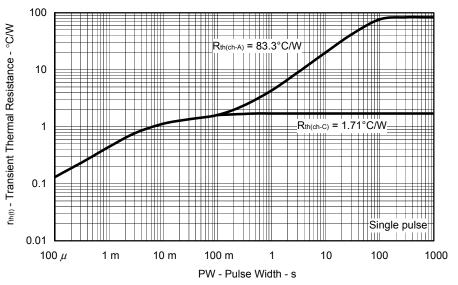
TYPICAL CHARACTERISTICS (T_A = 25°C)

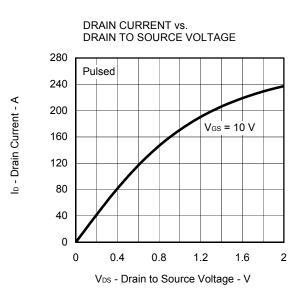


FORWARD BIAS SAFE OPERATING AREA

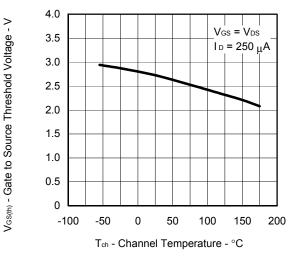


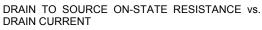


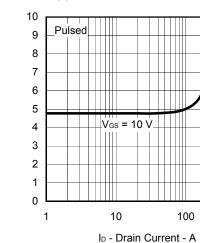




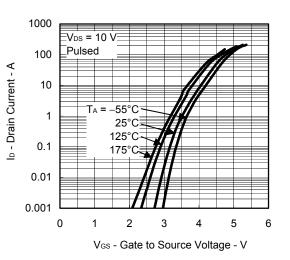
GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE



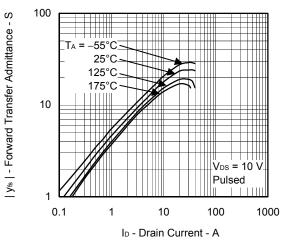




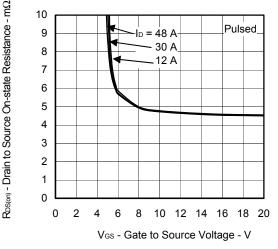




FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



1000

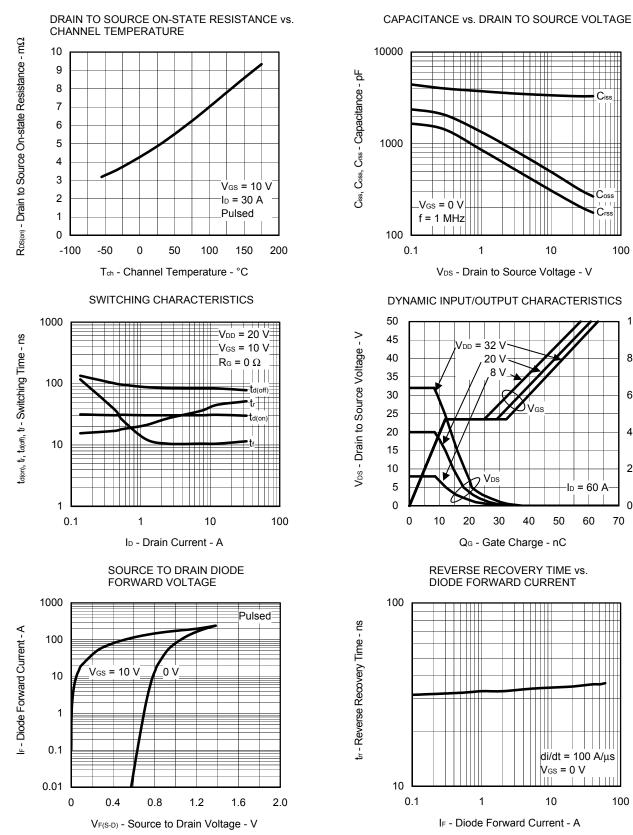
 $R_{DS(on)}$ - Drain to Source On-state Resistance - $m\Omega$

Ciss

-ID = 60 A

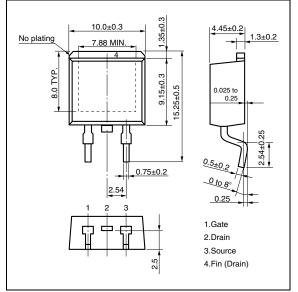
di/dt = 100 A/µs $V_{GS} = 0 V$

V_{GS} - Gate to Source Voltage - V

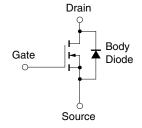


PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZK)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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