

Marketing Bulletin

DATE:	March 24 th , 2006
TO:	All Sales Personnel
-	Mark Stoner
RE:	Product Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the following Ecliptek series effective March 24th, 2006:

SeriesDescriptionRecommended ReplacementE13C93.3V 5 x 7mm SMD LVPECL OscillatorE13C7 or E13D8

In compliance with our End of Life (EOL) policy, this will serve as advanced notice of product termination. New orders will not be accepted after July 1st, 2006, with delivery to conclude by October 1st 2006.

If there are any questions pertaining to this bulletin, please fell free to contact me. Thank you again for your cooperation.

Best Regards,

Mark W Somer

Mark W. Stoner Director of Marketing Ecliptek Corporation

E13C9 Series

- RoHS Compliant (Pb-Free)
- LVPECL Output Oscillators
- 3.3V Supply Voltage
- AT-Cut Fundamental Mode Inverted Mesa Crystal
- Ceramic 6-pad SMD Package
- Stability to 25ppm
- Tri-State Enable High and Enable Low Options Available on Pad 1 or Pad 2
- Complementary Output
- ⁴ Wide Range of Available Frequencies

ELECTRICAL SPECIFICATIONS



OBSOLETE

Nominal Frequency			19.440	MHz to 200.000MHz		
Operating Temperature Range			0°C to 7	70°C, or -40°C to +85°	С	
Storage Temperature Range			-55°C t	o 125°C		
Supply Voltage (V _{cc})			3.3V _{DC} ±	5%		
Input Current			75mA M	aximum		
Frequency Tolerance / Stability	Inclusive of All Conditions: Calibrati	on Tolerance at 25°C,	±100pp	±100ppm, ±50ppm, or		
	Frequency Stability over the Operating Temperature Range, \pm 25ppm Maximum		n Maximum			
	Supply Voltage Change, Output Load Change, 1st Year					
	Aging at 25°C, Shock, and Vibration					
Output Voltage Logic High (V _{OH})			V _{cc} -1.02	25V _{DC} Minimum		
Output Voltage Logic Low (V _{oL})			V _{cc} -1.62	20V _{DC} Maximum		
Rise Time / Fall Time	20% to 80% of waveform 1.5 nSeconds Maximum, 600 pSec Ty		Sec Typical			
Duty Cycle	at 50% of waveform		50 ±10(%)			
				50 ±5(%)		
Load Drive Capability				s into V_{cc} -2.0 V_{Dc}		
Logic Control / Additional Output			No Conr	nect and Complementa	ry Output or	
	Enable High or Enable Low			e and Complementary	Output	
Enable High Tri-State Input Voltage	V_{IH} of 70% of V_{cc} Minimum		Enables	Enables Output		
	No Connection		Enables	Enables Output		
	V_{IL} of 30% of V_{CC} Maximum		Disables Output: High Impedance			
Enable Low Tri-State Input Voltage	V_{IH} of 70% of V_{CC} Minimum		Disables Output: High Impedance		nce	
No Connection			Enables Output			
	V_{IL} of 30% of V_{cc} Maximum Enables Output		Output	itput		
Output Disable Current			25mA M	laximum		
Start Up Time				conds Maximum		
RMS Phase Jitter	< 44.736MHz; F_{J} = 12kHz to 20MHz			5 pSec Maximum		
	\geq 44.736MHz, < 77.760MHz; F _J = 12k	Hz to 20MHz	2 pSec Maximum			
	\geq 77.760MHz; F _J = 12kHz to 20MHz		1 pSec Maximum			
Phase Noise (at 155.520MHz)	at 10Hz Offset		-75dBc/Hz Typical		,	
	at 100Hz Offset		-95dBc/Hz Typical -125dBc/Hz Typical			
	at 1kHz Offset					
	at 10kHz Offset		-140dBc/Hz Typical			
at 100kHz Offset -145dBc/Hz Typical						
MANUFACTURER CATEGORY ECLIPTEK CORP. OSCILLATOR	SERIES PACKA E13C9 CERAN			CLASS OS1T	REV . DATE 06/04	
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Specifications subject to change without notice.

