

SOT-23 Plastic-Encapsulate MOSFETS

CJ3402 N-Channel MOSFET

DESCRIPTION

The CJ3402 uses advanced trench technology to provide excellent

 $R_{DS(ON)}$, low gate charge and operation with gate voltage as low as 2.5V.

This device is suitable for use as a load switch or in PWM application.

FEATURES

- Lead free product is acquired
- Surface mount package

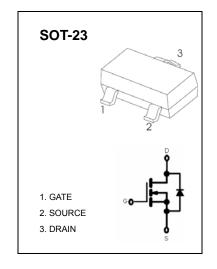


Load Switch and in PWM applications



Maximum ratings (T_a=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V_{GS}	±12	V	
Continuous Drain Current	I _D	4	Α	
Pulsed Drain Current (note 1)	I _{DM}	15	Α	
Power Dissipation	P _D	0.35	W	
Thermal Resistance from Junction to Ambient (note 2)	$R_{\theta JA}$	357	°C/W	
Junction Temperature	T_J	150	°C	
Storage Temperature	T _{STG}	-55~+150	°C	





Electrical characteristics (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
STATIC CHARACTERISTICS								
Drain-source breakdown voltage	V (BR)DSS	V _{GS} = 0V, I _D =250µA	30			V		
Zero gate voltage drain current	IDSS	V _{DS} =24V,V _{GS} = 0V			1	μA		
Gate-body leakage current	Igss	V _{GS} =±12V, V _{DS} = 0V			100	nA		
Gate threshold voltage (note 3)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	0.6	1	1.4	V		
Drain-source on-resistance (note 3)		V _{GS} =10V, I _D =4A		45	55	mΩ		
	RDS(on)	V _{GS} =4.5V, I _D =3A		55	70	mΩ		
		V _{GS} =2.5V, I _D =2A		83	110	mΩ		
Forward transconductance (note 3)	g FS	V _{DS} =15V, I _D =4A		8		S		
Diode forward voltage (note 3)	V _{SD}	I _S =1A, V _{GS} = 0V		8.0	1	V		
DYNAMIC CHARACTERISTICS (note 4	4)							
Input capacitance	C _{iss}	V _{DS} =15V,V _{GS} =0V,f =1MHz		390		pF		
Output capacitance	Coss			54.5		pF		
Reverse transfer capacitance	C _{rss}			41		Pf		
Gate resistance	Rg	V _{DS} =0V,V _{GS} =0V,f =1MHz		3		Ω		
SWITCHING CHARACTERISTICS (note 4)								
Turn-on delay time	td(on)			3.3		ns		
Turn-on rise time	tr	V_{GS} =10V, V_{DS} =15V, R_L =3.75 Ω , R_{GEN} =6 Ω		1		ns		
Turn-off delay time	td(off)			21.7		ns		
Turn-off fall time	tf			2.1		ns		
Total gate charge	Qg			4.34		nC		
Gate-source Charge	Q _{gs}	V _{DS} =15V,V _{GS} =4.5V,I _D =4A		0.6		nC		
Gate-drain Charge	Q_{gd}			1.38		nC		
Body diode reverse recovery time	t.	1 = 4 A d1/dt= 100 A /···		1.2		ns		
Body diode reverse recovery charge	Q _{rr}	- I _F =4A,dI/dt=100A/μs		6.3		nC		

Notes:

- ${\bf 1.}\ Repetitive\ rating\ :\ Pulse\ width\ limited\ by\ junction\ temperature.$
- 2. Surface mounted on FR4 board , t≤10s.
- 3. Pulse Test : Pulse Width≤80µs, Duty Cycle≤0.5%.
- 4. Guaranteed by design, not subject to producting.