



**CHENMKO ENTERPRISE CO.,LTD**

**CHDTC125TUPT**

*Lead free devices*

**SURFACE MOUNT  
NPN Digital Silicon Transistor**

VOLTAGE 50 Volts CURRENT 100 mAmpere

**APPLICATION**

\* Switching circuit, Inverter, Interface circuit, Driver circuit.

**FEATURE**

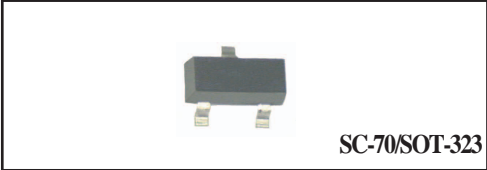
- \* Small surface mounting type. (SC-70/SOT-323)
- \* High current gain.
- \* Suitable for high packing density.
- \* Low collector-emitter saturation.
- \* High saturation current capability.
- \* Internal isolated NPN transistors in one package.
- \* Built in single resistor(R1=200kΩ, Typ. )

**CONSTRUCTION**

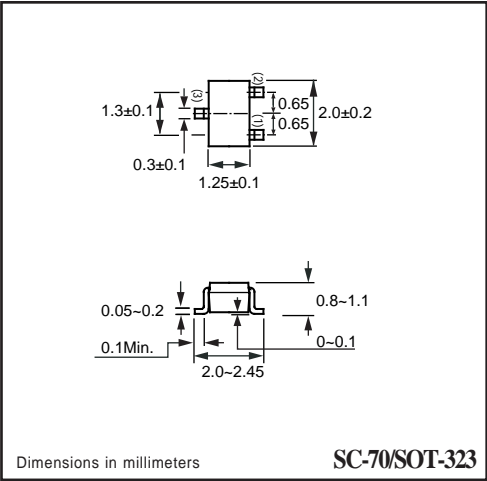
\* One NPN transistors and bias of thin-film resistors in one package.

**MARKING**

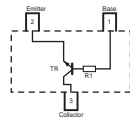
TUF



SC-70/SOT-323



**CIRCUIT**



**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base voltage		50	V
V <sub>CEO</sub>	Collector-Emitter voltage		50	V
V <sub>EBO</sub>	Emitter-Base voltage		5	V
I <sub>C(Max.)</sub>	Collector current		100	mA
P <sub>D</sub>	Power dissipation	T <sub>amb</sub> ≤ 25 °C, Note 1	200	mW
T <sub>STG</sub>	Storage temperature		-55 +150	°C
T <sub>J</sub>	Junction temperature		-55 +150	°C
R <sub>θJ-S</sub>	Thermal resistance , Note 1	junction - soldering point	140	°C/W

**Note**

1. Transistor mounted on an FR4 printed-circuit board.

## RATING CHARACTERISTIC ( CHDTC125TUPT )

### CHARACTERISTICS

$T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
BV <sub>CB0</sub>	Collector-base breakdown voltage	I <sub>C</sub> =50 $\mu$ A	50	–	–	V
BV <sub>CEO</sub>	Collector-emitter breakdown voltage	I <sub>C</sub> =1.0mA	50	–	–	V
BV <sub>EB0</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =50 $\mu$ A	5.0	–	–	V
I <sub>CB0</sub>	Collector cutoff current	V <sub>CB</sub> =50V	–	–	0.5	$\mu$ A
I <sub>EB0</sub>	Emitter cutoff current	V <sub>EB</sub> =4V	–	–	0.5	$\mu$ A
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>C</sub> /I <sub>B</sub> =0.5mA/0.05mA	–	–	0.3	V
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =1mA; V <sub>CE</sub> =5.0V	100	250	600	
R <sub>1</sub>	Input resistor		140	200	260	K $\Omega$
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =5mA, V <sub>CE</sub> =10.0V f=100MHz	–	250	–	MHz

### Note

1. Pulse test:  $t_p \leq 300\mu\text{s}$ ;  $\delta \leq 0.02$ .