TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

# **TLP180**

Telephone Use Equipment
Programmable Controllers
AC / DC-Input Module
Telecommunication

The TOSHIBA mini flat coupler TLP180 is a small outline coupler, suitable for surface mount assembly.

TLP180 consist of a photo transistor, optically coupled to a gallium arsenide infrared emitting diode connected inverse parallel, and can operate directly by AC input current.

- Collector-emitter voltage: 80 V (min)
- Current transfer ratio: 50% (min)
  Rank GB: 100% (min)
- Isolation voltage: 3750 Vrms (min)
- UL recognized: UL1577, file No. E67349
- BSI approved: BS EN60065:2002, certificate no.8285
   BS EN60950-1:2002, certificate no.8286

# Unit in mm 3.6 ± 0.2 3.6 ± 0.2 3.6 ± 0.2 7.0 ± 0.4 11-4C1 TOSH)BA 11-4C1

Weight: 0.09 g (typ.)

### **Current Transfer Ratio**

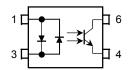
Classi- fication(*1)		insfer Ratio = 5 V, Ta = 25°C Max	Marking Of Classification
Standard	50	600	Blank, YE, GR, BL, GB
Rank Y	50	150	YE
Rank GR	100	300	GR ((//
Rank BL	200	600	BL
Rank GB	100	600	GB

\*The product with the Rank Y and BL are limited in production.
For details, please contact your nearest Toshiba sales representative.

### (\*1): Ex. rank GB: TLP180 (GB)

(Note) Application type name for certification test, please use standard product type name, i.e. TLP180(GB): TLP180

# Pin Configuration (top view)



- 1: Anode, Cathode
- 3: Cathode, Anode
- 4: Emitter
- 6: Collector

### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	I <sub>F(RMS)</sub>	±50	mA
Ω	Forward current detating (Ta≥53°C)	ΔI <sub>F</sub> / °C	-0.7	mA / °C
LED	Pulse forward current (Note 1)	I <sub>FP</sub>	±1 <	Α
	Junction temperature	Tj	125	°C
	Collector-emitter voltage	$V_{CEO}$	80	$\bigvee \bigvee$
	Emitter-collector voltage	V <sub>ECO</sub>	7	V
Detector	Collector current	IC	50	)) mA
Dete	Power dissipation	PC	150	mW
	Power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> / °C	1.5	mW / °C
	Junction temperature	Tj	125	°Ç-
Stor	rage temperature range	T <sub>stg</sub>	-55 to 125	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\
Оре	erating temperature range	T <sub>opr</sub>	_55 to 100	°C
Lea	d soldering temperature (10 s)	T <sub>sol</sub>	260 🔷	C
Tota	al package power dissipation	Pf	200	mW
Tota	al package power dissipation derating (Ta ≥ 25°C)	APT 1°C	-2.0	mW / °C
Isolation voltage (AC,1 min., R.H. ≤ 60%) (Note 2)		BVS	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width ≤ 100 µs,f=100 Hz

Note 2: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

# **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	VCC	_	5	48	V
Forward current	I <sub>F(RMS)</sub>	_	16	20	mA
Collector current	IC	_	1	10	mA
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

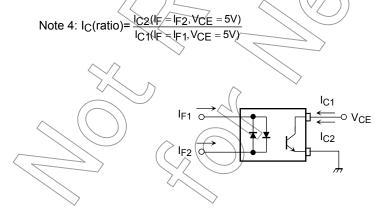
## **Electrical Characteristics (Ta = 25°C)**

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
LED	Forward voltage	$V_{F}$	I <sub>F</sub> = ±10 mA	1.0	1.15	1.3	V
۳	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	60	_	pF
	Collector–emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	_	_	V
_	Emitter–collector breakdown voltage	V <sub>(BR)</sub> ECO	I <sub>E</sub> = 0.1 mA	7	)>	_	V
Detector	Collector dark current	loso	V <sub>CE</sub> = 48 V (ambient light below 1000 (x) (Note 3)		0.01 (2)	0.1 (10)	μΑ
	Collector dark current	ICEO	V <sub>CE</sub> = 48 V (ambient light Ta = 85°C below 1000 (x) (Note 3)	)	2 (4)	50 (50)	μΑ
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF

Note 3: Please use standard electric lamp to light up the device's marking surface.

# **Coupled Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Current transfer ratio	I <sub>C</sub> / I <sub>F</sub>	$I_F = \pm 5$ mA, $V_{CE} = 5$ V	_50	_	600	%
	IC/IF	Rank GB	100	_	600	70
Saturated CTR	lo /le / S	1F = ±1 mA, V <sub>CE</sub> = 0.4 V	/ —	60		%
	IC / IF (sat)	Rank GB	30	_	-	/0
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = ±8 mA	_	_	0.4	
Collector-emitter saturation voltage	VCE (sat)	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = ±1 mA	_	0.2		V
(		Rank GB	_	_	0.4	
Off-state collector current	lc(off)	$V_F = \pm 0.7 V$ , $V_{CE} = 48 V$	_	1	10	μΑ
CTR symmetry	C (ratio)	$I_C (I_F = 5 \text{ mA}) \times I_C (I_F = 5 \text{ mA})$ (Note 4)	0.33	1	3	-



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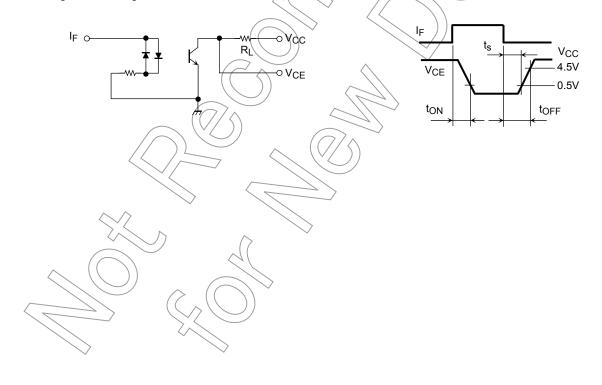
# Isolation Characteristics (Ta = 25°C)

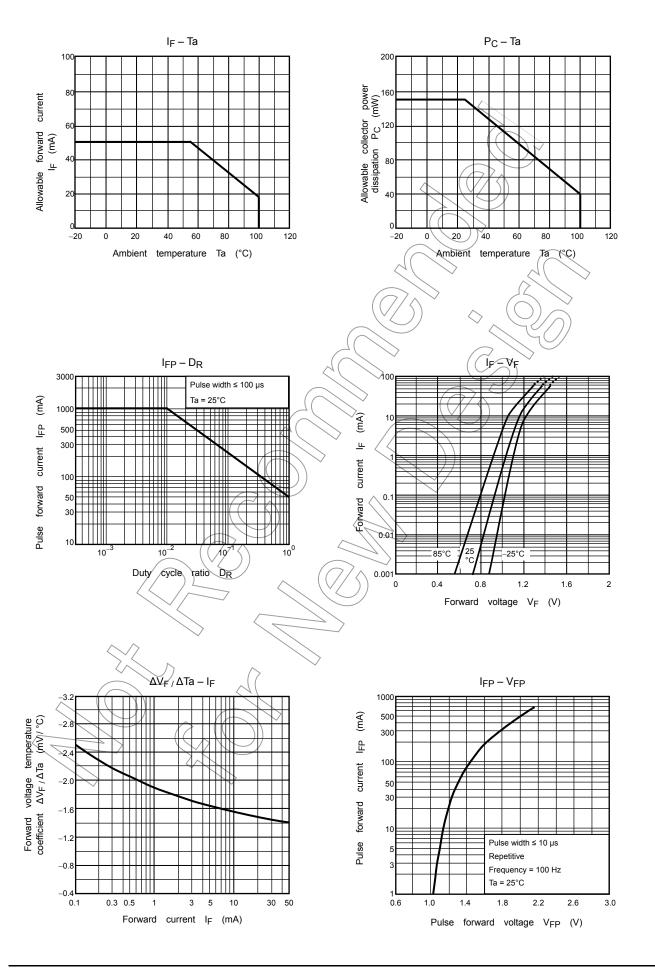
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0 V, f = 1 MHz	_	8.0	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVS	AC, 1 minute	3750	_	_	V
		AC, 1 second, in oil		10000	_	V <sub>rms</sub>
		DC, 1 minute, in oil	(F	10000	_	V <sub>dc</sub>

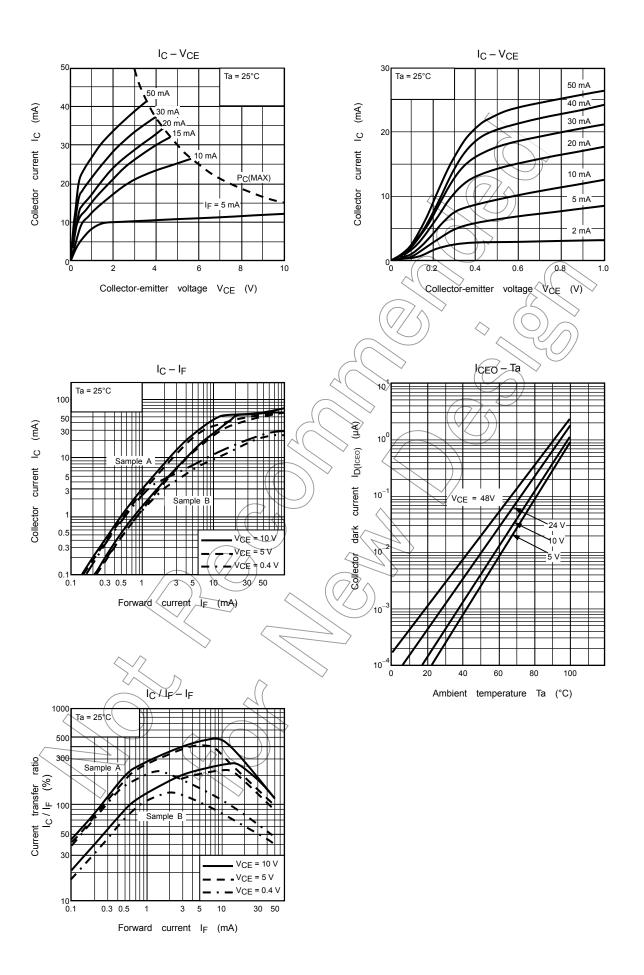
# **Switching Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>	4/ >	_	1/2	$\rightarrow$	
Fall time	t <sub>f</sub>	$V_{CC} = 10 \text{ V, IC} = 2 \text{ mA}$ $R_L = 100 \Omega$	-/-	3/	<u> </u>	μs
Turn-on time	t <sub>ON</sub>	$R_L = 100 \Omega$	4(	))3	_	μδ
Turn-off time	toff			3/	_	
Turn-on time	t <sub>ON</sub>		7 (	> 2	_	
Storage time	ts	$R_{L} = 1.9 \text{ k}\Omega$ (Fig. 1) $V_{CC} = 5 \text{ V}, I_F = \pm 16 \text{ mA}$	<del>/</del> <del>)</del>	25	_	μs
Turn-off time	toff			40	_	

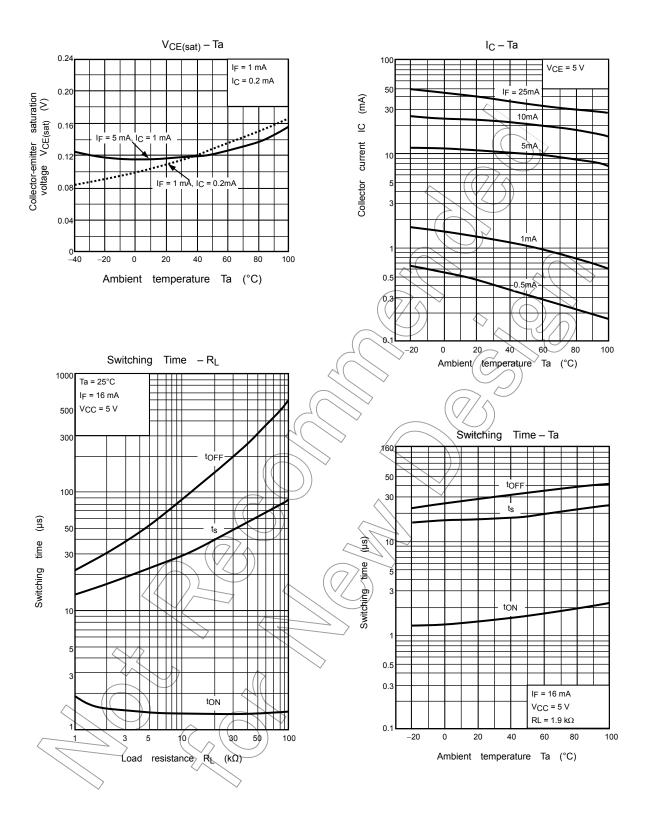
Fig. 1: Switching time test circuit







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