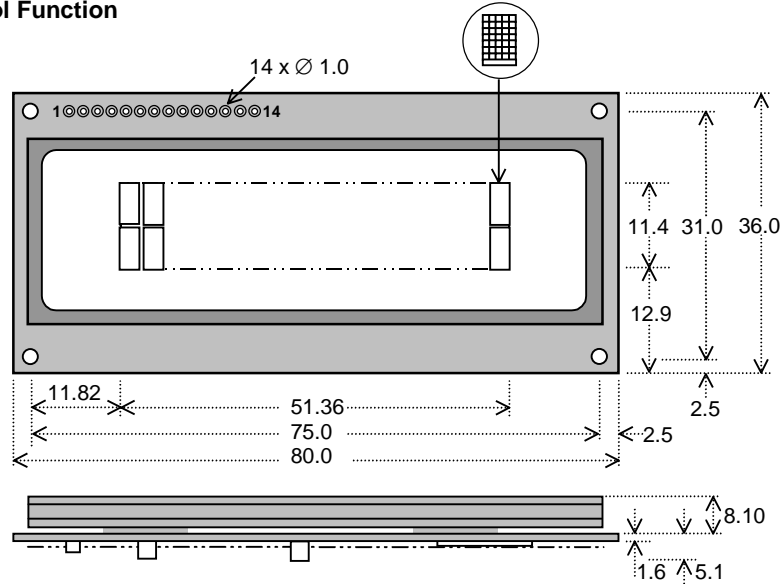


5X7 Dot Character VFD Module

CU16025ECPB-W6J

- ❑ 2 X 16 Characters 5mm High
- ❑ LCD Compatible Design
- ❑ Operating Temp -40°C to +85°C
- ❑ Single 5V Supply with Power Save Mode
- ❑ High Brightness Blue Green Display
- ❑ Selectable 4/8 bit M68/i80 Interface
- ❑ ASCII + Extended Character Font
- ❑ 8 User Definable Character RAM
- ❑ 4 Level Brightness Control Function

The module includes the Vacuum Fluorescent Display glass, driver and micro-controller ICs with refresh RAM, character generator and interface logic. The high speed 8 bit parallel interface is 5V CMOS compatible suitable for connection to a host CPU bus which can be set to M68 or i80 series interface by a solder link on the module. Brightness control and power down functions are provided. A full data sheet is available.



Dimensions in mm & subject to tolerances. Mounting holes 2.5mm dia.

ELECTRICAL SPECIFICATION

Parameter	Symbol	Value	Condition
Power Supply Voltage	Vcc	5.0VDC +/- 5%	GND=0V
Power Supply Current	Icc	150mADC typ.	Vcc=5V
Logic High Input	V _{IH}	2.0VDC min.	Vcc=5V
Logic Low Input	V _{IL}	0.8VDC max.	Vcc=5V
Logic High Output	V _{OH}	Vcc-0.4VDC min.	I _{OH} = -1.6mA
Logic Low Output	V _{OL}	0.4VDC max.	I _{OH} = 1.6mA

The power on rise time should be less than 50ms. The inrush current at power on can be 2 x Icc. The Icc current is 10mA maximum while in power down mode.

OPTICAL and ENVIRONMENTAL SPECIFICATIONS

Parameter	Value
Character Size/Pitch (XxY mm)	2.46 x 4.76/3.26 x 6.01
Dot Size/Pitch (XxY mm)	0.38 x 0.56/0.52 x 0.7
Luminance	700 cd/m ² (204 fL) Typ.
Colour of Illumination	Blue-Green (Filter for more colours)
Operating Temperature	-40°C to +85°C
Storage Temperature	-50°C to +85°C
Operating Humidity (non condensing)	20 to 80% RH @ 25°C

SOFTWARE COMMANDS

Instruction	R/W	RS	D0-D7
Clear Display	L	L	01H
Cursor Return Home	L	L	02H-03H
Entry Mode Set	L	L	04H-07H
Display ON/OFF	L	L	08H-0FH
Cursor/Display Shift	L	L	10H-1FH
Function Set	L	L	20H-3FH
Brightness Set	L	H	00H-03H
Set CG RAM Addr.	L	L	40H-7FH
Set DD RAM Addr.	L	L	80H-E7H
Read BUSY/Addr.	H	L	00H-FFH
Write Data to RAM	L	H	00H-FFH
Read Data from RAM	H	H	00H-FFH

PIN CONNECTIONS

Pin	Sig	Pin	Sig
1	GND	2	VCC
3	(Fnc)	4	RS
5	R/W #	6	E #
7	DB0	8	DB1
9	DB2	10	DB3
11	DB4	12	DB5
13	DB6	14	DB7

TIMING PARAMETERS (min)

(E)nable Cycle Time	1000ns
(E)nable Pulse Width	450ns
Hold after (E)nable	10ns

CHARACTER FONT

h _E x	00	10	20	30	40	50	60	70	80	90	A0	B0	C0	D0	E0	F0
00		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E
01		!	"	#	\$	%	&	'	()	*	+	,	-	.	:
02		@	A	B	C	D	E	F	G	H	I	J	K	L	M	N
03		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E
04		\$	%	&	'	()	*	+	,	-	.	:	;	<	>
05		%	^	_	0	1	2	3	4	5	6	7	8	9	A	B
06		&	'	()	*	+	,	-	.	:	;	<	>	?	@
07		'	()	*	+	,	-	.	:	;	<	>	?	@	A
08		()	*	+	,	-	.	:	;	<	>	?	@	A	B
09)	*	+	,	-	.	:	;	<	>	?	@	A	B	C
0A		*	+	,	-	.	:	;	<	>	?	@	A	B	C	D
0B		+	,	-	.	:	;	<	>	?	@	A	B	C	D	E
0C		,	-	.	:	;	<	>	?	@	A	B	C	D	E	F
0D		-	.	:	;	<	>	?	@	A	B	C	D	E	F	G
0E		.	:	;	<	>	?	@	A	B	C	D	E	F	G	H
0F		:	<	>	?	@	A	B	C	D	E	F	G	H	I	J

JUMPER LINKS

Interface M68/i80
When jumper link JP2 is soldered, these inputs change to i80 series CPU control lines.
Pin 5= /WR Pin 6 = /RD

Pin 3 (Fnc) Input

This is normally open circuit. If pads JP1.1 and JP1.2 are linked. Pin 3 = /Reset.

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