Rectifier Diode



DS5978 - 1 January 2011 (LN27997)

KEY PARAMETERS

 $\begin{array}{ll} V_{RRM} & 4000V \\ I_{F(AV)} & 1225A \\ I_{FSM} & 25000A \end{array}$

FEATURES

- Double Side Cooling
- High Surge Capability

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD1230F40 DRD1230F36 DRD1230F30	4000 3600 3000	$V_{RSM} = V_{RRM} + 100V$

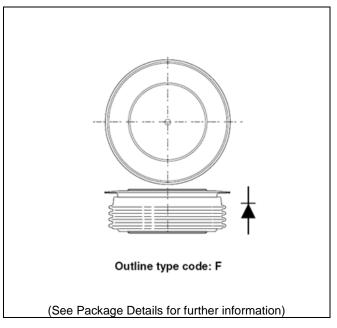


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD1230F36 for a 3600V device



CURRENT RATINGS

$T_{case} = 75$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units		
Double Si	Double Side Cooled					
$I_{F(AV)}$	Mean forward current	Half wave resistive load	1594	А		
I _{F(RMS)}	RMS value	-	2504	Α		
I _F	Continuous (direct) on-state current	-	2295	А		
Single Sid	Single Side Cooled (Anode side)					
$I_{F(AV)}$	Mean forward current	Half wave resistive load	1144	Α		
I _{F(RMS)}	RMS value	-	1797	Α		
I _F	Continuous (direct) on-state current	-	1553	Α		

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	1225	Α			
I _{F(RMS)}	RMS value	-	1923	Α			
I _F	Continuous (direct) on-state current	-	1720	А			
Single Sid	de Cooled (Anode side)						
I _{F(AV)}	Mean forward current	Half wave resistive load	820	Α			
I _{F(RMS)}	RMS value	-	1287	Α			
l _F	Continuous (direct) on-state current	-	1050	Α			



SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	20.0	kA
l ² t	I ² t for fusing	$V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	2.0	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	25.0	kA
l ² t	I ² t for fusing	$V_R = 0$	3.125	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.022	°C/W
		Single side cooled	Anode DC	-	0.038	°C/W
			Cathode DC	-	0.052	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 19.5kN	Double side	-	0.004	°C/W
		(with mounting compound)	Single side	-	0.008	°C/W
T _{vj}	Virtual junction temperature	On-state (conducting)		-	160	°C
		Reverse (blocking)		-	150	°C
T _{stg}	Storage temperature range			-55	175	°C
Fm	Clamping force			18	22	kN

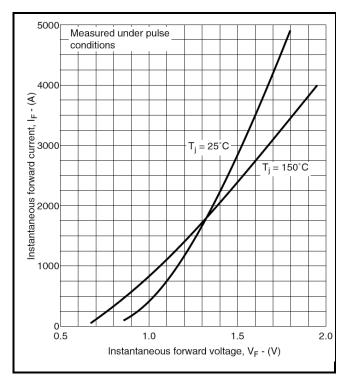
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CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 3400A peak, T _{case} = 25°C	-	1.6	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	75	mA
Qs	Total stored charge	I _F = 2000A, dI _{RR} /dt =3A/μs	-	3500	μC
Irr	Peak reverse recovery current	$T_{case} = 150$ °C, $V_R = 100$ V	-	110	Α
V _{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.82	V
r _T	Slope resistance	At T _{vj} = 150°C	-	0.29	mΩ

CURVES



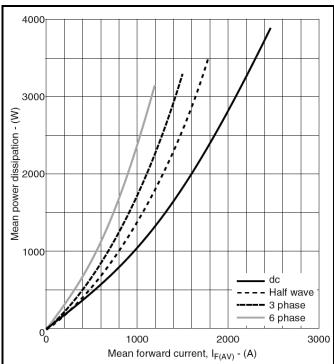


Fig.2 Maximum (limit) on-state characteristics

Fig.3 Dissipation curves

 V_{TM} EQUATION

Where A = 0.658789B = -0.01706

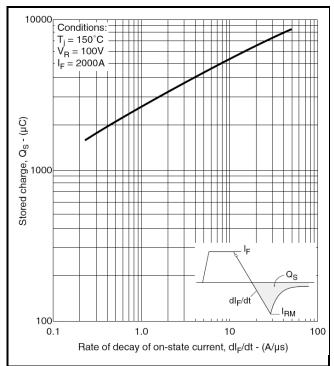
 $V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$

C = 0.000194

D = 0.010358

these values are valid for $T_j = 150$ °C for $I_F 500$ A to 5000A





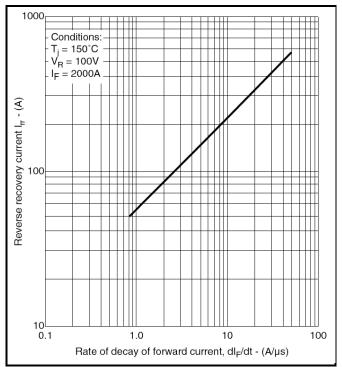


Fig.4 Total stored charge

Fig.5 Maximum reverse recovery current

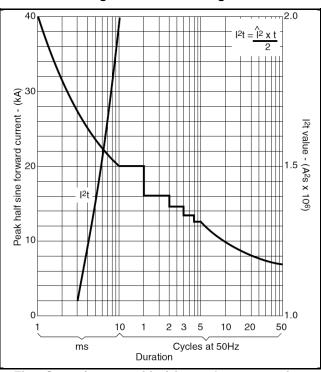


Fig.6 Surge (non-repetitive) forward current vs time (with 50% V_{RRM} at T_{case} 150°C)

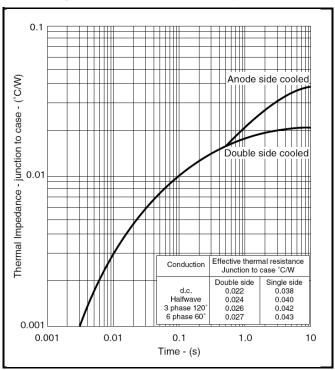
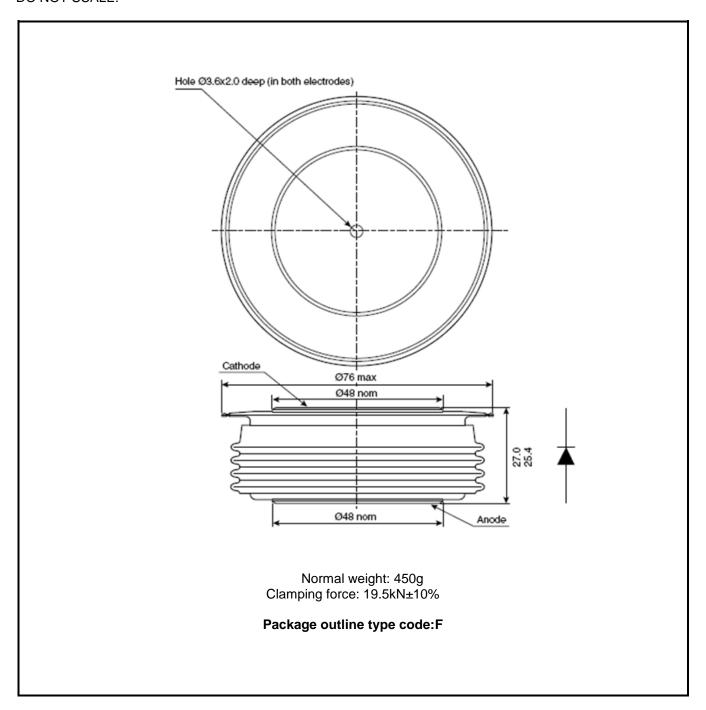


Fig.7 Maximum (limit) transient thermal impedancejunction to case



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.



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