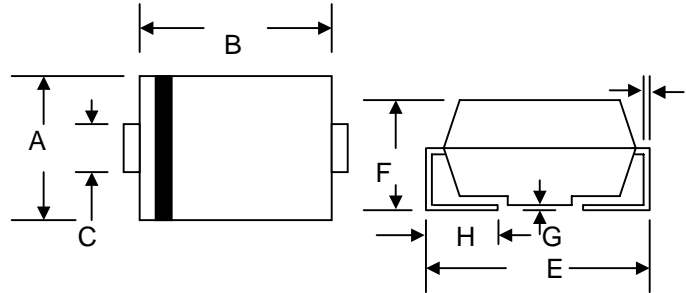


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Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop, High Efficiency
- Surge Overload Rating to 30A Peak
- Low Power Loss
- Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-O



Mechanical Data

- Case: Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.064 grams (approx.)

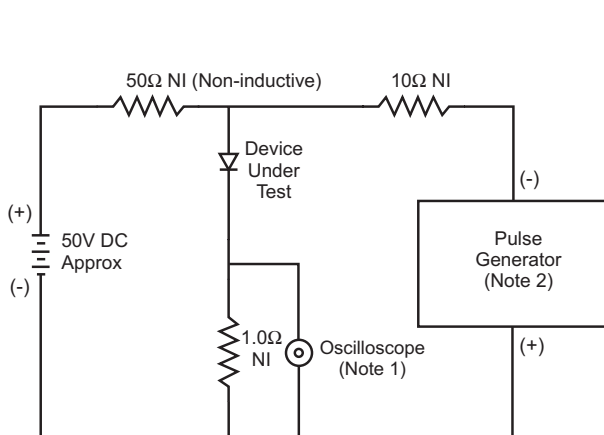
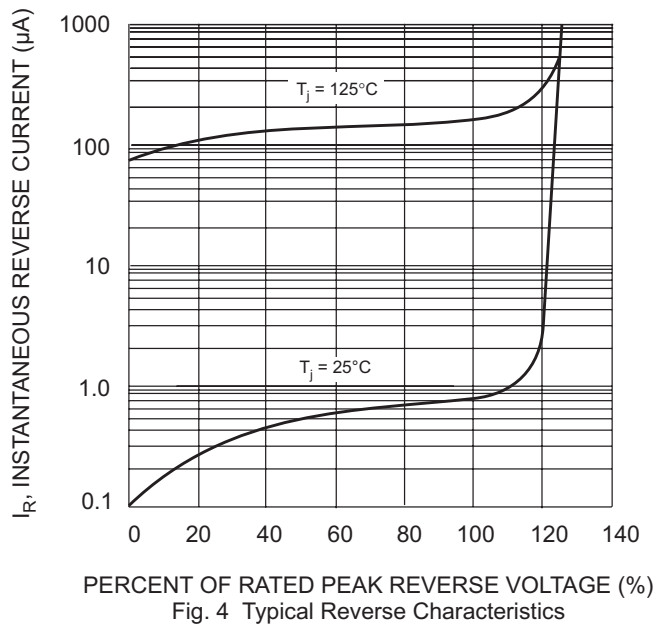
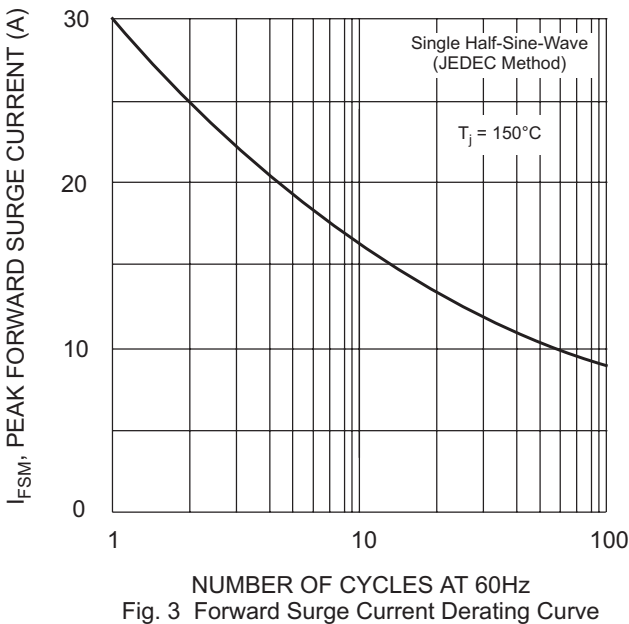
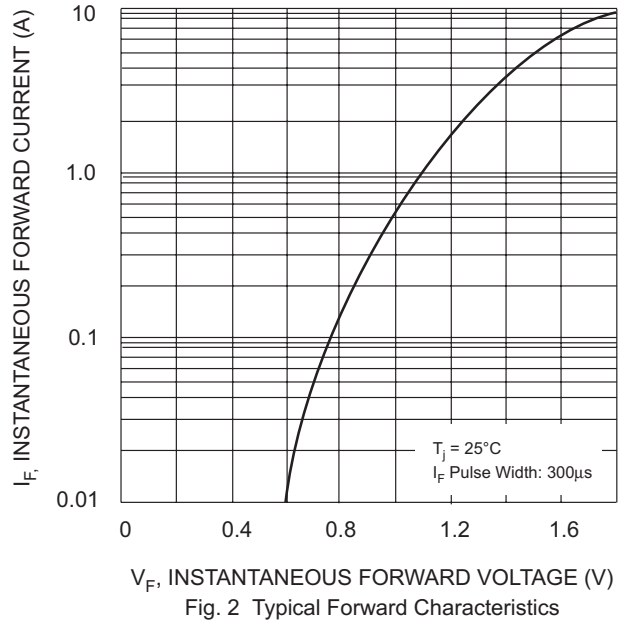
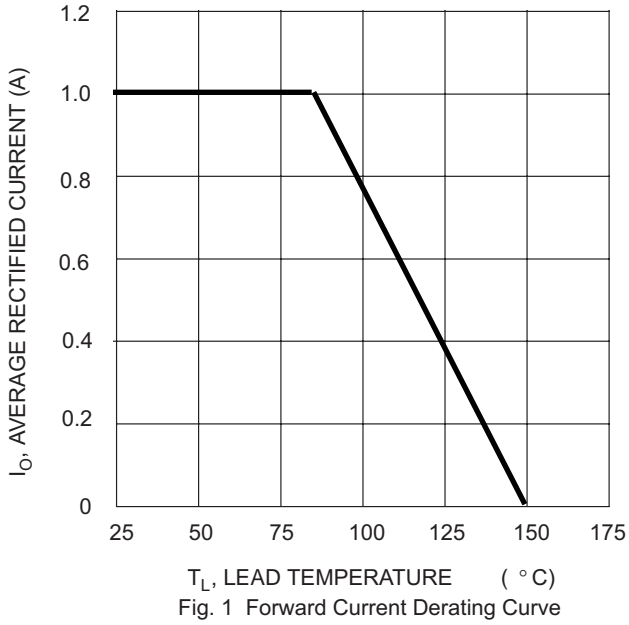
SMA/DO-214AC				
Dim	Min	Max	Min	Max
A	2.50	2.90	0.098	0.114
B	4.00	4.60	0.157	0.181
C	1.40	1.60	0.055	0.063
D	0.152	0.305	0.006	0.012
E	4.80	5.28	0.189	0.208
F	2.00	2.44	0.079	0.096
G	0.051	0.203	0.002	0.008
H	0.76	1.52	0.030	0.060
	In mm		In inch	

Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	RS1A	RS1B	RS1D	RS1G	RS1J	RS1K	RS1M	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Working Peak Reverse Voltage	V_{RWM}								
DC Blocking Voltage	V_R								
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	280	420	560	700	V
Average Rectified Output Current @ $T_L = 90^\circ\text{C}$	I_o	1.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	30							A
Forward Voltage @ $I_F = 1.0\text{A}$	V_{FM}	1.30							V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 125^\circ\text{C}$	I_{RM}	5.0 300							μA
Reverse Recovery Time (Note 1)	t_{rr}	150				250	500		nS
Typical Junction Capacitance (Note 2)	C_j	10							pF
Typical Thermal Resistance (Note 3)	$R_{\theta JL}$	32							K/W
Operating and Storage Temperature Range	T_j, T_{STG}	-50 to +150							$^\circ\text{C}$

Note: 1. Measured with $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $I_{rr} = 0.25\text{A}$,
2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.
3. Mounted on P.C. Board with 8.0mm^2 land area.

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Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

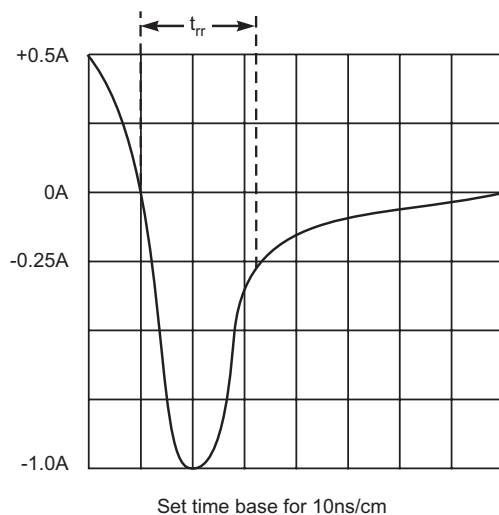


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

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