



BIPOLAR ANALOG INTEGRATED CIRCUIT

μ PC3231GV

GENERAL PURPOSE 5 V 100 MHz AGC AMPLIFIER

DESCRIPTION

The μ PC3231GV is a silicon monolithic IC designed for use as AGC amplifier for digital CATV, cable modem and digital terrestrial systems. This IC consists of gain control amplifier and video amplifier.

The package is 8-pin SSOP (Shrink Small Outline Package) suitable for surface mount.

This IC is manufactured using our 30 GHz $f_{\max}$ UHS0 (Ultra High Speed Process) silicon bipolar process.

This process uses silicon nitride passivation film. This material can protect chip surface from external pollution and prevent corrosion/migration. Thus, this IC has excellent performance, uniformity and reliability.

FEATURES

- Low distortion : $IM_3 = 53.5$ dBc TYP. @ single-ended output, $V_{out} = 105$ dB μ V (0.5 V_{p-p}) /tone
- Low noise figure : NF = 5.0 dB TYP. @ maximum gain
- Wide AGC dynamic range : GCR_{in} = 61 dB TYP. @ input prescribe
- On-chip video amplifier : $V_{out} = 1.0$ V_{p-p} TYP. @ single-ended output
- Supply voltage : $V_{CC} = 5.0$ V TYP.
- Packaged in 8-pin SSOP suitable for surface mounting

APPLICATION

- Digital terrestrial TV/Digital CATV/Cable modem receivers

ORDERING INFORMATION

Part Number	Order Number	Package	Marking	Supplying Form
μ PC3231GV-E1	μ PC3231GV-E1-A	8-pin plastic SSOP (4.45 mm (175)) (Pb-Free)	3231	• Embossed tape 8 mm wide • Pin 1 indicates pull-out direction of tape • Qty 1 kpcs/reel

Remark To order evaluation samples, contact your nearby sales office.

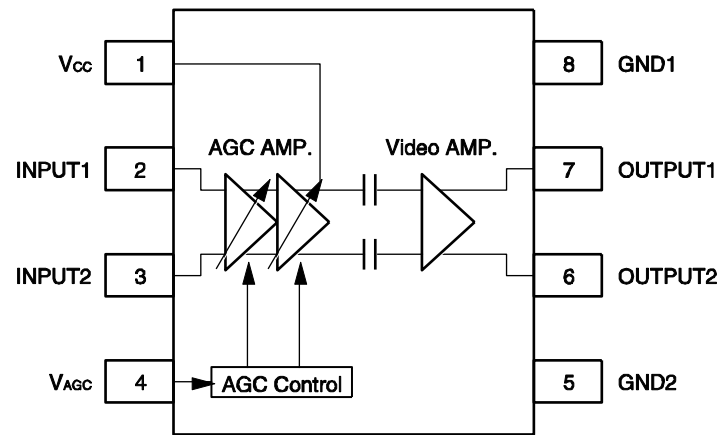
Part number for sample order: μ PC3231GV-A

Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

INTERNAL BLOCK DIAGRAM AND PIN CONNECTIONS

(Top View)

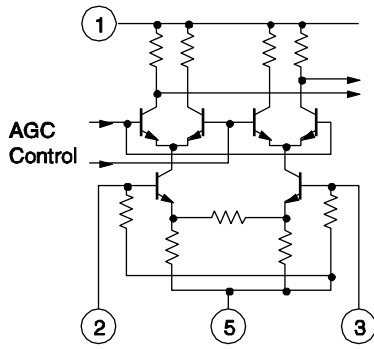
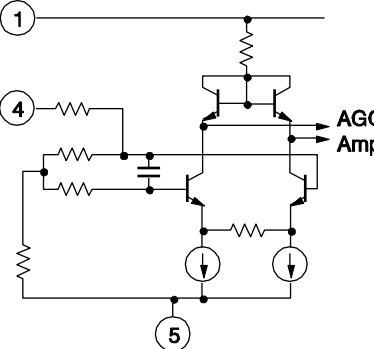
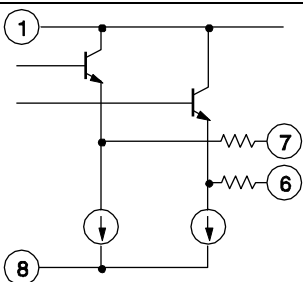


PRODUCT LINE-UP OF 5 V AGC AMPLIFIER

Part Number	I _{CC} (mA)	G _{MAX} (dB)	G _{MIN} (dB)	GCR (dB)	NF (dB)	IM ₃ (dBc)	Package
μ PC3217GV	23	53	0	53	6.5	50 ^{Note1}	8-pin SSOP (4.45 mm (175))
μ PC3218GV	23	63	10	53	3.5	50 ^{Note1}	
μ PC3219GV	36.5	42.5	0	42.5	9.0	58 ^{Note1}	
μ PC3221GV	33	60	10	50	4.2	56 ^{Note1}	
μ PC3231GV	36	65	4	61	5.0	53.5 ^{Note2}	

- Notes 1.** f₁ = 44 MHz, f₂ = 45 MHz, V_{out} = 0.7 V_{p-p}/tone, single-ended output
2. f₁ = 44 MHz, f₂ = 45 MHz, V_{out} = 0.5 V_{p-p}/tone, single-ended output

PIN EXPLANATIONS

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V) ^{Note}	Function and Application	Internal Equivalent Circuit
1	V _{CC}	4.5 to 5.5	—	Power supply pin. This pin should be externally equipped with bypass capacitor to minimize ground impedance.	
2	INPUT1	—	1.32	Signal input pins to AGC amplifier. This pin should be coupled with capacitor for DC cut.	
3	INPUT2	—	1.32		
4	V _{AGC}	0 to V _{CC}	—	Gain control pin. This pin's bias govern the AGC output level. Minimum Gain at V _{AGC} : 0 to 0.1 V Maximum Gain at V _{AGC} : 2.7 to 3.3 V Recommended to use AGC voltage with externally resistor (example: 1 k Ω).	
5	GND2	0	—	Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible.	
6	OUTPUT2	—	1.91	Signal output pins of video amplifier. This pin should be coupled with capacitor for DC cut.	
7	OUTPUT1	—	1.91		
8	GND1	0	—	Ground pin. This pin should be connected to system ground with minimum inductance. Ground pattern on the board should be formed as wide as possible. All ground pins must be connected together with wide ground pattern to decrease impedance difference.	

Note Pin voltage is measured at V_{CC} = 5.0 V.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions	Ratings	Unit
Supply Voltage	V _{CC}	T _A = +25°C	6.0	V
Gain Control Voltage Range	V _{AGC}	T _A = +25°C	0 to V _{CC}	V
Power Dissipation	P _D	T _A = +85°C Note	250	mW
Storage Temperature	T _{stg}		–55 to +150	°C

Note Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

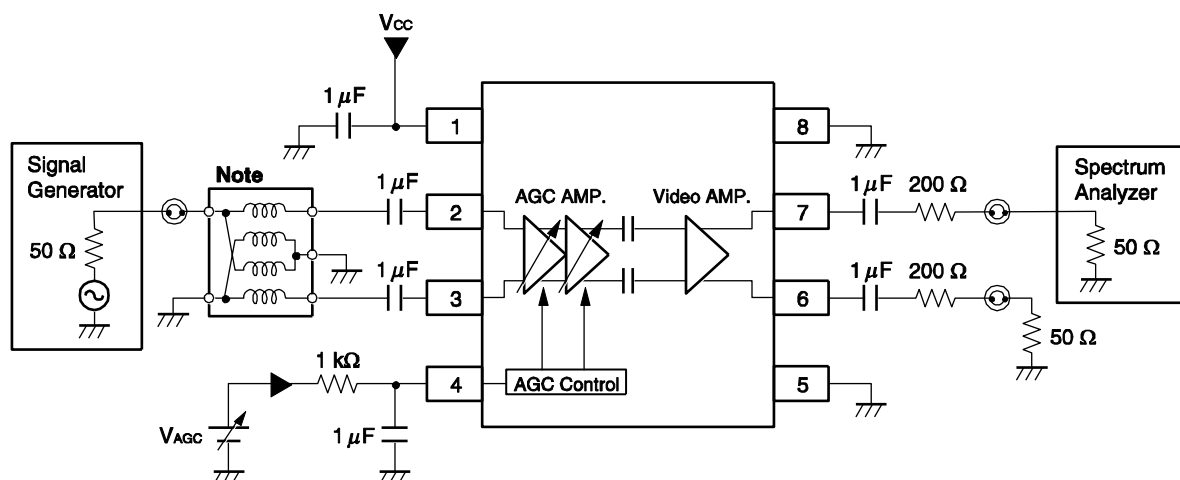
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{CC}		4.5	5.0	5.5	V
Operating Ambient Temperature	T _A	V _{CC} = 4.5 to 5.5 V	–40	+25	+85	°C
Gain Control Voltage Range	V _{AGC}		0	–	3.3	V
Operating Frequency Range	f _{BW}		30	–	90	MHz

ELECTRICAL CHARACTERISTICS(T_A = +25°C, V_{CC} = 5 V, f = 45 MHz, Z_S = 50 Ω , Z_L = 250 Ω , single-ended output)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Circuit Current	I _{CC}	V _{CC} = 5 V, No input signal Note 1	28	36	44	mA
AGC Voltage High Level	V _{AGC (H)}	@ Maximum gain Note 1	2.7	–	3.3	V
AGC Voltage Low Level	V _{AGC (L)}	@ Minimum gain Note 1	0	–	0.1	V
RF Characteristics						
IF Input Voltage Range	f _{IFin}	f _c = –3 dB Note 1	30	–	90	MHz
Maximum Voltage Gain	G _{MAX}	V _{AGC} = 2.7 V, P _{in} = –60 dBm Note 1	62.5	65	67.5	dB
Minimum Voltage Gain	G _{MIN}	V _{AGC} = 0.1 V, P _{in} = –30 dBm Note 1	0	4	7	dB
Gain Control Range (input prescribe)	GCR _{in}	V _{AGC} = 0.1 to 2.7 V Note 1	55.5	61	–	dB
Gain Control Range (output prescribe)	GCR _{out}	V _{out} = 1.0 V _{p-p} Note 1	45	55	–	dB
Output Voltage	V _{out}	P _{in} = –61 to –6 dBm Note 1	–	1.0	–	V _{p-p}
Maximum Output Voltage	V _{oclip}	V _{AGC} = 3.0 V Note 1	2.0	3.3	–	V _{p-p}
Noise Figure	NF	V _{AGC} = 3.0 V Note 2	–	5.0	6.5	dB
3rd Order Intermodulation Distortion	IM ₃	f ₁ = 44 MHz, f ₂ = 45 MHz, P _{in} = –20 dBm/tone, V _{out} = 105 dB μ V (0.5 V _{p-p}) /tone Note 1	50	53.5	–	dBc
Input Impedance	Z _{in}	V _{AGC} = 0 V Note 3	–	1.35//6	–	k Ω //pF

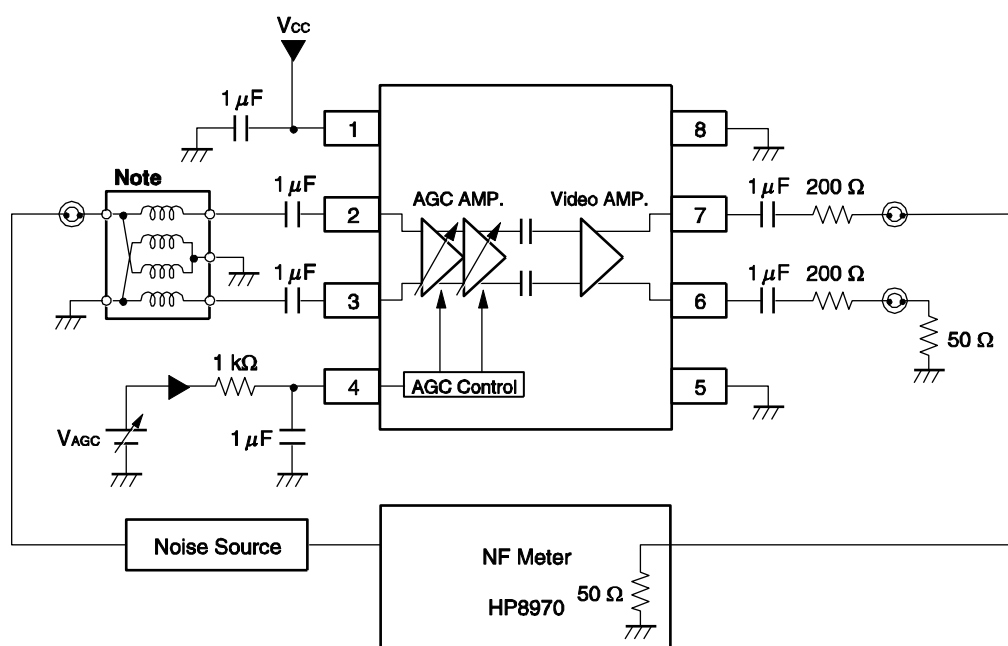
Notes 1. By measurement circuit 1**2.** By measurement circuit 2**3.** By measurement circuit 3

MEASUREMENT CIRCUIT 1



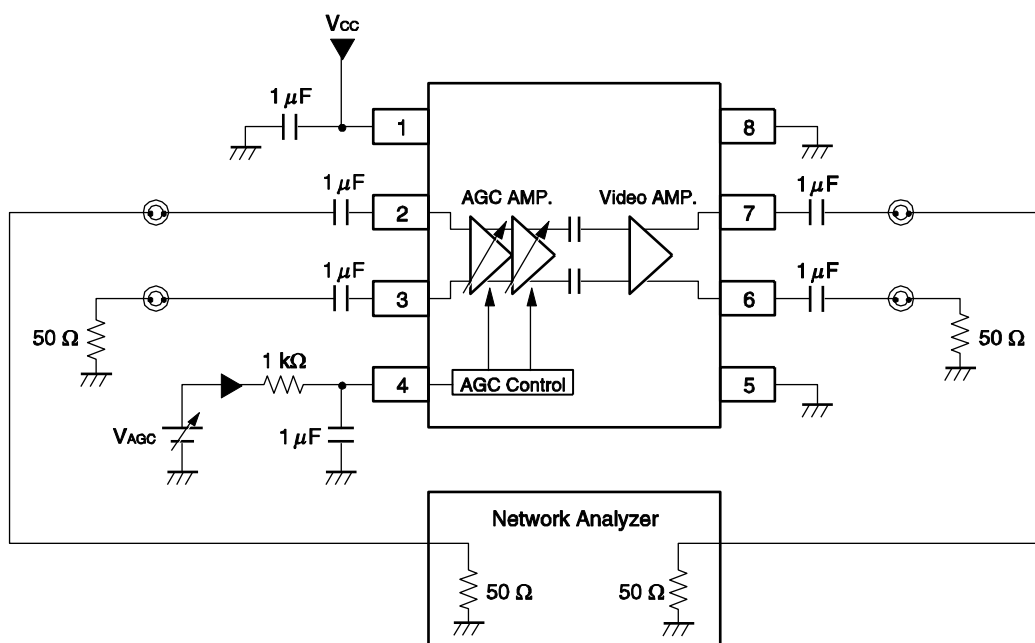
Note Balun Transformer: TOKO 617DB-1674 B4F (Double balanced type)

MEASUREMENT CIRCUIT 2

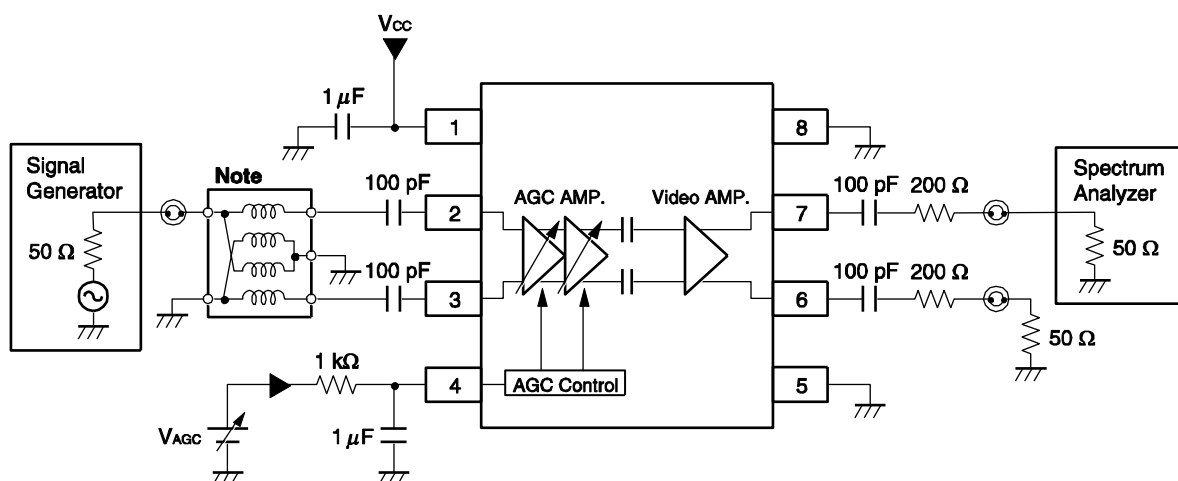


Note Balun Transformer: TOKO 617DB-1674 B4F (Double balanced type)

MEASUREMENT CIRCUIT 3

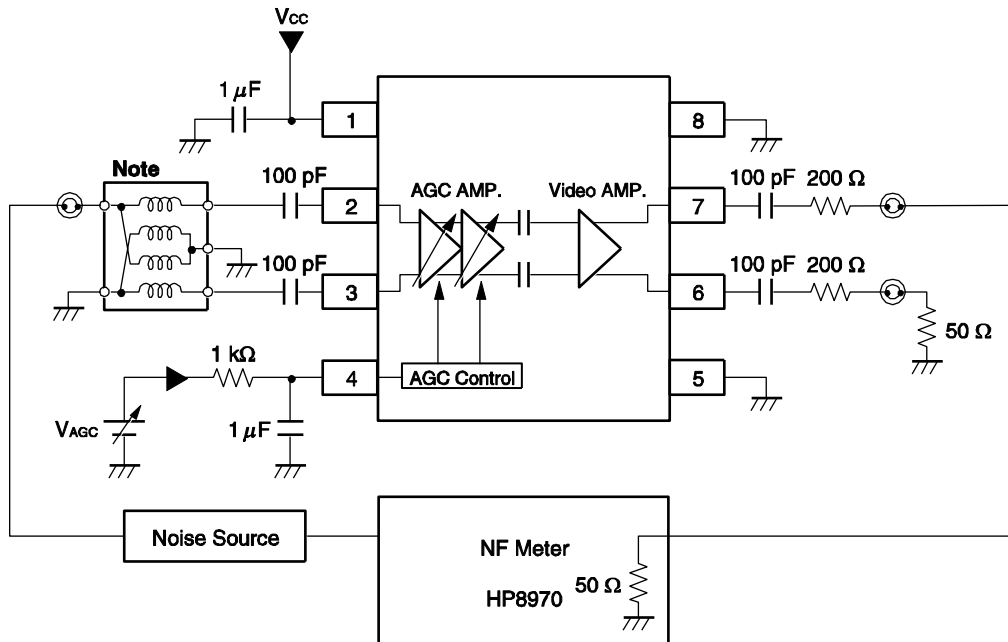


MEASUREMENT CIRCUIT 4 (PRESSURE IMPROVEMENT RECOMMENDATION CIRCUIT)



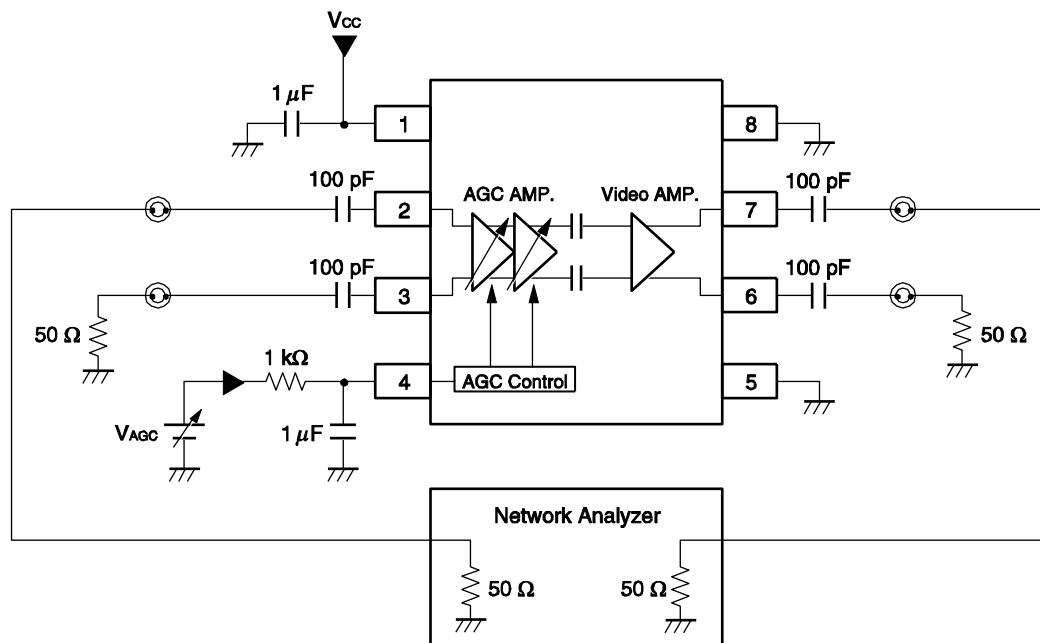
Note Balun Transformer: TOKO 617DB-1674 B4F (Double balanced type)

MEASUREMENT CIRCUIT 5 (PRESSURE IMPROVEMENT RECOMMENDATION CIRCUIT)



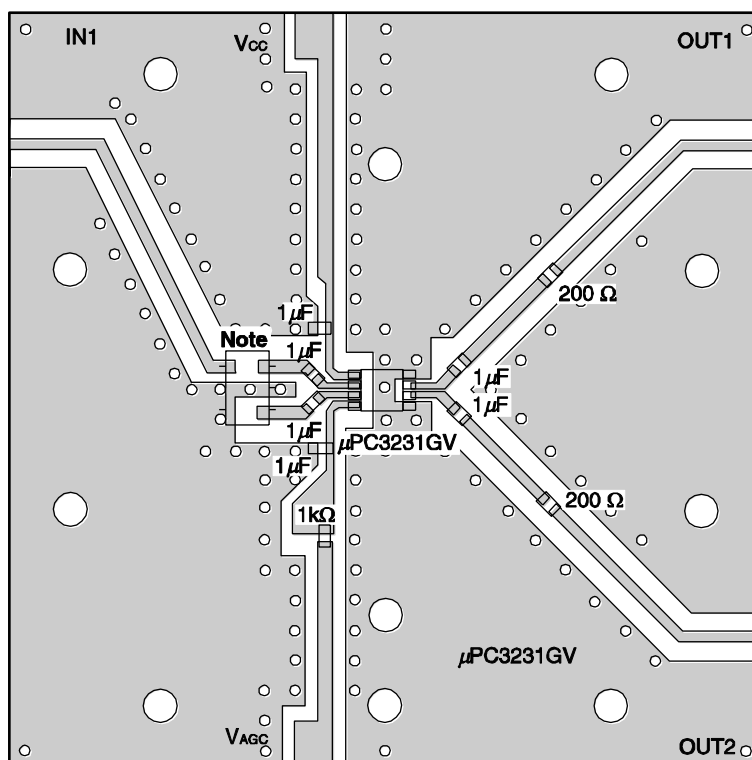
Note Balun Transformer: TOKO 617DB-1674 B4F (Double balanced type)

MEASUREMENT CIRCUIT 6 (PRESSURE IMPROVEMENT RECOMMENDATION CIRCUIT)



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD
(MEASUREMENT CIRCUIT 1)



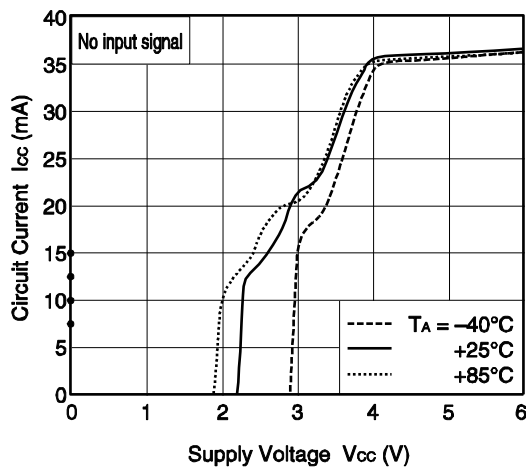
Note Balun Transformer

Remarks

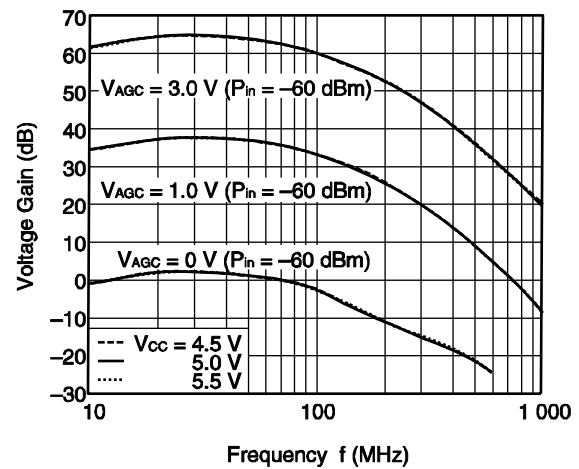
1. Back side: GND pattern
2. Au plated on pattern
3. ○○: Through hole

TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, unless otherwise specified)

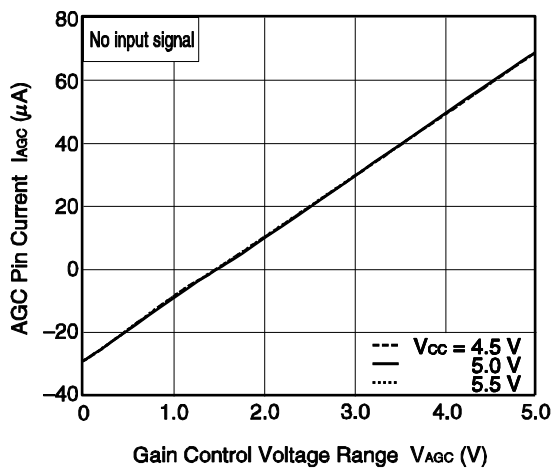
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



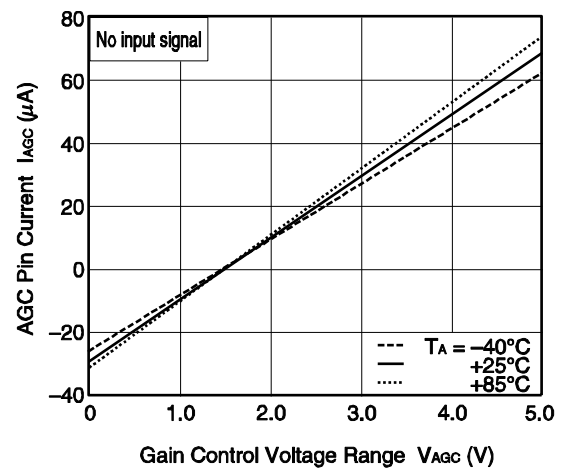
VOLTAGE GAIN vs. FREQUENCY



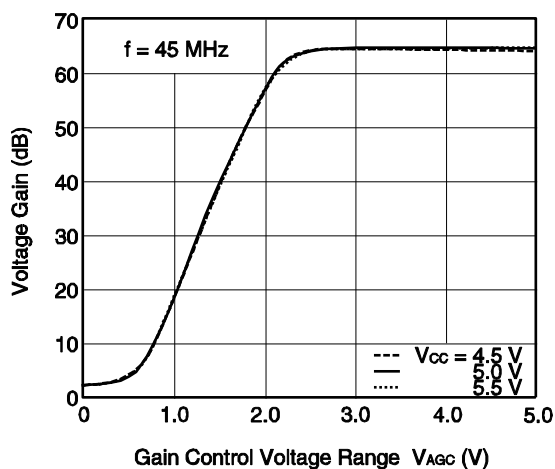
AGC PIN CURRENT vs. GAIN CONTROL VOLTAGE RANGE



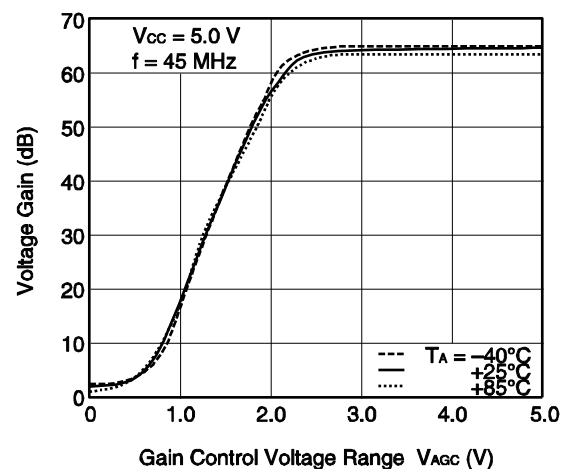
AGC PIN CURRENT vs. GAIN CONTROL VOLTAGE RANGE



VOLTAGE GAIN vs. GAIN CONTROL VOLTAGE RANGE

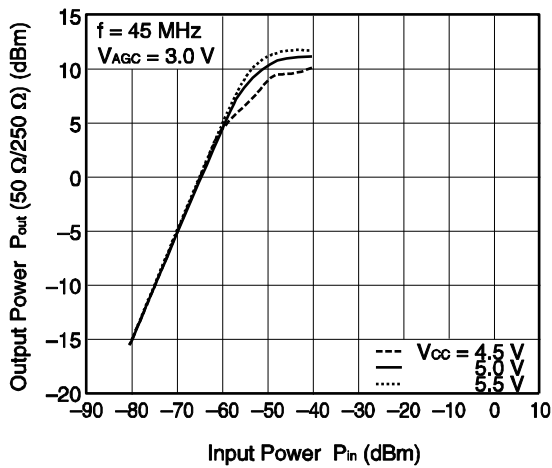


VOLTAGE GAIN vs. GAIN CONTROL VOLTAGE RANGE

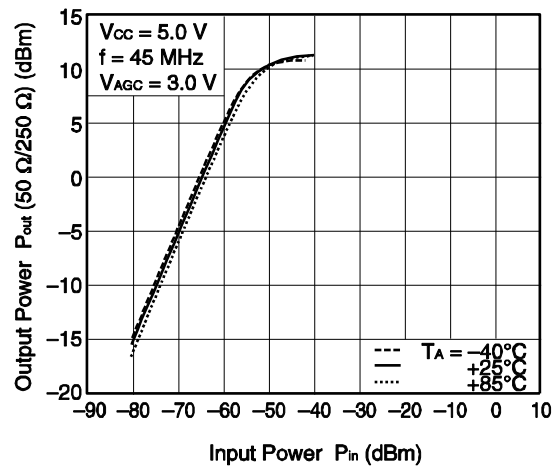


Remark The graphs indicate nominal characteristics.

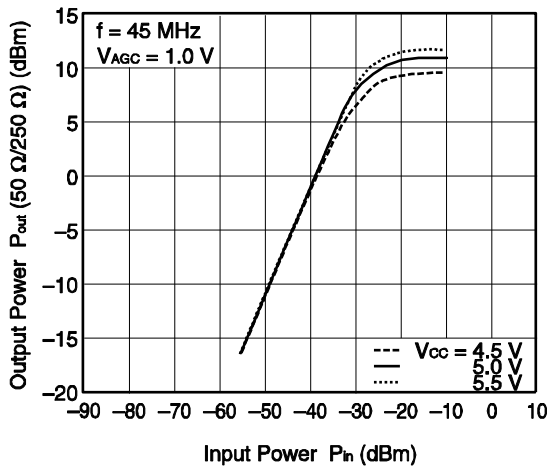
OUTPUT POWER vs. INPUT POWER



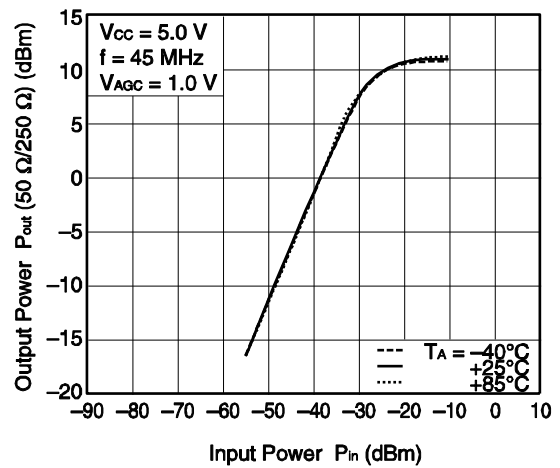
OUTPUT POWER vs. INPUT POWER



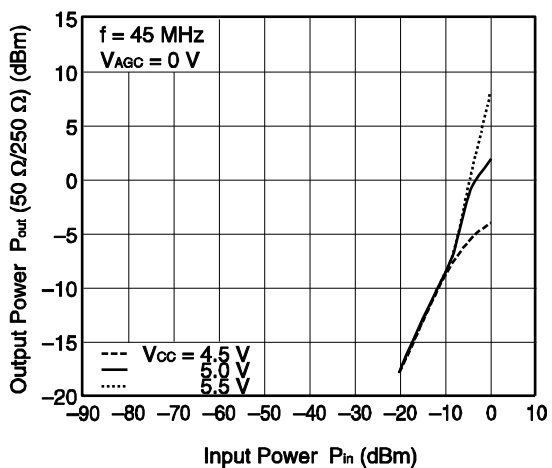
OUTPUT POWER vs. INPUT POWER



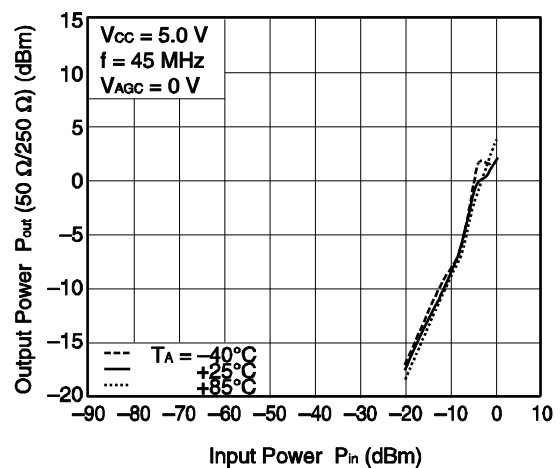
OUTPUT POWER vs. INPUT POWER



OUTPUT POWER vs. INPUT POWER

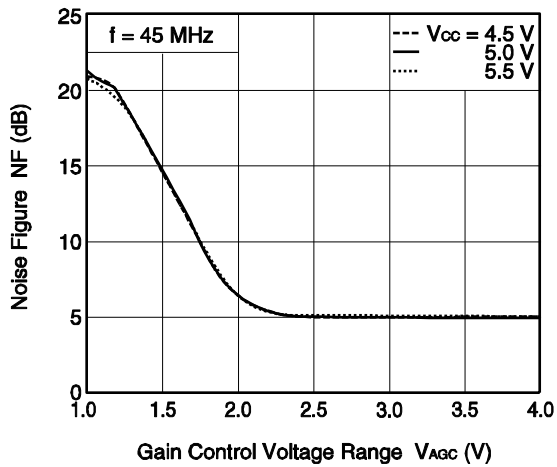


OUTPUT POWER vs. INPUT POWER

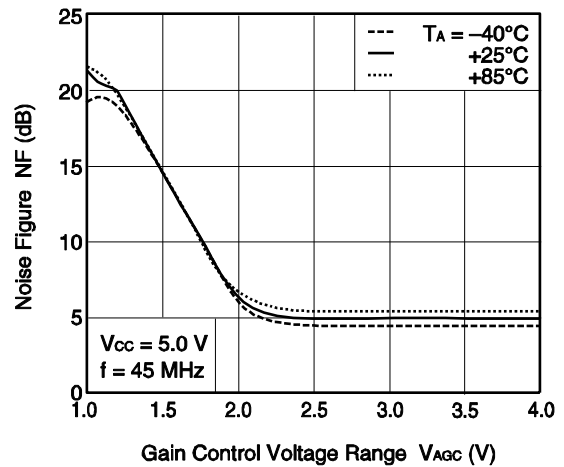


Remark The graphs indicate nominal characteristics.

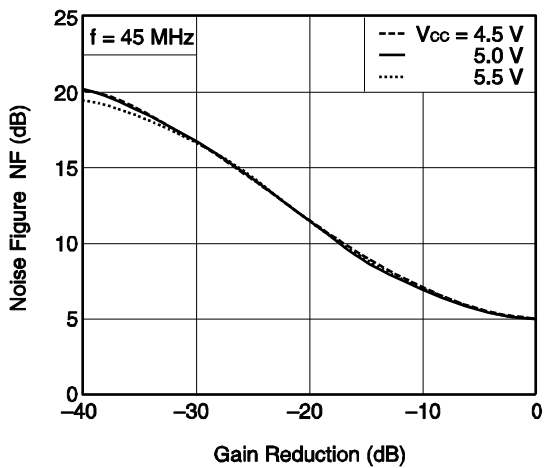
NOISE FIGURE vs.
GAIN CONTROL VOLTAGE RANGE



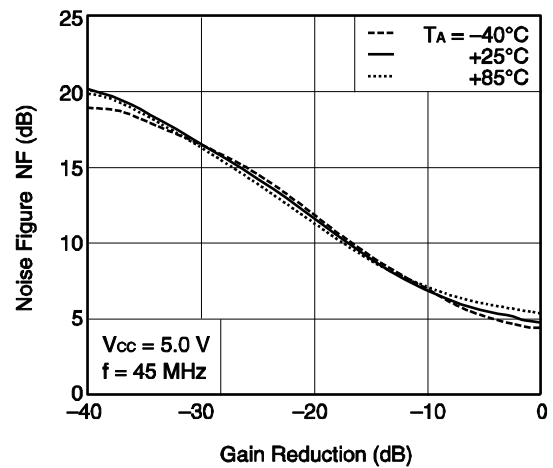
NOISE FIGURE vs.
GAIN CONTROL VOLTAGE RANGE



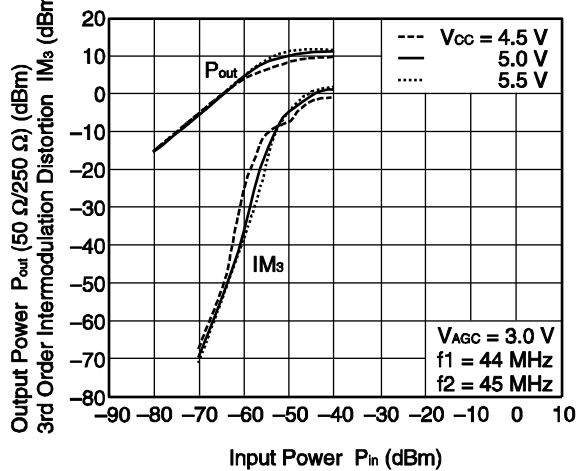
NOISE FIGURE vs. GAIN REDUCTION



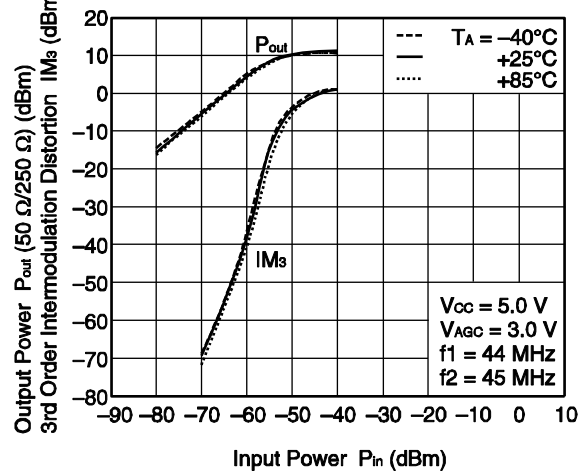
NOISE FIGURE vs. GAIN REDUCTION



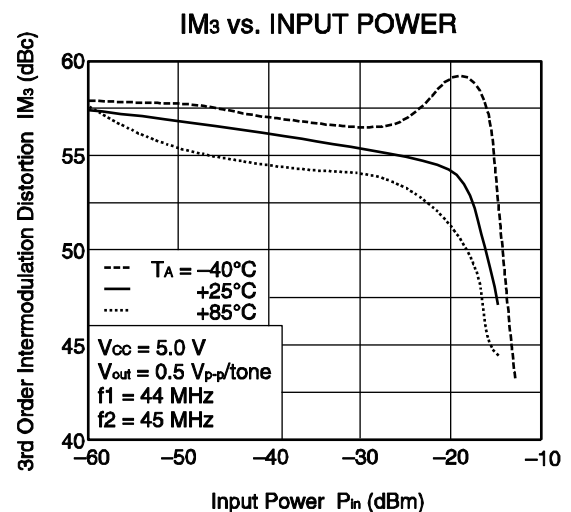
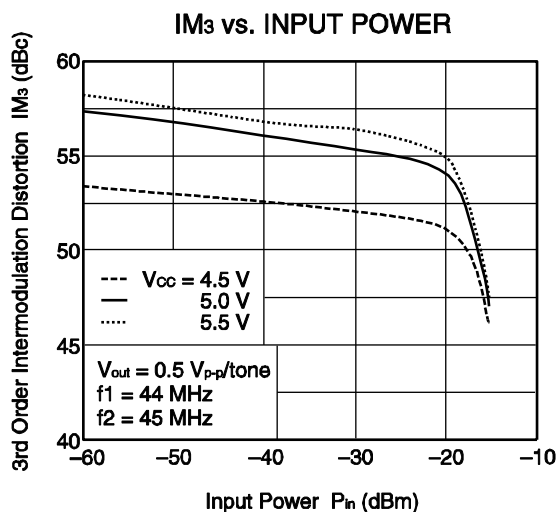
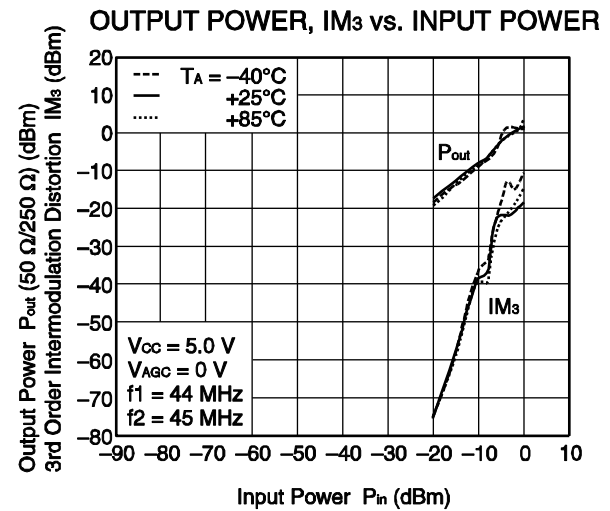
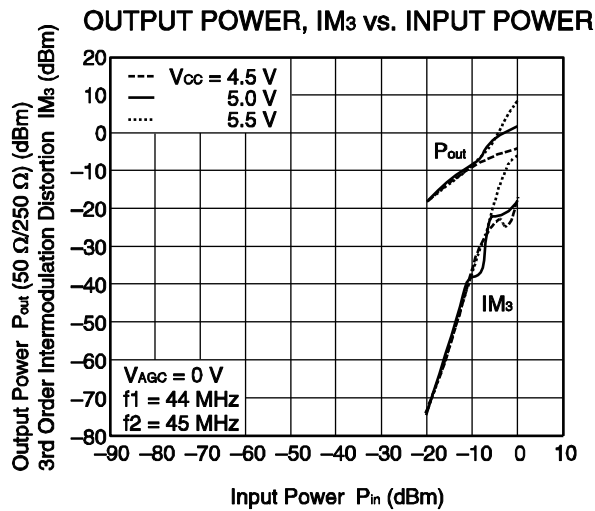
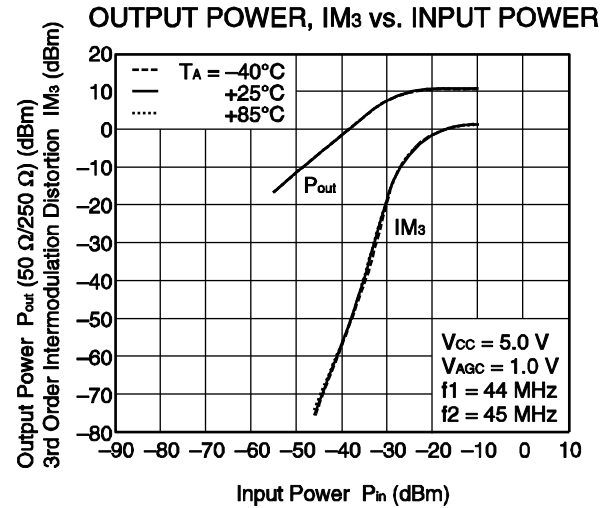
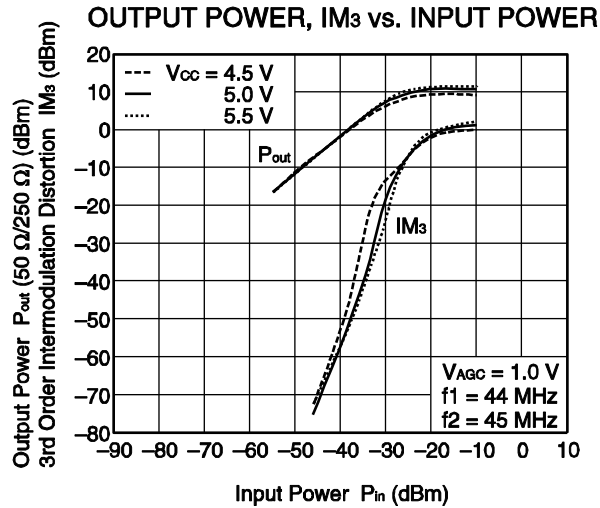
OUTPUT POWER, IM_3 vs. INPUT POWER



OUTPUT POWER, IM_3 vs. INPUT POWER



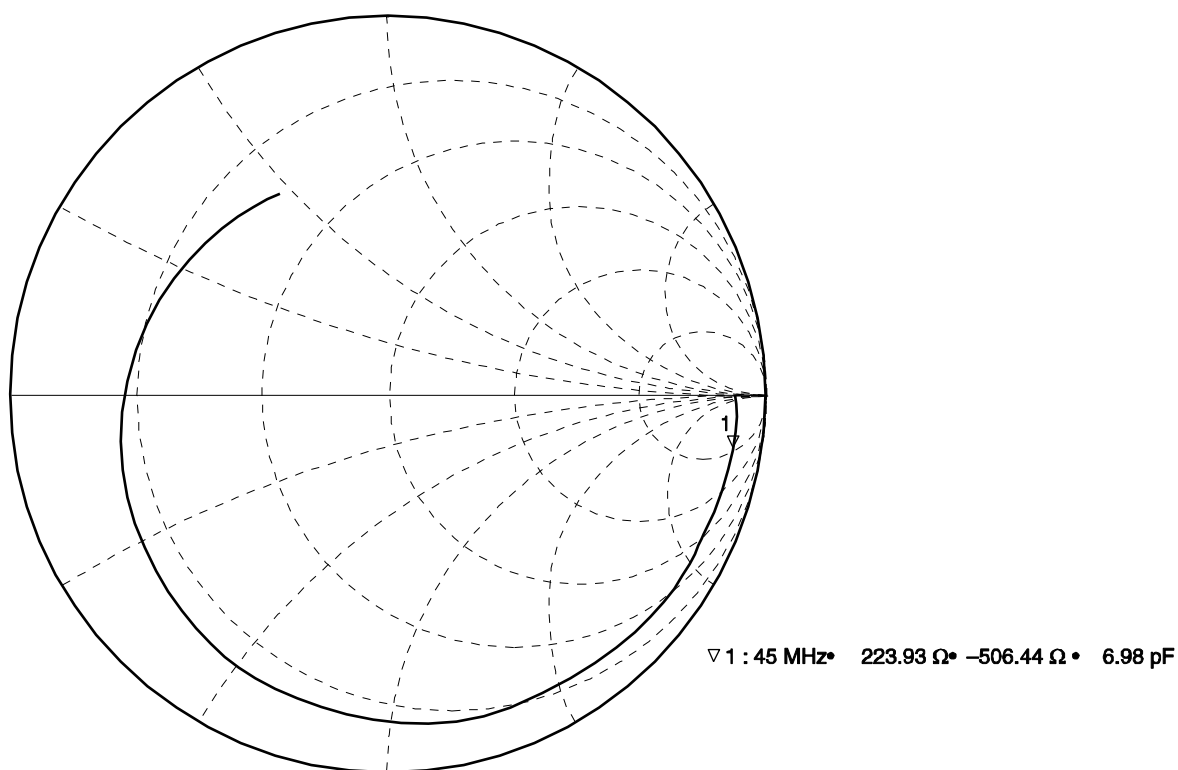
Remark The graphs indicate nominal characteristics.



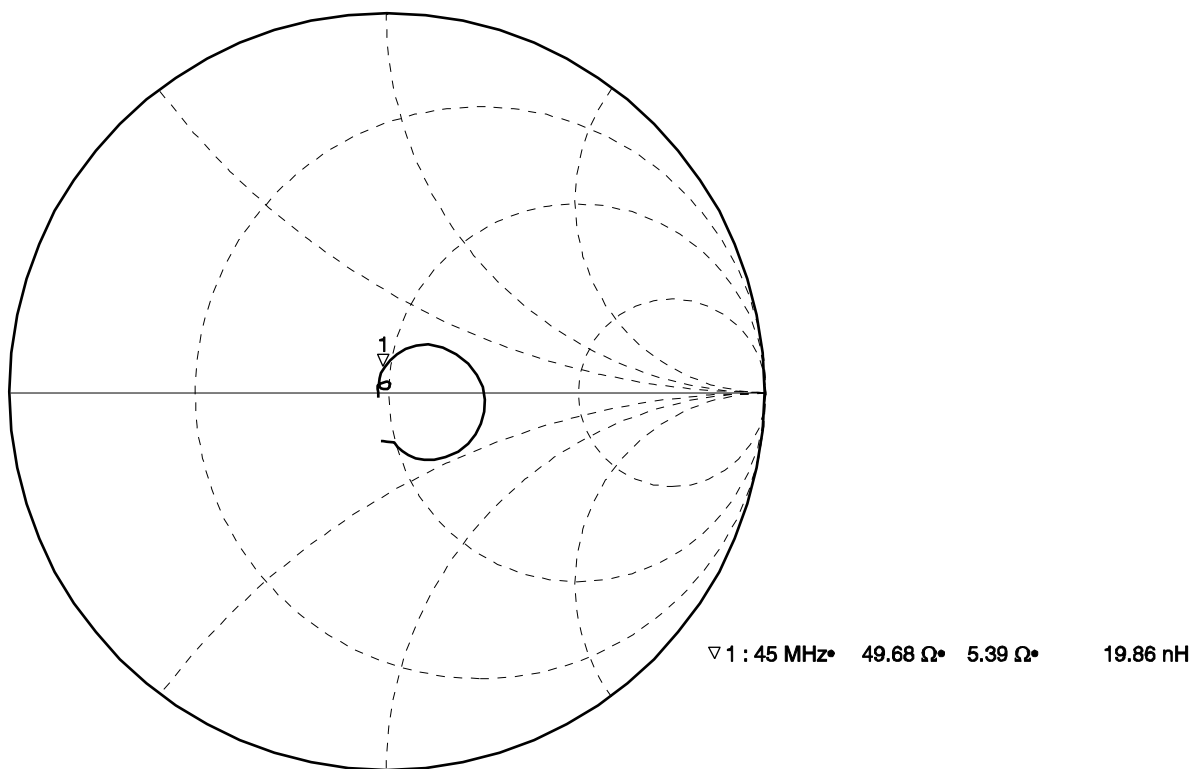
Remark The graphs indicate nominal characteristics.

S-PARAMETERS ($T_A = +25^\circ\text{C}$, $V_{CC} = 5.0\text{ V}$, $V_{AGC} = 0\text{ V}$)

S_{11} —FREQUENCY

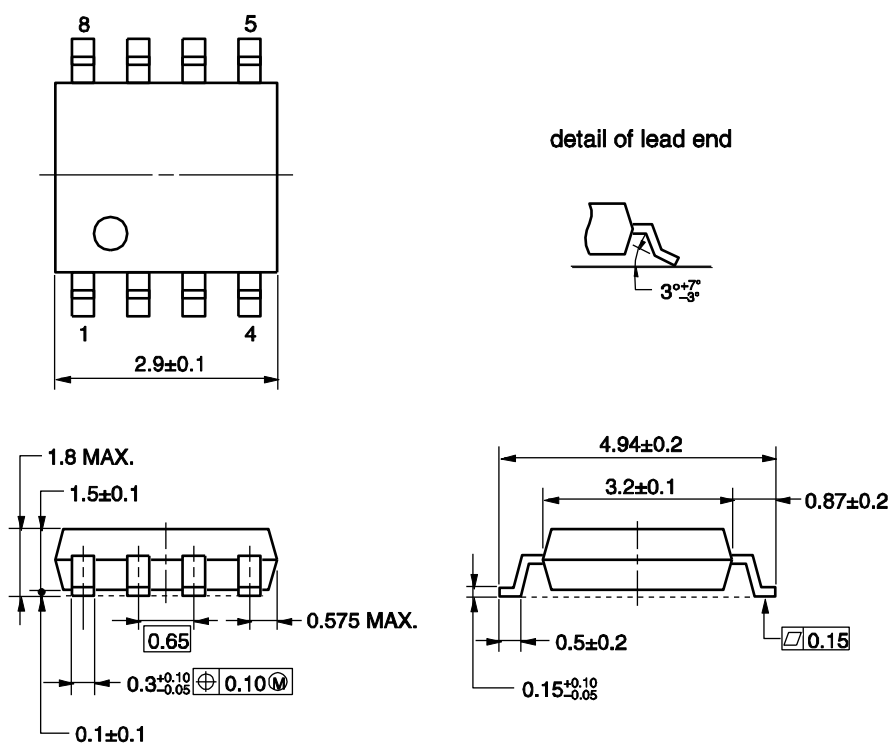


S_{22} —FREQUENCY



PACKAGE DIMENSIONS

8-PIN PLASTIC SSOP (4.45 mm (175)) (UNIT: mm)



NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation).
All the ground pins must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to V_{CC} line.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (pin temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).