

CMPS5064

SILICON CONTROLLED RECTIFIER



SOT-23 CASE

CentralTM
Semiconductor Corp.

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMPS5064 type is an epoxy molded PNP Silicon Controlled Rectifier manufactured in an SOT-23 case, designed for control systems and sensing circuit applications.

Marking code is P2D.

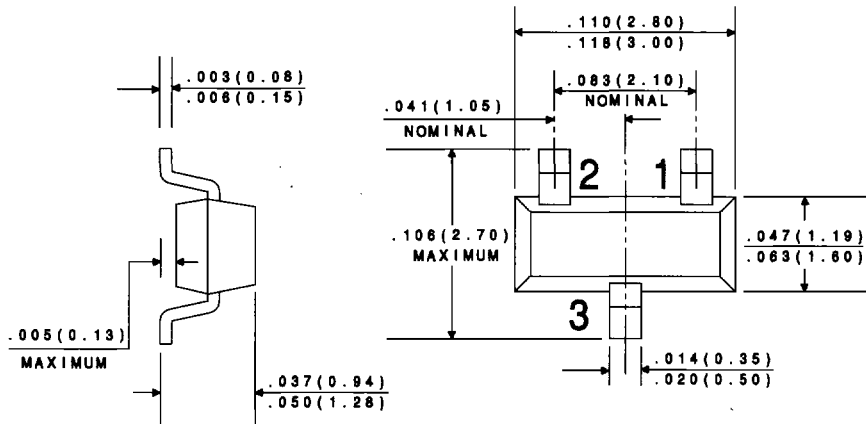
MAXIMUM RATINGS ($T_A=25^{\circ}\text{C}$)

	SYMBOL		UNITS
Peak Repetitive Off-State Voltage	V_{DRM}	400	V
Peak Repetitive Reverse Voltage	V_{RRM}	400	V
RMS On-State Current	$I_{\text{T(RMS)}}$	0.8	A
Average On-State Current ($T_C=67^{\circ}\text{C}$)	$I_{\text{T(AV)}}$	0.51	A
Power Dissipation	P_{D}	350	mW
Operating and Storage			
Junction Temperature	$T_{\text{J}}, T_{\text{stg}}$	-65 to +150	$^{\circ}\text{C}$
Thermal Resistance	θ_{JA}	357	$^{\circ}\text{C/W}$

ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I_{DRM}	$V_{\text{D}}=400\text{V}, R_{\text{GK}}=1\text{K}\Omega, T_{\text{C}}=125^{\circ}\text{C}$		50	μA
I_{RRM}	$V_{\text{D}}=400\text{V}, R_{\text{GK}}=1\text{K}\Omega, T_{\text{C}}=125^{\circ}\text{C}$		50	μA
V_{T}	$I_{\text{T}}=1.2\text{A}$		1.7	V
I_{GT}	$V_{\text{D}}=7.0\text{V}, R_{\text{L}}=100\Omega, R_{\text{GK}}=1\text{K}\Omega$		200	μA
V_{GT}	$V_{\text{D}}=7.0\text{V}, R_{\text{L}}=100\Omega, R_{\text{GK}}=1\text{K}\Omega$		0.8	V
V_{GD}	$V_{\text{D}}=400\text{V}, R_{\text{L}}=100\Omega, T_{\text{C}}=125^{\circ}\text{C}$	0.1		V
I_{H}	$V_{\text{D}}=7.0, R_{\text{GK}}=1\text{K}\Omega$		5.0	mA
t_{ON}	$V_{\text{D}}=400\text{V}, I_{\text{GT}}=1.0\text{mA}, R_{\text{GK}}=1.0\Omega, di/dt=6.0\text{A}/\mu\text{s}$		2.8 TYP	μs

All dimensions in inches (mm).



LEAD CODE:

- 1) CATHODE
- 2) GATE
- 3) ANODE

DATA
SHEET

R2