

**ELECTRICAL CHARACTERISTICS**

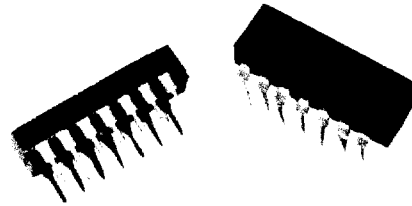
Supply Voltage,  $V_{cc}$  ..... 4.5 to 5.5 V  
 Logic 1 Input Voltage ..... 2.0 to 5.5 V  
 Maximum Logic 1 Input Current ( $V_{IN}=2.0V$ ) ..... 50  $\mu$  A  
 Maximum Logic 0 Input Voltage ..... 0.8V  
 Maximum Logic 0 Input Current ..... -2.0mA  
 Maximum Logic 1 Output Voltage ..... 2.4V  
 Maximum Logic 0 Output Voltage ..... 0.5V  
 Maximum Rise Time ..... 4.0ns(0.75V to 2.4V)

**INPUT TEST CONDITIONS**

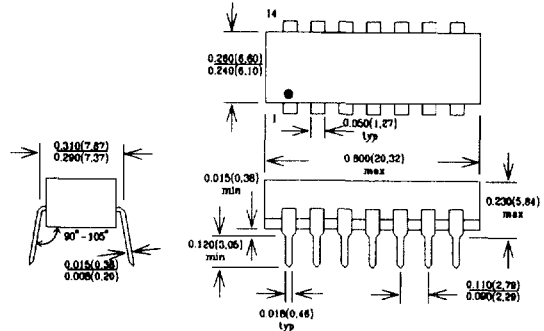
Pulse Voltage ..... 3.2V  
 Rise Time ..... 3.0ns  
 Typical Supply Current ..... 60mA  
 Minimum Pulse Width ..... 0.4td

**MAXIMUM DRIVE CAPABILITIES**

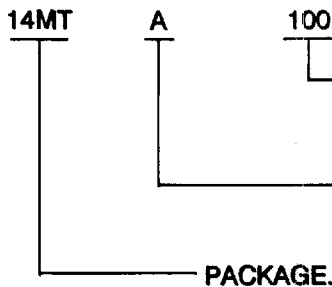
Logic 1 Output (TTL) ..... 20 Loads/Unit  
 Logic 0 Output (TTL) ..... 10 Loads/Tap  
 (TTL) ..... 20 Loads/Unit



**PACKAGE DIMENSIONS**



**PART NUMBERING SYSTEM**



**TOTAL DELAY TIME.** In nanoseconds. If necessary, zeros precede the value given to complete three-digit block.

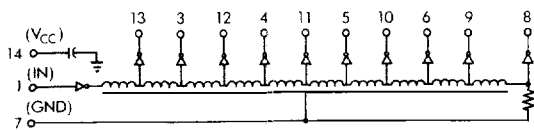
**CIRCUIT CONFIGURATION.**

10MT=14-Pin(10 Tap)  
 14MT=14-Pin(5 Tap)  
 12MT=16-Pin (optional 10 Tap)

**10MT**

**CIRCUIT DIAGRAM**

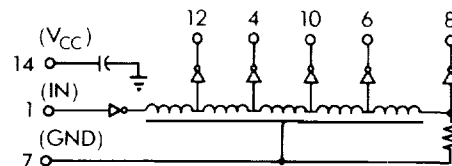
**CIRCUIT B**



Catalog Part Number	Total Delay (ns) <sup>(1&amp;2)</sup>	Tap Delay (ns) <sup>(1)</sup>
10MTB050	50	5
10MTB100	100	10
10MTB150	150	15
10MTB200	200	20
10MTB250	250	25
10MTB300	300	30
10MTB350	350	35
10MTB400	400	40

**14MT**

**CIRCUIT A**



Catalog Part Number	Total Delay (ns) <sup>(1&amp;2)</sup>	Tap Delay (ns) <sup>(1)</sup>
14MTA025	25	5
14MTA030	30	6
14MTA035	35	7
14MTA040	40	8
14MTA050	50	10
14MTA060	60	12
14MTA075	75	15
14MTA100	100	20
14MTA125	125	25
14MTA150	150	30
14MTA175	175	35
14MTA200	200	40
14MTA250	250	50
14MTA300	300	60
14MTA400	400	80
14MTA500	500	100