

## BD136/138/140

# Medium Power Linear and Switching Applications

• Complement to BD135, BD137 and BD139 respectively



## **PNP Epitaxial Silicon Transistor**

## Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

Symbol	Parar	Parameter		Units	
V <sub>CBO</sub>	Collector-Base Voltage	: BD136	- 45	V	
020		: BD138	- 60	V	
		: BD140	- 80	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	: BD136	- 45	V	
		: BD138	- 60	V	
		: BD140	- 80	V	
V <sub>EBO</sub>	Emitter-Base Voltage		- 5	V	
I <sub>C</sub>	Collector Current (DC)		- 1.5	Α	
I <sub>CP</sub>	Collector Current (Pulse)		- 3.0	Α	
I <sub>B</sub>	Base Current		- 0.5	Α	
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C	)	12.5	W	
P <sub>C</sub>	Collector Dissipation (T <sub>a</sub> =25°C)		1.25	W	
T <sub>J</sub>	Junction Temperature		150	°C	
T <sub>STG</sub>	Storage Temperature		- 55 ~ 150	°C	

## **Electrical Characteristics** $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage					
	: BD136	$I_C = -30 \text{mA}, I_B = 0$	- 45			V
	: BD138		- 60			V
	: BD140		- 80			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -30V, I_{E} = 0$			- 0.1	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 10	μΑ
h <sub>FE1</sub>	* DC Current Gain	$V_{CE} = -2V, I_{C} = -5mA$	25			
h <sub>FE2</sub>		$V_{CE} = -2V, I_{C} = -0.5A$	25			
$h_{FE3}$		$V_{CE} = -2V, I_{C} = -150mA$	40		250	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = - 500mA, I <sub>B</sub> = - 50mA			- 0.5	V
V <sub>BE</sub> (on)	* Base-Emitter ON Voltage	$V_{CE} = -2V, I_{C} = -0.5A$			- 1	V

### \* Pulse Test: PW=350μs, duty Cycle=2% Pulsed

## **h**<sub>FE</sub> Classificntion

	O	10	10
h <sub>FE3</sub>	40 ~ 100	63 ~ 160	100 ~ 250

## **Typical Characteristics**

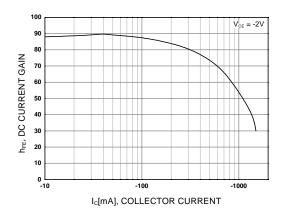


Figure 1. DC current Gain

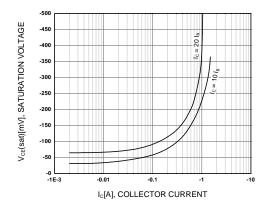


Figure 2. Collector-Emitter Saturation Voltage

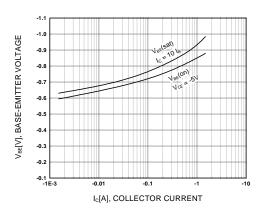


Figure 3. Base-Emitter Voltage

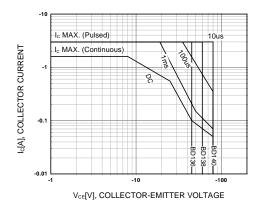


Figure 4. Safe Operating Area

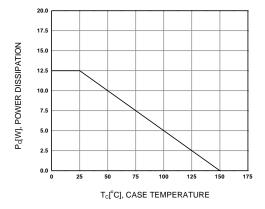


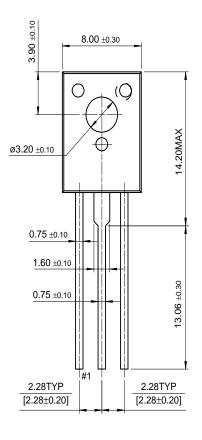
Figure 5. Power Derating

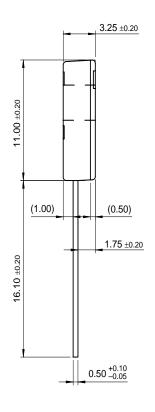
©2000 Fairchild Semiconductor International Rev. A, February 2000

BD136/138/140

## **Package Demensions**

## TO-126





Dimensions in Millimeters

#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

FACT™ QFET™ FACT Quiet Series™ QS™

FAST<sup>®</sup> Quiet Series<sup>™</sup> SuperSOT<sup>™</sup>-3 GTO<sup>™</sup> SuperSOT<sup>™</sup>-6

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

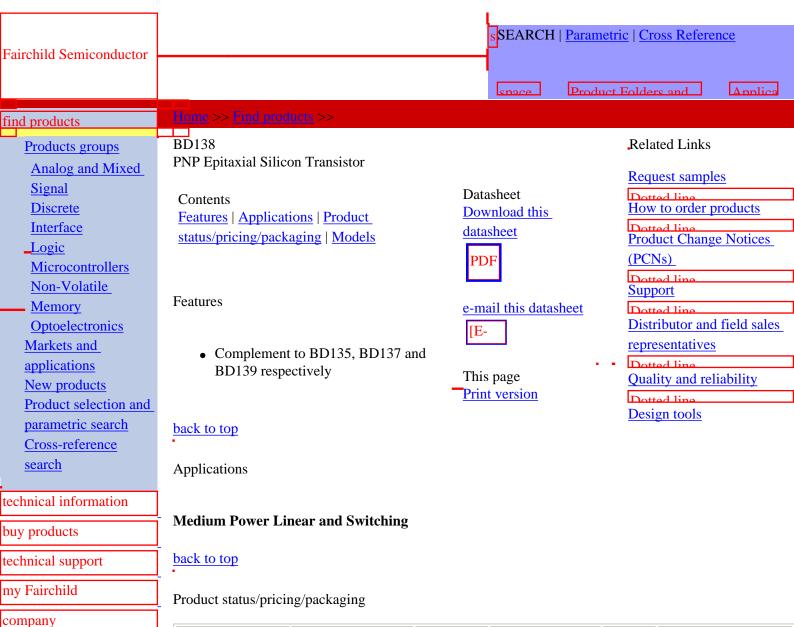
- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2000 Fairchild Semiconductor International Rev.



Product	Product status	Pricing*	Package type	Leads	Packing method
BD13810S	Full Production	\$0.163	<u>TO-126</u>	3	BULK
BD1386S	Full Production	\$0.163	<u>TO-126</u>	3	BULK
BD13816S	Full Production	\$0.163	<u>TO-126</u>	3	BULK
BD13810STU	Full Production	\$0.163	<u>TO-126</u>	3	RAIL
BD1386STU	Full Production	\$0.163	<u>TO-126</u>	3	RAIL
BD13816STU	Full Production	\$0.163	<u>TO-126</u>	3	RAIL

<sup>\* 1,000</sup> piece Budgetary Pricing

#### Models

Package & leads	Condition	Temperature range	Software version	<b>Revision date</b>
-----------------	-----------	-------------------	------------------	----------------------

PSPICE				
TO-126-3	Electrical/Thermal	-25°C to 100°C	9.0	Mar 20, 2000

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor



Product	Product status	Pricing*	Package type	Leads	Packing method
BD14016S	Full Production	\$0.193	<u>TO-126</u>	3	BULK
BD14010S	Full Production	\$0.193	<u>TO-126</u>	3	BULK
BD14010STU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL
BD1406S	Full Production	\$0.193	<u>TO-126</u>	3	BULK
BD14016STU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL
BD1406STU	Full Production	\$0.193	<u>TO-126</u>	3	RAIL

<sup>\* 1,000</sup> piece Budgetary Pricing

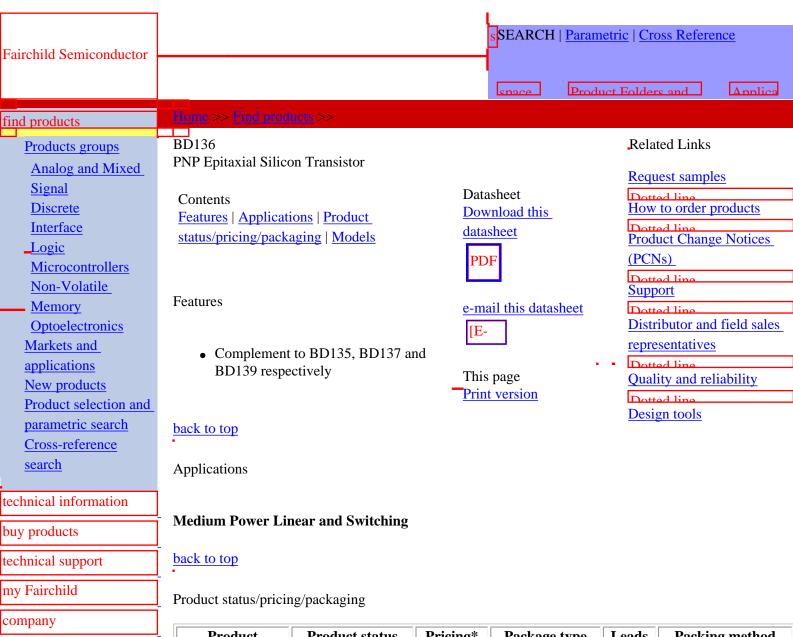
Models

Package & leads	Condition	Temperature range	Software version	<b>Revision date</b>
-----------------	-----------	-------------------	------------------	----------------------

PSPICE				
TO-126-3	Electrical/Thermal	-25°C to 100°C	9.0	Mar 20, 2000

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor



Product	Product status	Pricing*	Package type	Leads	Packing method
BD13610S	Full Production	\$0.19	<u>TO-126</u>	3	BULK
BD13610STU	Full Production	\$0.19	<u>TO-126</u>	3	RAIL
BD1366S	Full Production	\$0.19	<u>TO-126</u>	3	BULK
BD13616S	Full Production	\$0.192	<u>TO-126</u>	3	BULK
BD1366STU	Full Production	\$0.19	<u>TO-126</u>	3	RAIL
BD13616STU	Full Production	\$0.192	<u>TO-126</u>	3	RAIL

<sup>\* 1,000</sup> piece Budgetary Pricing

#### Models

Package & leads	Condition	Temperature range	Software version	<b>Revision date</b>
-----------------	-----------	-------------------	------------------	----------------------

PSPICE				
TO-126-3	Electrical/Thermal	-25°C to 100°C	9.0	Mar 20, 2000

<u>Home</u> | <u>Find products</u> | <u>Technical information</u> | <u>Buy products</u> | <u>Support</u> | <u>Company</u> | <u>Contact us</u> | <u>Site index</u> | <u>Privacy policy</u>

© Copyright 2002 Fairchild Semiconductor