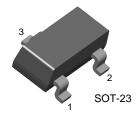


KST63/64

Darlington Transistor



1. Base 2. Emitter 3. Collector

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	-30	V
V _{CES}	Collector-Emitter Voltage	-30	V
V_{EBO}	Emitter-Base Voltage	-10	V
I _C	Collector Current	-500	mA
P _C	Collector Power Dissipation	350	mW
T _{STG}	Storage Temperature	150	°C

Electrical Characteristics T_a =25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
BV _{CES}	Collector-Emitter Breakdown Voltage	I _C = -100, V _{BE} =0	-30		V
I _{CBO}	Collector Cut-off Current	$V_{CE} = -30V, I_{E} = 0$		-100	nA
I _{EBO}	Emitter Cut-off Current	V _{EB} = -10V, I _C =0		-100	nA
h _{FE}	* DC Current Gain : KST63 : KST64 : KST63 : K ST64	V_{CE} = -5V, I_{C} = -10mA V_{CE} = -5V, I_{C} = -100mA	5K 10K 10K 20K		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -100mA, I _B = -0.1mA		-1.5	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = -5V, I_{C} = -100 \text{mA}$		-2.0	V
f _T	Current Gain Bandwidth Product	V _{CE} = -5V, I _C = -10mA f=100MHz	125		MHz

^{*} Pulse test: PW≤300μs, Duty Cycle≤2%

Marking Code

Туре	KST63	KST64	
Mark	2U	2V	





Typical Characteristics

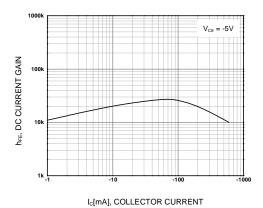


Figure 1. DC current Gain

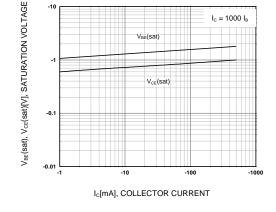


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

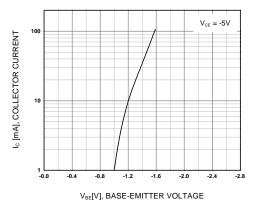


Figure 3. Base-Emitter On Voltage

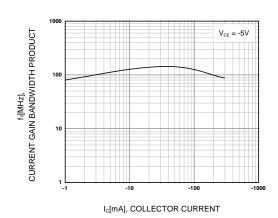
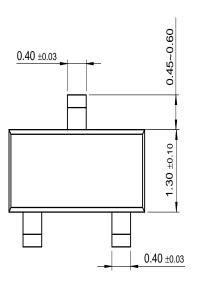
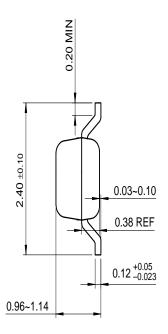


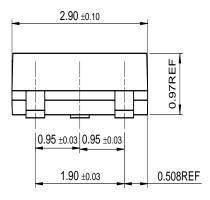
Figure 4. Current Gain Bandwidth Product

Package Demensions

SOT-23







Dimensions in Millimeters

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