ISO-9001 CERTIFIED BY DSCC

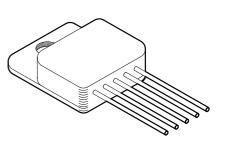


DUAL POSITIVE/NEGATIVE, 3 AMP, ULTRA LOW DROPOUT FIXED VOLTAGE REGULATORS

# 4707 Dey Road Liverpool, N.Y. 13088

### FEATURES:

- Ultra Low Dropout Voltage
- Internal Short Circuit Current Limit
- Output Voltages Are Internally Set To ±1% Maximum
- Electrically Isolated Case
- Internal Thermal Overload Protection
- Many Output Voltage Combinations
- Alternate Package and Lead Form Configurations Available

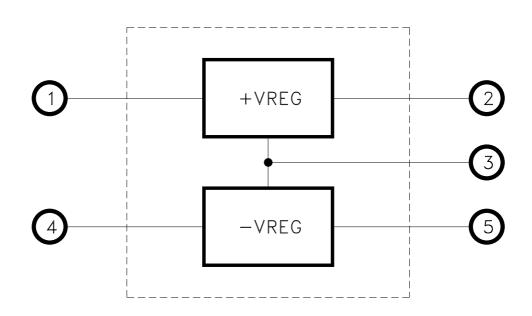


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## **DESCRIPTION:**

The MSK 5200 Series offers ultra low dropout voltages on both the positive and negative regulators. This, combined with the low  $\theta_{JC}$ , allows increased output current while providing exceptional device efficiency. Because of the increased efficiency, a small hermetic 5 pin package can be used providing maximum performance while occupying minimal board space. Output voltages are internally trimmed to  $\pm 1\%$  maximum resulting in consistent and accurate operation. Additionally, both regulators offer internal short circuit current and thermal limiting, which allows circuit protection and eliminates the need for external components and excessive derating.

# EQUIVALENT SCHEMATIC



## **TYPICAL APPLICATIONS**

- High Efficiency Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- Switching Power Supply Post Regulators

# **PIN-OUT INFORMATION**

- 1 + Vin
- 2 + Vout
- 3 GND
- 4 -Vin
- 5 -Vout

(315) 701-6751

# **ABSOLUTE MAXIMUM RATINGS**

$\pm V_{IN}$	Input Voltage	Тsт
PD	Power Dissipation Internally Limited	TLD
Ιουτ	Output Current	
ТJ	Junction Temperature + 175°C	Tc

Storage Temperature Range	-65°C to +150°C
Lead Temperature Range	300°C
(10 Seconds)	

Case Operating Temperature		
MSK5200-5210	-40°C to	+125°C
MSK5200H-5210H/E	-55°C to	$+125 ^{o}C$

# **ELECTRICAL SPECIFICATIONS**

Parameter	Test Conditions ① ③	Group A	Group A MSK 5200H/E SERIES		MSK 5200 SERIES					
Farameter		Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units	
POSITIVE OUTPUT REGULATO	RS:									
Output Voltage Tolerance	VIN = VOUT + 1V	1	-	0.5	1.0	-	0.5	1.5	%	
Output voltage rolerance	Iout = 0A	2,3	-	1.0	2.0	-	1.0	-	%	
Dropout Voltage (2)	Iout=2A; ΔVout = -1%	1	-	350	600	-	350	625	mV	
Land Parculation	100mA≤louт≤2.5A	1	-	0.2	1.0	-	0.2	1.2	%	
Load Regulation 🕕	$V_{IN} = V_{OUT} + 1V$	2,3	-	0.3	2.0	-	0.3	-	%	
	Iout = 0A	1	-	0.1	0.5	-	0.1	0.6	%	
Line Regulation	(Vout + 1V)≤VIN≤(26V)	2,3	-	0.5	1.0	-	0.5	-	%	
Quiescent Current	$V_{IN} = V_{OUT} + 1V$ ; $I_{OUT} = 0A$	1,2,3	-	10	15	-	10	15	mA	
Short Circuit Current 2	Vin = Vout + 1V	1	-	4.5	5.0	-	4.5	5.0	Α	
Ripple Rejection ②	IOUT = 3A; COUT = $25\mu$ F; f = 120Hz	4	60	75	-	60	75	-	dB	
Thermal Resistance ②	Junction to Case @ 125°C	-	-	2.5	3.0	-	2.5	3.2	°C/W	
NEGATIVE OUTPUT REGULATO	DRS: 9									
Output Voltage Tolerance	$V_{IN} = V_{OUT} + 1.5V$	1	-	0.1	1.0	-	0.1	2.0	%	
	Iout= OA	2,3	-	0.1	2.0	-	-	-	%	
Dropout Voltage ②	Ιουτ=2Α; ΔVουτ = -1%	1	-	550	700	-	550	750	mV	
Land Damilation	VIN = VOUT + 1.5V	1	-	0.3	1.5	-	0.3	1.7	%	
Load Regulation 10	100mA≤louт≤2.5A	2,3	-	0.5	2.5	-	0.5	-	%	
	Iout=0A	1	-	0.1	0.5	-	0.1	0.6	%	
Line Regulation	(Vout + 1.5V)≤VIN≤(26V)	2,3	-	0.5	1.0	-	0.5	-	%	
Quiescent Current	$V_{IN} = V_{OUT} + 1.5V$ ; $I_{OUT} = 0A$	1,2,3	-	4.5	10	-	4.5	10	mA	
Short Circuit Current 2	VIN = VOUT + 1.5V	1	-	3.6	5.0	-	3.6	5.0	Α	
Ripple Rejection (2)	lout = 3A; Cout = $25\mu$ F; f = 120Hz	4	60	75	-	60	75	-	dB	
Thermal Resistance ②	Junction to Case @ 125°C	-	-	4.7	5.9	-	4.7	5.9	°C/W	

## NOTES:

- (1) Outputs are decoupled to ground using  $33\mu$ F minimum
- tantalum capacitance unless otherwise specified.
- This parameter is guaranteed by design but need not be tested. 2
- Typical parameters are representative of actual device performance but are for reference only.
- 3 All output parameters are tested using a low duty cycle pulse to maintain TJ = Tc. 4 Industrial grade and "E suffix devices shall be tested to subgroups 1 and 4 unless o 5 Military grade devices ('H' suffix) shall be 100% tested to subgroups 1,2,3 and 4. 6 Subgroup 5 and 6 testing available upon request. 7 Subgroup 1,4 Tc =  $+25^{\circ}$ C 2 Subgroup 5 Tc =  $+25^{\circ}$ C Industrial grade and "E suffix devices shall be tested to subgroups 1 and 4 unless otherwise specified.

Subgroup 1,4 Tc = +25 °CSubgroup 2,5 TJ = +125 °C

- $T_A = -55 \,^{o}C$ Subgroup 3,6
- Please consult the factory if alternate output voltages are required. (8)

 Input voltage (Vin = Vour, a specified voltage) is implied to be more negative than Vour.
Due to current limit, maximum output current may not be available at all values of VIN-VOUT and temperatures. See typical performance curves for clarification.

PART ⑧	OUTPUT VOLTAGES					
NUMBER	POSITIVE	NEGATIVE				
MSK 5200	+ 3.3V	-5.2V				
MSK 5201	+5.0V	-5.0V				
MSK 5202	+5.0V	-5.2V				
MSK 5203	+12.0V	-5.0V				
MSK 5204	+12.0V	-12.0V				
MSK 5205	+ 15.0V	-15.0V				
MSK 5206	+15.0V	-5.0V				
MSK 5207	+5.0V	-12.0V				
MSK 5208	+5.0V	-15.0V				
MSK 5209	+ 10.0V	-10.0V				
MSK 5210	+5.2V	-5.2V				

### **APPLICATION NOTES**

#### BYPASS CAPACITORS:

For most applications a 47uF, tantalum capacitor should be attached as close to the regulator's output as possible. This will effectively lower the regulator's output impedance, improve transient response and eliminate any oscillations that may be normally associated with low dropout regulators. Additional bypass capacitors can be used at the remote load locations to further improve regulation. These can be either of the tantalum or the electrolytic variety. Unless the regulator is located very close to the power supply filter capacitor(s), a 4.7uF minimum tantalum capacitor should also be added to the regulator's input. An electrolytic may also be substituted if desired. When substituting electrolytic in place of tantalum capacitors, a good rule of thumb to follow is to increase the size of the electrolytic by a factor of 10 over the tantalum value.

#### LOAD REGULATION:

For best results the ground pin should be connected directly to the load as shown below. This effectively reduces the ground loop effect and eliminates excessive voltage drop in the sense leg. It is also important to keep the output connection between the regulator and the load as short as possible since this directly affects the load regulation. If 20 gauge wire were used as an example, which has a resistance of about .008 ohms per foot, this would result in a drop of 8mV/ft at 1Amp of load current. It is also important to follow the capacitor selection guidelines to achieve best performance. Refer to Figure 2 for connection diagram.

#### MSK 5202 TYPICAL APPLICATION:

Low Dropout Positive and Negative Power Supply

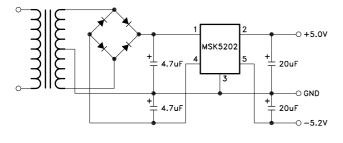
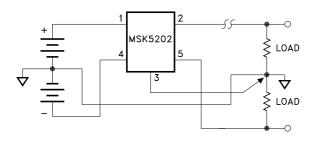


FIGURE 1

#### Avoiding Ground Loops





### OVERLOAD SHUTDOWN:

The regulators feature both current and thermal overload protection. When the maximum power dissipation is not exceeded, the regulators will current limit slightly above their 3 amp rating. As the Vin-Vout voltage increases, however, shutdown occurs in relation to the maximum power dissipation curve. If the device heats enough to exceed its rated die junction temperature due to excessive ambient temperature, improper heat sinking etc., the regulators also shutdown until an appropriate junction temperature is maintained. It should also be noted that in the case of an extreme overload, such as a sustained direct short, the device may not be able to recover. In these instances, the device must be shut off and power reapplied to eliminate the shutdown condition.

#### HEAT SINKING:

To determine if a heat sink is required for your application and if so, what type, refer to the thermal model and governing equation below.

Governing Equation:  $Tj = Pd x (R_{\theta}jc + R_{\theta}cs + R_{\theta}sa) + Ta$ 

#### WHERE

Tj = Junction Temperature Pd = Total Power Dissipation

 $R_{\theta j}$  = Junction to Case Thermal Resistance

- $R_{\theta CS}$  = Case to Heat Sink Thermal Resistance  $R_{\theta Sa}$  = Heat Sink to Ambient Thermal Resistance
- Tc = Case Temperature
- Ta = Ambient Temperature
- Ts = Heat Sink Temperature

#### EXAMPLE:

This example demonstrates an analysis where each regulator is at one-half of its maximum rated power dissipation, which occurs when the output currents are at 1.5 amps each. The negative regulator is worst case due to the larger thermal resistance.

Conditions for MSK 5202:

Vin =  $\pm$  7.0V; lout =  $\pm$  1.5A

- 1.) Assume 45° heat spreading model.
- 2.) Find regulator power dissipation:

$$Pd = (Vin - Vout)(lout)$$
  
 $Pd = (7-5)(1.5)$ 

$$Pd = 3.0W$$

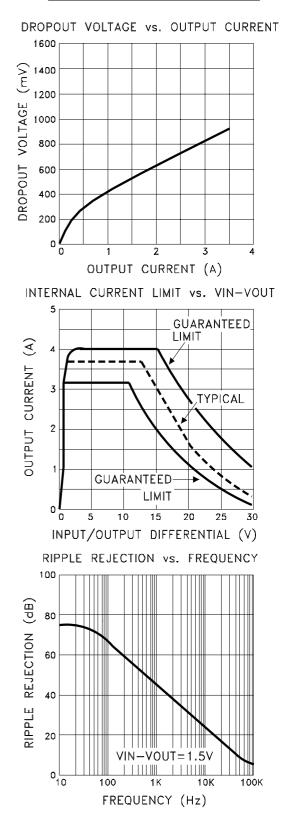
- 3.) For conservative design, set  $Tj = +125^{\circ}C$  Max.
- 4.) For this example, worst case  $Ta = +90^{\circ}C$ .
- 5.)  $R_{\theta jc} = 4.7 \,^{\circ}C/W$  from the Electrical Specification Table.
- 6.) Recs =  $0.15^{\circ}C/W$  for most thermal greases.
- 7.) Rearrange governing equation to solve for Resa:
  - $R_{\theta}sa = ((Tj Ta)/Pd) (R_{\theta}jc) (R_{\theta}cs)$ 
    - = (125°C 90°C)/3.0W (4.7°C/W) (0.15°C/W) = 6.8°C/W

The same exercise must be performed for the negative regulator. In this case the result is  $6.82^{\circ}$ C/W. Therefore, a heat sink with a thermal resistance of no more than  $6.8^{\circ}$ C/W must be used in this application to maintain both regulator circuit junction temperatures under  $125^{\circ}$ C.

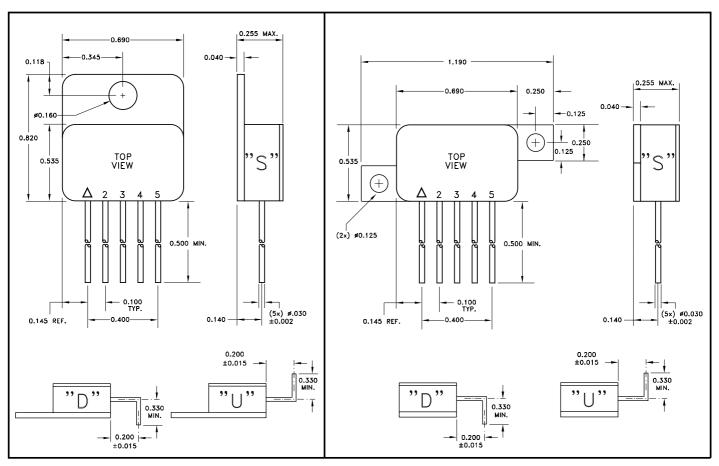
# **TYPICAL PERFORMANCE CURVES**

### POSITIVE REGULATORS DROPOUT vs OUTPUT CURRENT 450 400 DROPOUT VOLTAGE (mV) 350 300 250 200 150 100 50 0.0 1.0 2.0 3.0 OUTPUT CURRENT (A) SHORT CIRCUIT CURRENT vs. TEMP. 8.0 6.5 CURRENT (A) 5.0 V<sub>OUT</sub>=0V 3.5 2.0 0.5 0\_60\_-30 0 30 60 90 120 150 TEMPERATURE ("C) OUTPUT IMPEDANCE vs. FREQUENCY 10 OUTPUT IMPEDANCE ( $\Omega$ ) 1 0.1 0.01 0.001<u>∟</u> 10 100K 100 1K 10K 1 M FREQUENCY (Hz)

# NEGATIVE REGULATORS

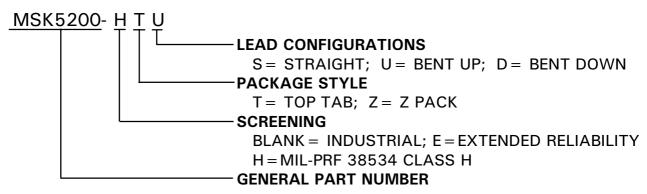


# MECHANICAL SPECIFICATIONS



NOTE: ALL DIMENSIONS ARE  $\pm 0.010$  INCHES UNLESS OTHERWISE LABELED. ESD Triangle indicates Pin 1.

# ORDERING INFORMATION



The above example is a dual +3.3V, -5.2V, Military regulator using the top tab package with leads bent up.

M.S. Kennedy Corp. 4707 Dey Road, Liverpool, New York 13088 Phone (315) 701-6751 FAX (315) 701-6752 www.mskennedy.com

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