

n-Channel Power MOSFET

OptiMOS™3
BSB044N08NN3 G

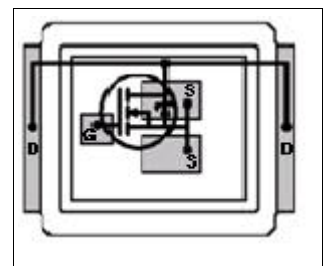
Data Sheet

1.2, 2011-03-07
Preliminary

Industrial & Multimarket

1 Description

OptiMOS™80V products are class leading power MOSFETs for highest power density and energy efficient solutions. Ultra low gate- and output charges together with lowest on state resistance in small footprint packages make OptiMOS™ 80V the best choice for the demanding requirements of switched mode power supplies in Servers, Datacom and Telecom applications but also for motor drives. With almost no parasitic package inductances, the CanPAK allows best controllability of the gate in highly dynamic switching environments. This package in addition features best cooling capability through top-side cooling of the metal can. Hence, this packaging technology combined with the OptiMOS silicon enables highest efficiency levels while having minimal space requirements at the same time



Features

- Optimized technology for DC/DC converters
- 100% avalanche tested
- Excellent $Q_g \times R_{DS(on)}$ product (FOM)
- Qualified according to JEDEC¹⁾ for target applications
- Superior thermal resistance
- Pb-free plating; RoHS compliant
- Halogen-free according to IEC61249-2-21
- Double sided cooling
- Compatible with DirectFET® package ST footprint and outline²⁾
- N-Channel, normal level
- Low parasitic inductance
- Low profile (<0.7 mm)

Applications

- DC/DC converters
- Synchronous rectification
- Power distribution
- Motor drive applications



Table 1 Key Performance Parameters

Parameter	Value	Unit	Related Links
V_{DS}	80	V	IFX OptiMOS webpage IFX OptiMOS product brief IFX OptiMOS spice models IFX Design tools
$R_{DS(on),max}$	4.4	mΩ	
I_D	90	A	
Q_{OSS}	75	nC	
$Q_{g,typ}$	55		

Type	Package	Marking
BSB044N08NN3 G	MG-WDSO-2	0208

1) J-STD20 and JESD22

2) DirectFET® is a trademark of International Rectifier Corporation. BSB044N08NN3 G uses DirectFET® technology licensed from International Rectifier Corporation

2 Maximum ratings

at $T_j = 25\text{ °C}$, unless otherwise specified.

Table 2 Maximum ratings

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Continuous drain current	I_D	-	-	90	A	$V_{GS}=10\text{ V}, T_C=25\text{ °C}$
				68		$V_{GS}=10\text{ V}, T_C=100\text{ °C}$
				18		$V_{GS}=10\text{ V}, T_A=25\text{ °C}, R_{thJA}=58\text{ K/W}^{1)}$
Pulsed drain current ²⁾	$I_{D,pulse}$	-	-	360		$T_C=25\text{ °C}$
Avalanche energy, single pulse	E_{AS}	-	-	660	mJ	$I_D=30\text{ A}, R_{GS}=25\text{ }\Omega$
Gate source voltage	V_{GS}	-20	-	20	V	
Power dissipation	P_{tot}	-	-	78	W	$T_C=25\text{ °C}$
				2.2		$T_A=25\text{ °C}, R_{thJA}=58^{3)}\text{ K/W}$
Operating and storage temperature	T_j, T_{stg}	-40	-	150	°C	
IEC climatic category; DIN IEC 68-1		55	150	56	Ncm	

- 1) DirectFET® is a trademark of International Rectifier Corporation. BSB044N08NN3 G uses DirectFET® technology licensed from International Rectifier Corporation
- 2) See figure 3 for more detailed information
- 3) Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6cm² (one layer, 70 µm thick) copper area for drain connection. PCB is vertical in still air.

3 Thermal characteristics

Table 3 Thermal characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case	R_{thJC}	-	1.0	-	°K/W	bottom
		-	-	1.6		top
Device on PCB	R_{thJA}	-	-	58		6 cm ² cooling area ¹⁾

- 1) Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70µ, thick) copper area for drain connecton. PCB is vertical in still air.

4 Electrical characteristics

Electrical characteristics, at $T_J=25\text{ °C}$, unless otherwise specified.

Table 4 Static characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	80	-	-	V	$V_{GS}=0\text{ V}$, $I_D=1\text{ mA}$
Gate threshold voltage	$V_{GS(th)}$	2	2.8	3.5		$V_{DS}=V_{GS}$, $I_D=97\text{ }\mu\text{A}$
Zero gate voltage drain current	I_{DSS}	-	0.1	10	μA	$V_{DS}=80\text{ V}$, $V_{GS}=0\text{ V}$, $T_J=25\text{ °C}$
		-	10	100		$V_{DS}=80\text{ V}$, $V_{GS}=0\text{ V}$, $T_J=125\text{ °C}$
Gate-source leakage current	I_{GSS}	-	10	100	nA	$V_{GS}=20\text{ V}$, $V_{DS}=0\text{ V}$
Drain-source on-state resistance	$R_{DS(on)}$	-	3.7	4.4	$\text{m}\Omega$	$V_{GS}=10\text{ V}$, $I_D=30\text{ A}$
Gate resistance	R_G	-	0.5	-	Ω	
Transconductance	g_{fs}	36	72		S	$ V_{DS} >2 I_{Dj}R_{DS(on)max}$, $I_D=30\text{ A}$

Table 5 Dynamic characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Input capacitance	C_{iss}	-	4300	5700	pF	$V_{GS}=0\text{ V}$, $V_{DS}=40\text{ V}$, $f=1\text{ MHz}$
Output capacitance	C_{oss}	-	1100	1450		
Reverse transfer capacitance	C_{rss}	-	38	-		
Turn-on delay time	$t_{d(on)}$	-	14	-	ns	$V_{DD}=40\text{ V}$, $V_{GS}=10\text{ V}$, $I_D=30\text{ A}$, $R_G=1.6\text{ }\Omega$
Rise time	t_r	-	9	-		
Turn-off delay time	$t_{d(off)}$	-	26	-		
Fall time	t_f	-	7	-		

Table 6 Gate charge characteristics¹⁾

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	17	-	nC	$V_{DD}=40\text{ V}$, $I_D=30\text{ A}$, $V_{GS}=0\text{ to }10\text{ V}$
	Q_{gd}		11			
Gate to drain charge	Q_{sw}	-	17	-		
Switching charge	Q_g	-	55	73		
Gate charge total	$V_{plateau}$	-	4.6	-		
Output charge	Q_{oss}		75	99	nC	$V_{DD}=30\text{ V}$, $V_{GS}=0\text{ V}$

1) See figure 16 for gate charge parameter definition

Table 7 Reverse diode characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Diode continuous forward current	I_s			30	A	$T_C=25\text{ °C}$
Diode pulse current	$I_{S,pulse}$			120		
Diode forward voltage	V_{SD}	-	0.9	1.2	V	$V_{GS}=0\text{ V}$, $I_F=30\text{ A}$, $T_j=25\text{ °C}$
Reverse recovery charge	Q_{rr}	-	110	-	nC	$V_R=40\text{ V}$, $I_F=I_s$,
Reverse recovery time	t_{rr}	-	55	-	ns	$di_F/dt=400\text{ A}/\mu\text{s}$

5 Electrical characteristics diagrams

Table 8

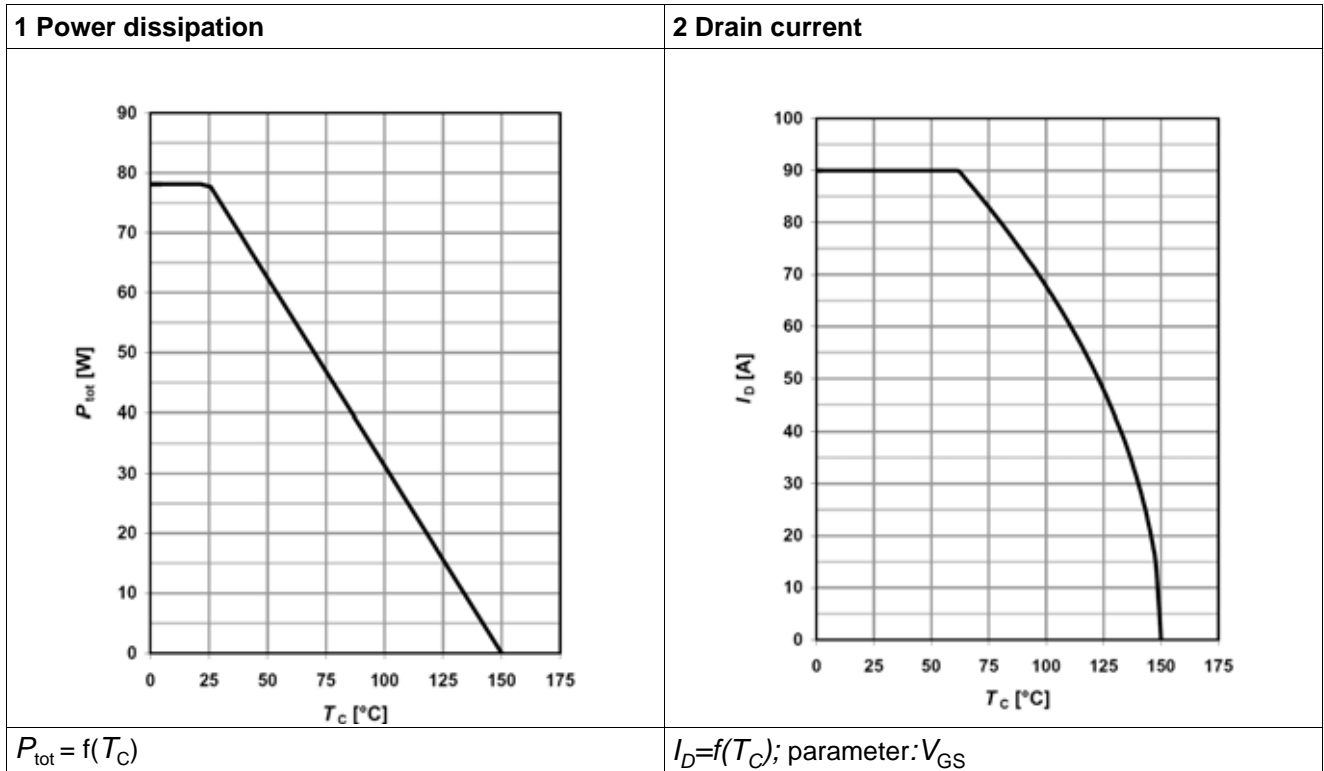


Table 9

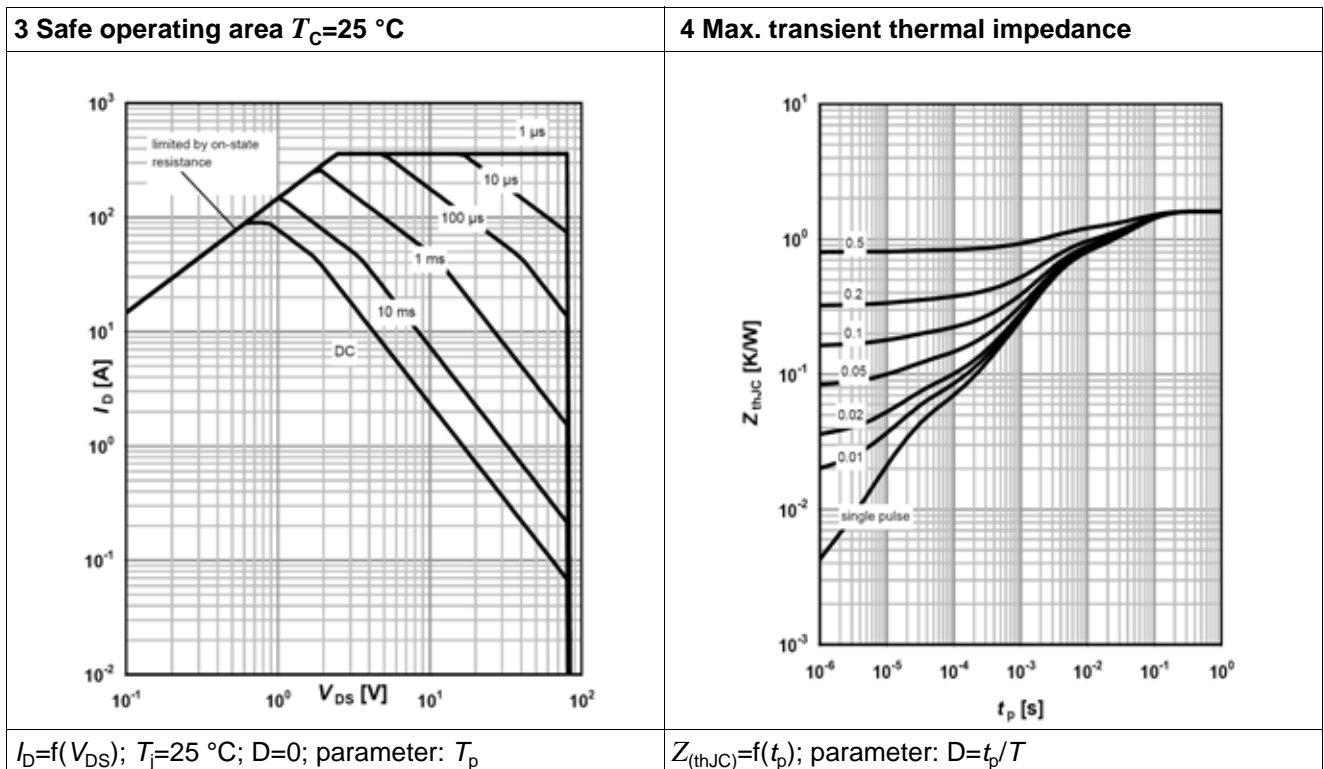


Table 10

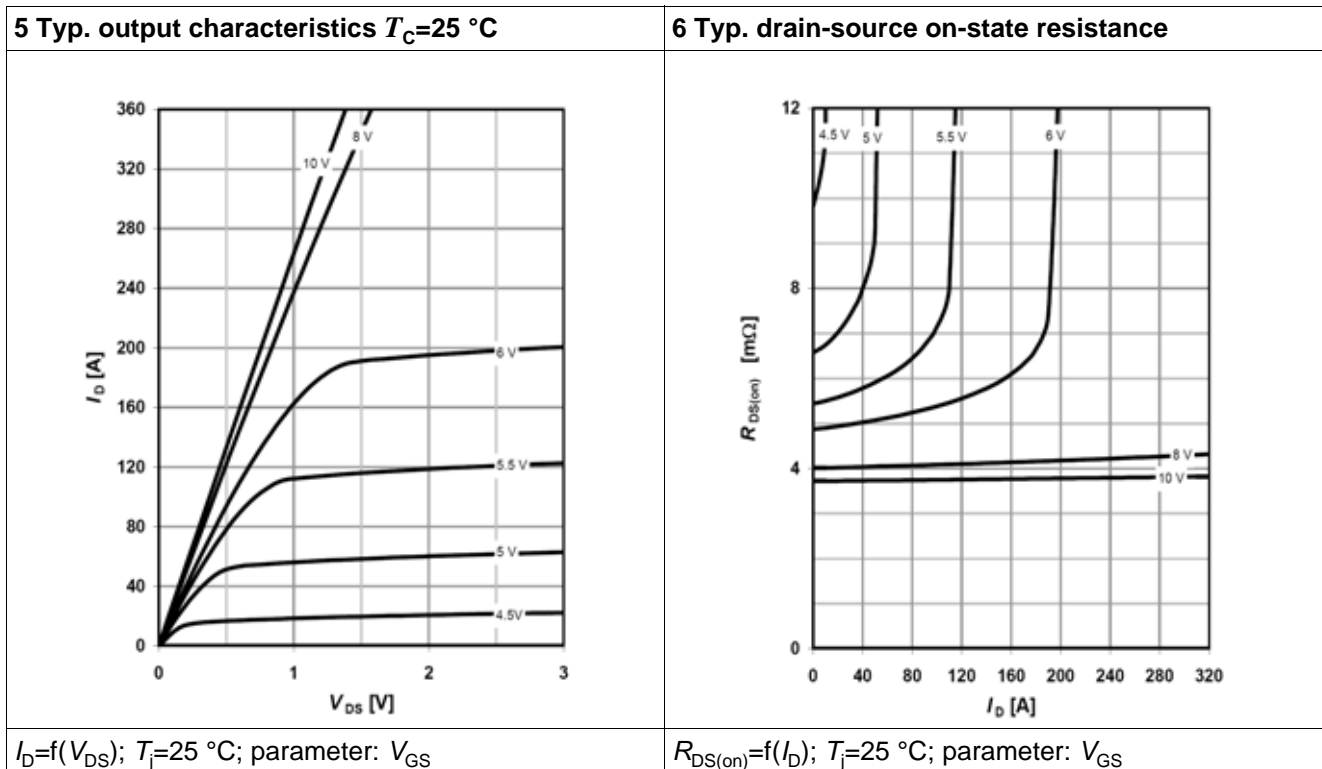


Table 11

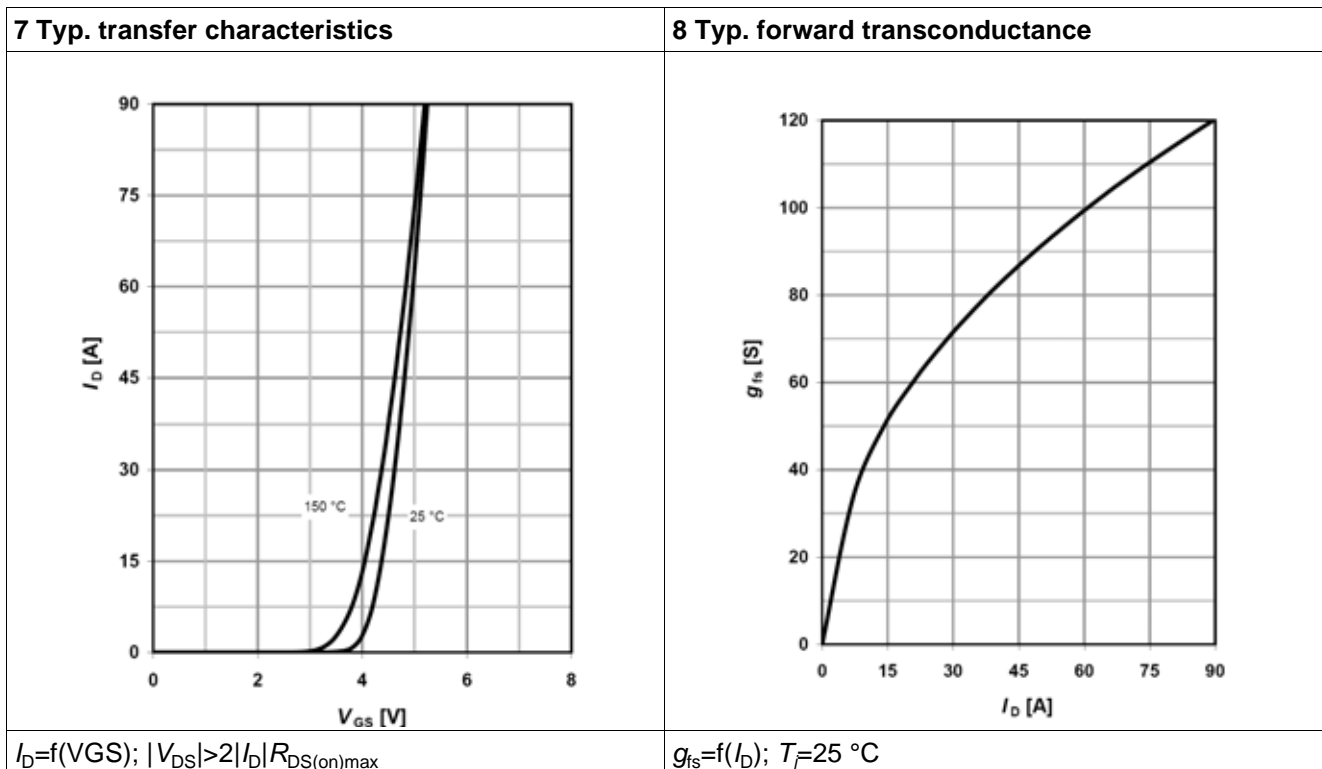


Table 12

<p>9 Drain-source on-state resistance</p> <p>$R_{DS(on)}=f(T_j)$; $I_D=30\text{ A}$; $V_{GS}=10\text{ V}$</p>	<p>10 Typ. gate threshold voltage</p> <p>$V_{GS(th)}=f(T_j)$; $V_{GS}=V_{DS}$</p>
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Table 13

<p>11 Typ. capacitances</p> <p>$C=f(V_{DS})$; $V_{GS}=0\text{ V}$; $f=1\text{ MHz}$</p>	<p>12 Forward characteristics of reverse diode</p> <p>$I_F=f(V_{SD})$; parameter: T_j</p>
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Table 14

<p>13 Avalanche characteristics</p> <p>$I_{AS}=f(t_{AV}); R_{GS}=25 \Omega; \text{parameter: } T_{j(\text{start})}$</p>	<p>14 Typ. gate charge</p> <p>$V_{GS}=f(Q_{\text{gate}}); I_D=30 \text{ A pulsed}; \text{parameter: } V_{DD}$</p>
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Table 15

<p>15 Drain-source breakdown voltage</p> <p>$V_{BR(DSS)}=f(V_{DS}); I_D=1 \text{ mA}$</p>	<p>16 Gate charge waveforms</p>
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6 Package outlines

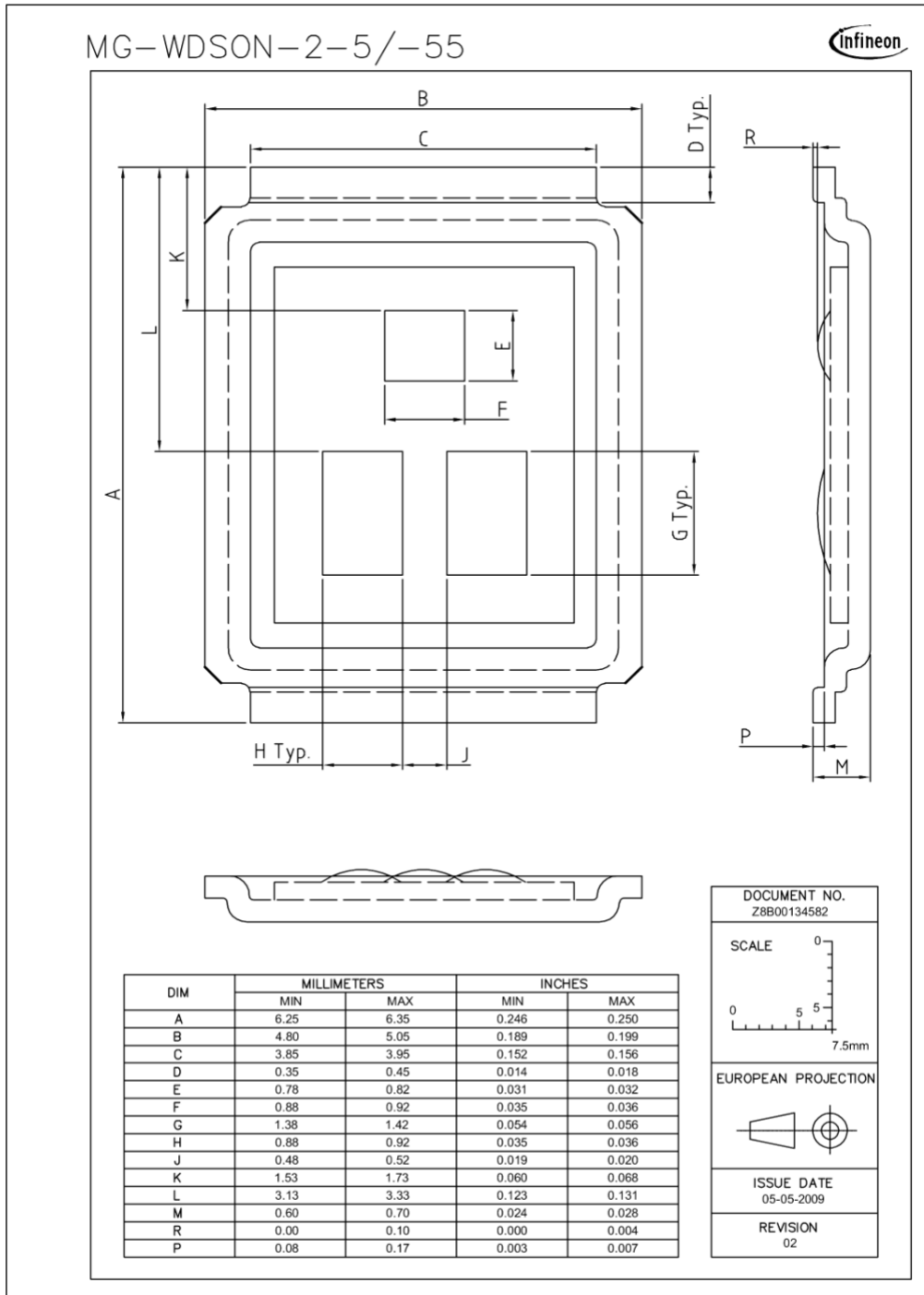


Figure 1 Outlines MG-WDSO-2, dimensions in mm/inches

7 Package outlines

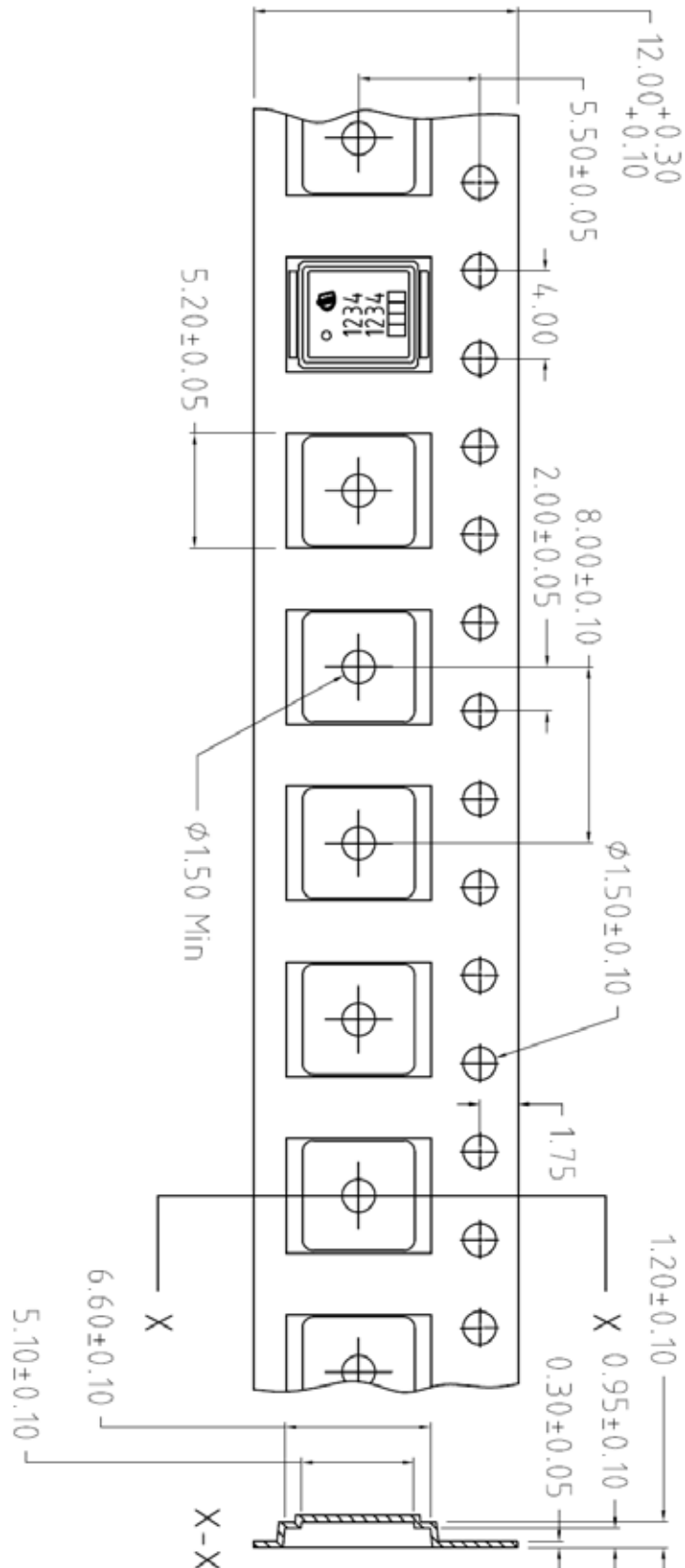
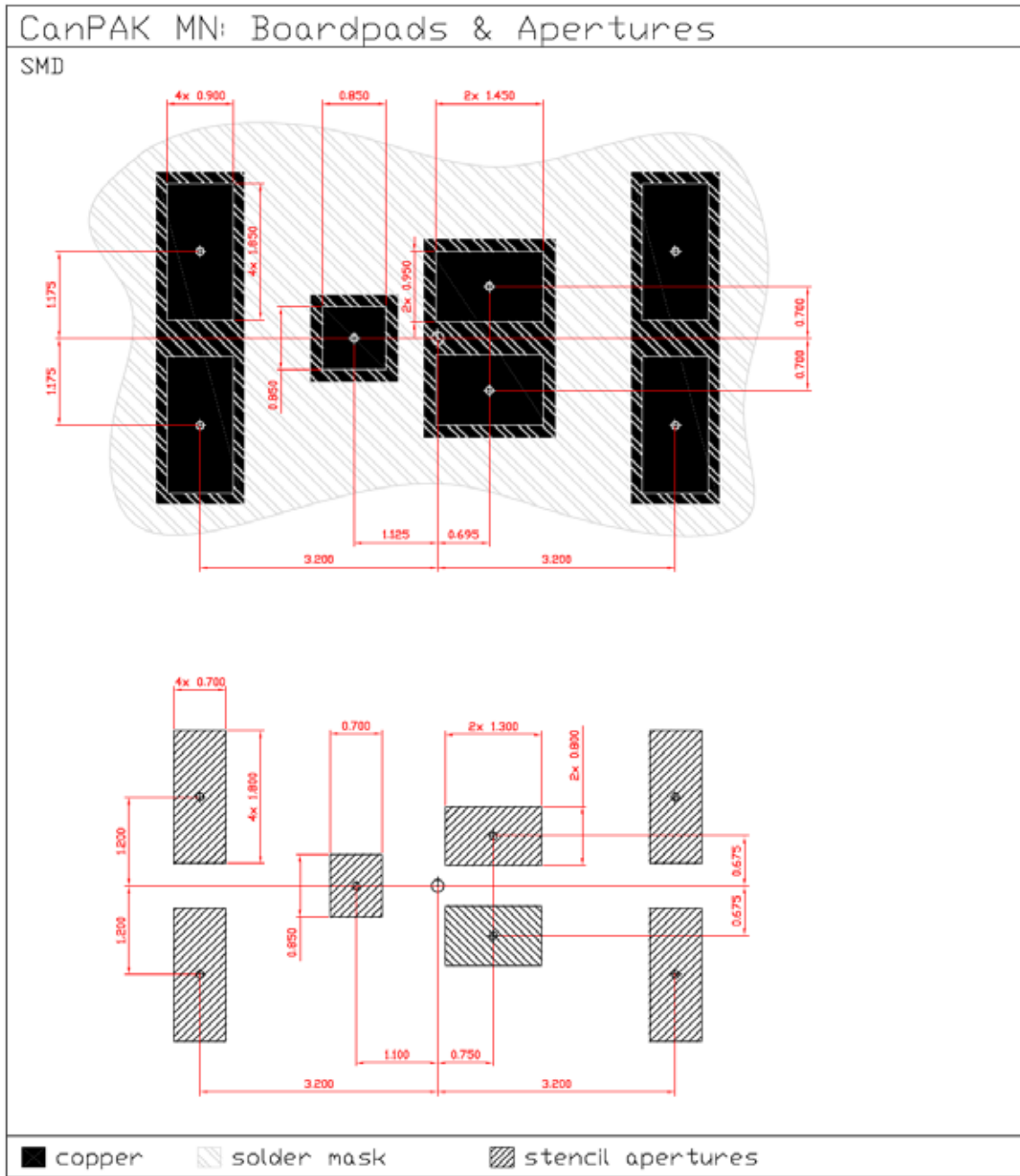


Figure 2 Outlines MG-WDSO-2, dimensions in mm/inches

8 Package outlines



9 Revision History

Revision History: 2011-03-07, 1.2

Previous Revision:

Revision	Subjects (major changes since last revision)
0.1	Release of target data sheet
1.1	Release of Preliminary data sheet

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