

RJH60M2DPE

600 V - 12 A - IGBT Application: Inverter

R07DS0531EJ0100 Rev.1.00 Aug 30, 2011

Features

- Short circuit withstand time (8 µs typ.)
- Low collector to emitter saturation voltage $V_{CE(sat)}=1.9~V$ typ. (at $I_C=12~A,~V_{GE}=15~V,~Ta=25^{\circ}C$)
- Built in fast recovery diode (100 ns typ.) in one package
- Trench gate and thin wafer technology
- High speed switching t_f = 80 ns typ. (at V_{CC} = 300 V, V_{GE} = 15 V, I_C = 12 A, Rg = 5 Ω , Ta = 25°C, inductive load)

Outline

RENESAS Package code: PRSS0004AE-B (Package name: LDPAK (S)-(1))

1. Gate 2. Collector 3. Emitter 4. Collector

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V _{CES} / V _R	600	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	Tc = 25°C	Ic	25	A
	Tc = 100°C	Ic	12	A
Collector peak current		ic(peak) Note1	50	A
Collector to emitter diode forward current		i _{DF}	12	A
Collector to emitter diode forward peak current		i _D (peak) Note1	50	A
Collector dissipation		P _C Note2	63	W
Junction to case thermal resistance (IGBT)		θj-c Note2	1.98	°C/W
Junction to case thermal resistance (Diode)		θj-cd ^{Note2}	4.2	°C/W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

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Electrical Characteristics

 $(Ta = 25^{\circ}C)$

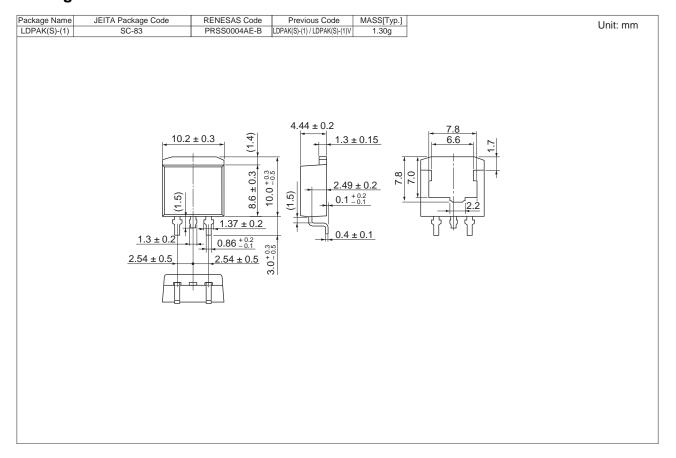
 $di_F/dt = 100 A/\mu s$

ltem	Symbol	Min	Тур	Max	Unit	Test Conditions	
Zero gate voltage collector current / diode reverse current	I _{CES} / I _R	_	_	5	μΑ	V _{CE} = 600 V, V _{GE} = 0	
Gate to emitter leak current	I _{GES}	_	_	±1	μΑ	$V_{GE} = \pm 30 \text{ V}, V_{CE} = 0$	
Gate to emitter cutoff voltage	$V_{GE(off)}$	5	_	7	V	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.9	2.5	V	I _C = 12 A, V _{GE} = 15 V Note3	
	V _{CE(sat)}	_	2.3	_	V	$I_C = 25 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note3}}$	
Input capacitance	Cies	_	430	_	pF	V _{CE} = 25 V V _{GE} = 0	
Output capacitance	Coes	_	40	_	pF		
Reverse transfer capacitance	Cres	_	15	_	pF	f = 1 MHz	
Total gate charge	Qg	_	19	_	nC	V _{GE} = 15 V	
Gate to emitter charge	Qge	_	4	_	nC	V _{CE} = 300 V	
Gate to collector charge	Qgc	_	8	_	nC	I _C = 12 A	
Switching time	t _{d(on)}	_	30	_	ns	V_{CC} = 300 V, V_{GE} = 15 V I_{C} = 12 A Rg = 5 Ω Inductive load	
	t _r	_	15	_	ns		
	t _{d(off)}	_	90	_	ns		
	t _f	_	80	_	ns		
Short circuit withstand time	t _{sc}	6	8	_	μS	Tc = 100 °C	
						$V_{CC} \le 360 \text{ V}, V_{GE} = 15 \text{ V}$	
FRD forward voltage	V_{F}	_	1.2	1.6	V	I _F = 12 A Note3	
FRD reverse recovery time	t _{rr}	_	100	_	ns	I _F = 12 A	

Notes: 3. Pulse test.

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Package Dimension



Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJH60M2DPE-00-J3	1000 pcs	Taping

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