## $0.2 \mathrm{~A} \mathrm{SBR}{ }^{\circledR}$ <br> Super Barrier Rectifier

## Features

- Low Leakage Current
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- $150^{\circ} \mathrm{C}$ Operating Junction Temperature
- Lead Free Finish, RoHS Compliant
- "Green" Molding Compound (No Br, Sb)


## Mechanical Data

- Case: SOD-523
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Polarity Indicator: Cathode Band
- Terminals: Finish - Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.002 grams (approximate)

Maximum Ratings @ $T_{A}=25^{\circ} \mathrm{C}$ unless othervise specified
Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitive load, derate current by 20\%.

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Peak Repetitive Reverse Voltage <br> Working Peak Reverse Voltage <br> DC Blocking Voltage | $\mathrm{V}_{\mathrm{RRM}}$ <br> $\mathrm{V}_{\mathrm{RWM}}$ <br> $\mathrm{V}_{\mathrm{RM}}$ | 30 | V |
| RMS Reverse Voltage | $\mathrm{V}_{\mathrm{R}(\mathrm{RMS})}$ | 21 | V |
| Average Rectified Output Current (See Figure 1) | $\mathrm{I}_{\mathrm{O}}$ | 0.2 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms <br> Single Half Sine-Wave Superimposed on Rated Load | $\mathrm{I}_{\text {FSM }}$ | 5 | A |
| Maximum Thermal Resistance <br> Thermal Resistance Junction to Soldering (Note 1) | $\mathrm{R}_{\text {日JA }}$ | 400 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{j}, \mathrm{T}} \mathrm{T}_{\text {STG }}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage (Note 2) | $\mathrm{V}_{(\mathrm{BR}) \mathrm{R}}$ | 30 |  | - | V | $\mathrm{I}_{\mathrm{R}}=400 \mu \mathrm{~A}$ |
| Forward Voltage Drop | $V_{\text {F }}$ | - | $\begin{aligned} & 0.50 \\ & 0.46 \\ & 0.57 \\ & 0.55 \end{aligned}$ | $\begin{aligned} & 0.54 \\ & 0.49 \\ & 0.61 \\ & 0.58 \end{aligned}$ | V | $\begin{aligned} & I_{F}=0.1 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=0.1 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=85^{\circ} \mathrm{C} \\ & \mathrm{I}_{\mathrm{F}}=0.2 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \\ & I_{F}=0.2 \mathrm{~A}, \mathrm{~T}_{\mathrm{j}}=85^{\circ} \mathrm{C} \end{aligned}$ |
| Leakage Current (Note 2) | IR | - |  | $\begin{aligned} & 20 \\ & 0.1 \end{aligned}$ | $\underset{\mathrm{mA}}{\mu \mathrm{~A}}$ | $\begin{aligned} & V_{R}=30 \mathrm{~V}, \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \\ & V_{R}=30 \mathrm{~V}, \mathrm{~T}_{\mathrm{j}}=125^{\circ} \mathrm{C} \end{aligned}$ |

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## Dopes



Fig. 1 Typical Forward Characteristics


Fig. 3 Typical Total Capacitance


## Package Outline Drawing



| SOD-523 |  |  |
| :---: | :---: | :---: |
| Dim | Min | Max |
| A | 1.50 | 1.70 |
| B | 1.10 | 1.30 |
| C | 0.25 | 0.35 |
| D | 0.70 | 0.90 |
| E | 0.10 | 0.20 |
| G | 0.55 | 0.65 |
| All Dimensions in mm |  |  |

SBR0230T5

## Marking, Polarity, Weight \& Ordering Information

|  | Case Style |  | Marking | Weight |
| :---: | :---: | :---: | :---: | :---: |
|  |  <br> Top View | Back View | $\boxed{\|l\|}$ | 0.002g (approx.) |


| Ordering Information | Date Code |
| :---: | :---: |
| SBRO230T5-7 <br> 3000/Tape \& Reel | 23 = Product Type Marking Code |

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[^0]:    Notes: 1. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
    2. Short duration pulse test used to minimize self-heating effect

