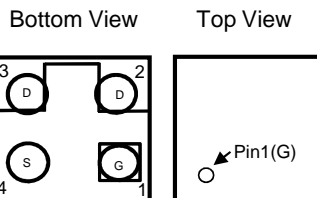


## General Description

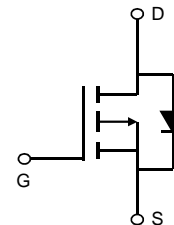
The AOC2411 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V while retaining a 12V  $V_{GS(MAX)}$  rating.

## Features

$V_{DS}$	-30V
$I_D$ (at $V_{GS}=-4.5V$ )	-3.4A
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	< 45m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=-2.5V$ )	< 60m $\Omega$



Equivalent Circuit



### Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Source Current (DC) <sup>Note1</sup>	$I_D$	-3.4	A
Source Current (Pulse) <sup>Note2</sup>	$I_{SM}$	-52	
Power Dissipation <sup>Note1</sup>	$P_D$	0.8	W
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

### Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>Note1</sup>	$R_{\theta JA}$	75	90	$^\circ\text{C/W}$
Maximum Junction-to-Ambient <sup>Note1</sup>		Steady-State	130	155
Maximum Junction-to-Foot(Drain)	$R_{\theta JF}$	16	20	$^\circ\text{C/W}$

**Note 1.** Mounted on minimum pad PCB

**Note 2.** PW <300  $\mu\text{s}$  pulses, duty cycle 0.5% max

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
BV <sub>DSS</sub>	Source-Source Breakdown Voltage	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-30			V
I <sub>DSS</sub>	Zero Gate Voltage Source Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C			-1 -5	μA
I <sub>GSS</sub>	Gate leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.6	-1	-1.4	V
R <sub>DS(ON)</sub>	Static Source to Source On-Resistance	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1A T <sub>J</sub> =125°C		37 52	45 63	mΩ
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-1A		7.5		S
V <sub>FSD</sub>	Diode Forward Voltage	I <sub>D</sub> =-1A, V <sub>GS</sub> =0V,		-0.7	-1	V
<b>DYNAMIC PARAMETERS</b> <sup>Note1</sup>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1MHz,		1253	1630	pF
C <sub>oss</sub>	Output Capacitance		167	220	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance		105	150	pF	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz		16.7	34	Ω
<b>SWITCHING PARAMETERS</b> <sup>Note1</sup>						
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> =-4.5V, V <sub>DS</sub> =-10V, I <sub>D</sub> =-1A		12.5	20	nC
Q <sub>gs</sub>	Gate Source Charge		2		nC	
Q <sub>gd</sub>	Gate Drain Charge		3.2		nC	
t <sub>D(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> =-4.5V, V <sub>DS</sub> =-10V, R <sub>L</sub> =10Ω, I <sub>D</sub> =1A, R <sub>GEN</sub> =6Ω		14	25	ns
t <sub>r</sub>	Turn-On Rise Time		12	20		
t <sub>D(off)</sub>	Turn-Off DelayTime		150	225		
t <sub>f</sub>	Turn-Off Fall Time		72	110		
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =-1A, di/dt=100A/μs		14.5	30	ns

**Note 1: Guaranteed by design**

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

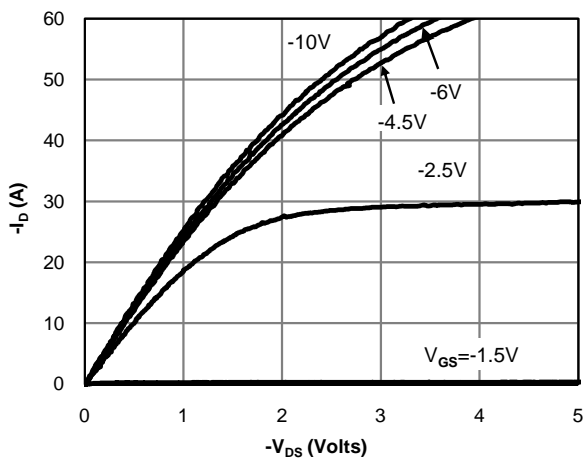


Fig 1: On-Region Characteristics

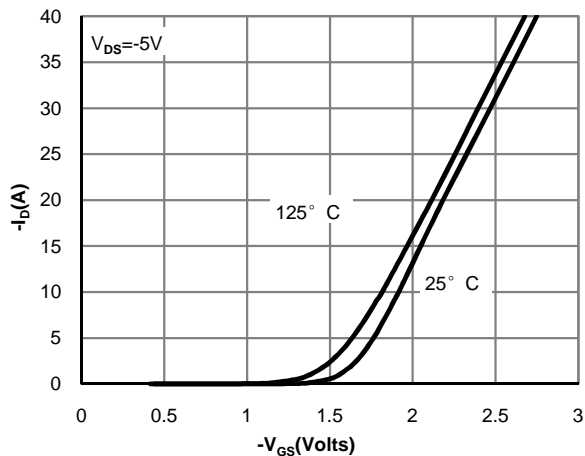


Figure 2: Transfer Characteristics

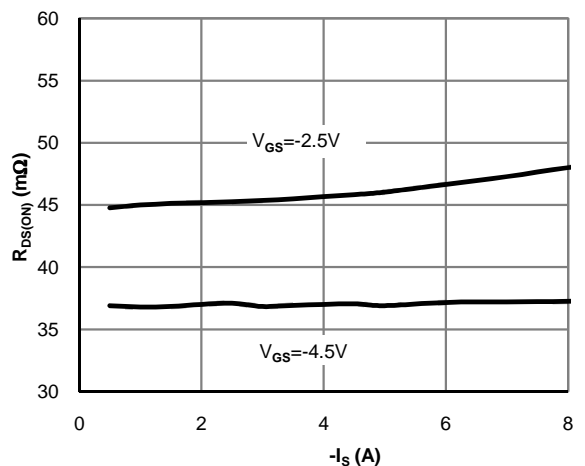


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

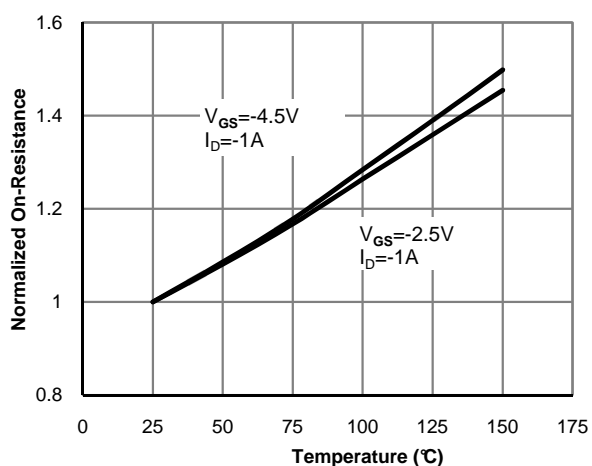


Figure 4: On-Resistance vs. Junction Temperature

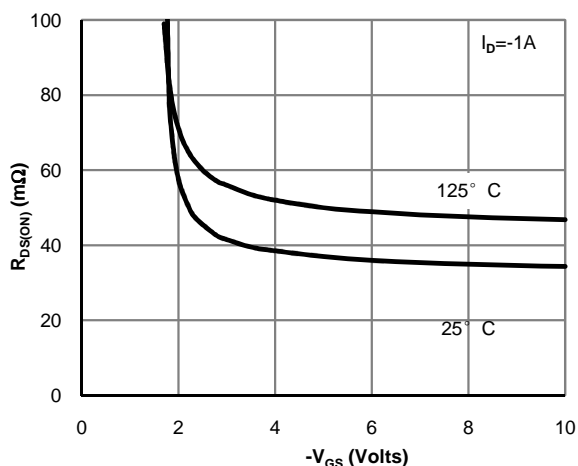


Figure 5: On-Resistance vs. Gate-Source Voltage

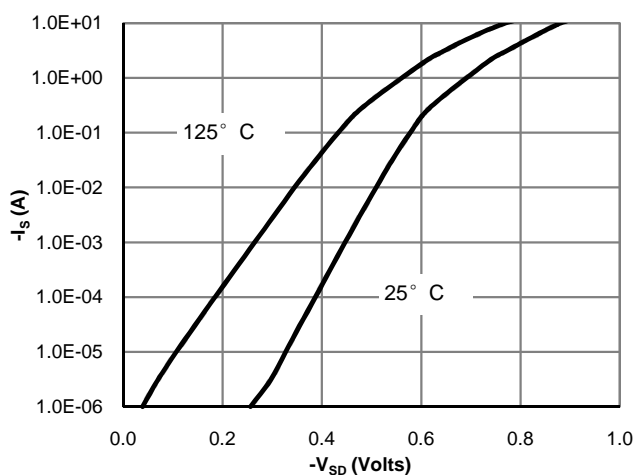


Figure 6: Body-Diode Characteristics

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

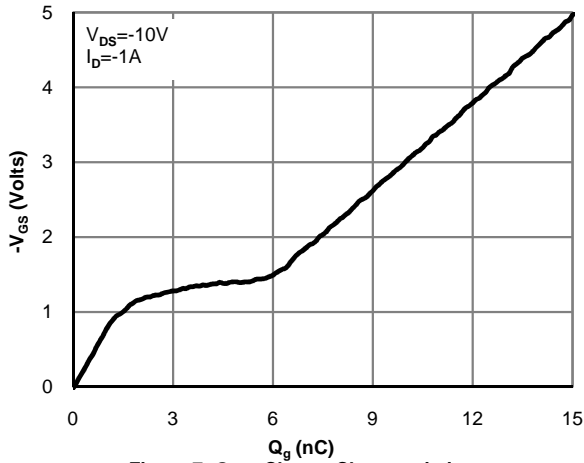


Figure 7: Gate-Charge Characteristics

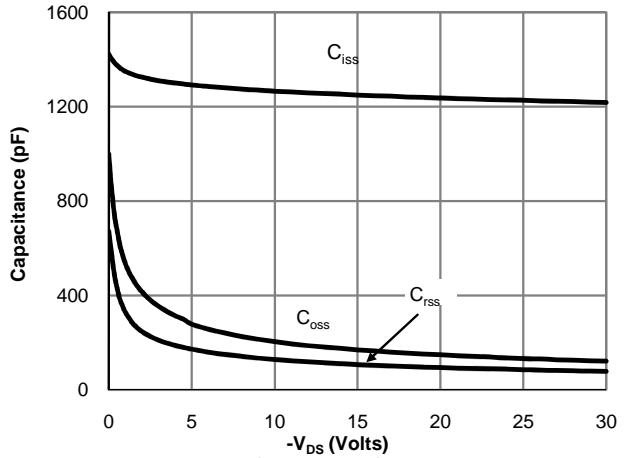


Figure 8: Capacitance Characteristics

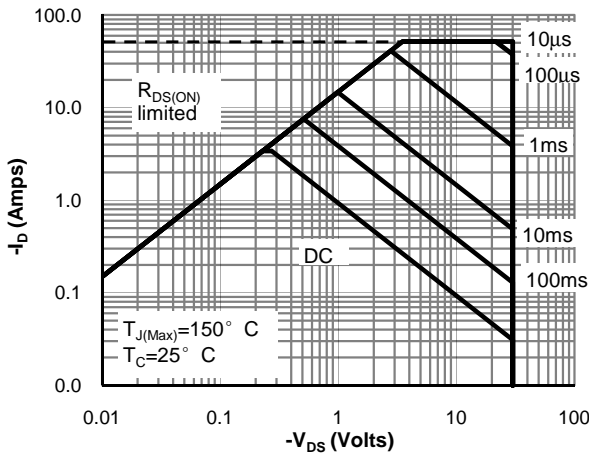


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

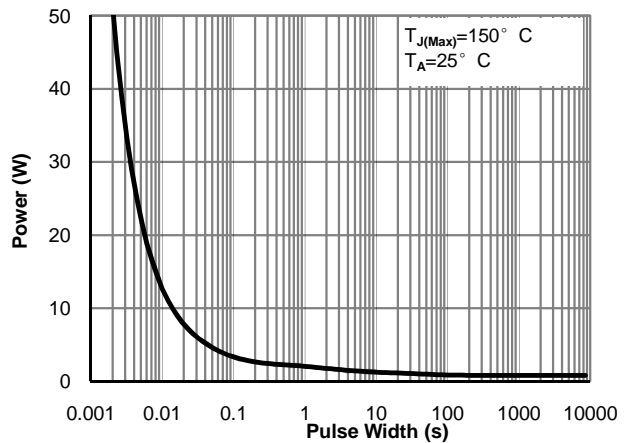


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

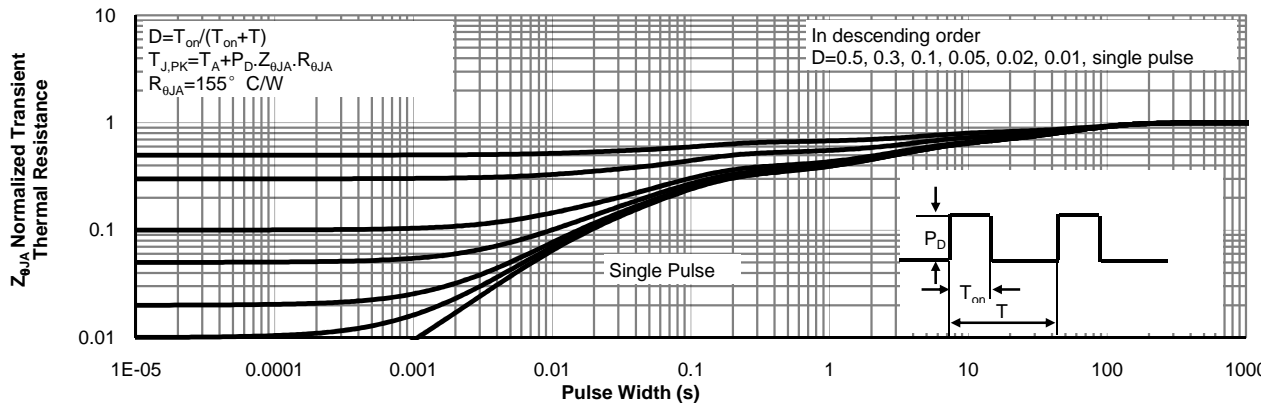


Figure 11: Normalized Maximum Transient Thermal Impedance