# EH4720ETTS-20.000M



EH47 20 ET

Series -RoHS Compliant (Pb-free) 2.5V 4 Pad 2.5mm x 3.2mm Ceramic SMD LVCMOS Oscillator

Frequency Tolerance/Stability ±20ppm Maximum

Operating Temperatu -40°C to +85°C

TS

Pin 1 Connection Tri-State (High Impedance)

Duty Cycle 50 ±10(%)

-20.000M

- Nominal Frequency

20.000MHz

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<b>FI FCTRICA</b>	L SPECIFICATIONS

Operating Temperature Range-40°C to +85°CSupply Voltage2.5Vdc ±5%Input Current6mA Maximum (No Load)Output Voltage Logic High (Voh)90% of Vdd Minimum (IOH = -8mA)Output Voltage Logic Low (Vol)10% of Vdd Maximum (IOL = +8mA)Rise/Fall Time6nSec Maximum (Measured at 20% to 80% of waveform)Duty Cycle50 ±10(%) (Measured at 50% of waveform)Load Drive Capability15pF MaximumOutput Logic TypeCMOSPin 1 ConnectionTri-State (High Impedance)Tri-State Input Voltage (Vih and Vil)90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)Standby Current10µA Maximum (Pin 1 = Ground)Absolute Clock Jitter±100pSec Maximum	Nominal Frequency	20.000MHz
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Supply Voltage2.5Vdc ±5%Input Current6mA Maximum (No Load)Output Voltage Logic High (Voh)90% of Vdd Minimum (IOH = -8mA)Output Voltage Logic Low (Vol)10% of Vdd Maximum (IOL = +8mA)Rise/Fall Time6nSec Maximum (Measured at 20% to 80% of waveform)Duty Cycle50 ±10(%) (Measured at 50% of waveform)Load Drive Capability15pF MaximumOutput Logic TypeCMOSPin 1 ConnectionTri-State (High Impedance)Tri-State Input Voltage (Vih and Vil)90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)Standby Current10µA Maximum (Pin 1 = Ground)Absolute Clock Jitter±100pSec Maximum	Aging at 25°C	±5ppm/Year Maximum
Input Current6mA Maximum (No Load)Output Voltage Logic High (Voh)90% of Vdd Minimum (IOH = -8mA)Output Voltage Logic Low (Vol)10% of Vdd Maximum (IOL = +8mA)Rise/Fall Time6nSec Maximum (Measured at 20% to 80% of waveform)Duty Cycle50 ±10(%) (Measured at 50% of waveform)Load Drive Capability15pF MaximumOutput Logic TypeCMOSPin 1 ConnectionTri-State (High Impedance)Tri-State Input Voltage (Vih and Vil)90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)Standby Current10µA Maximum (Pin 1 = Ground)Absolute Clock Jitter±100pSec Maximum	Operating Temperature Range	-40°C to +85°C
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Rise/Fall Time6nSec Maximum (Measured at 20% to 80% of waveform)Duty Cycle50 ±10(%) (Measured at 50% of waveform)Load Drive Capability15pF MaximumOutput Logic TypeCMOSPin 1 ConnectionTri-State (High Impedance)Tri-State Input Voltage (Vih and Vil)90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)Standby Current10µA Maximum (Pin 1 = Ground)Absolute Clock Jitter±100pSec MaximumStart Up Time10mSec Maximum	Output Voltage Logic High (Voh)	90% of Vdd Minimum (IOH = -8mA)
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Output Logic Type   CMOS     Pin 1 Connection   Tri-State (High Impedance)     Tri-State Input Voltage (Vih and Vil)   90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)     Standby Current   10µA Maximum (Pin 1 = Ground)     Absolute Clock Jitter   ±100pSec Maximum     Start Up Time   10mSec Maximum	Duty Cycle	50 ±10(%) (Measured at 50% of waveform)
Pin 1 Connection   Tri-State (High Impedance)     Tri-State Input Voltage (Vih and Vil)   90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)     Standby Current   10µA Maximum (Pin 1 = Ground)     Absolute Clock Jitter   ±100pSec Maximum     Start Up Time   10mSec Maximum	Load Drive Capability	15pF Maximum
Tri-State Input Voltage (Vih and Vil)   90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)     Standby Current   10µA Maximum (Pin 1 = Ground)     Absolute Clock Jitter   ±100pSec Maximum     Start Up Time   10mSec Maximum	Output Logic Type	CMOS
Impedance)   Standby Current 10μA Maximum (Pin 1 = Ground)   Absolute Clock Jitter ±100pSec Maximum   Start Up Time 10mSec Maximum	Pin 1 Connection	Tri-State (High Impedance)
Absolute Clock Jitter ±100pSec Maximum   Start Up Time 10mSec Maximum	Tri-State Input Voltage (Vih and Vil)	
Start Up Time     10mSec Maximum	Standby Current	10μA Maximum (Pin 1 = Ground)
	Absolute Clock Jitter	±100pSec Maximum
Storage Temperature Range -55°C to +125°C	Start Up Time	10mSec Maximum
	Storage Temperature Range	-55°C to +125°C

## **ENVIRONMENTAL & MECHANICAL SPECIFICATIONS**

ESD Susceptibility	MIL-STD-883, Method 3015, Class 1, HBM: 1500Vdc
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Flammability	UL94-V0
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-883, Method 2002, Condition B
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A

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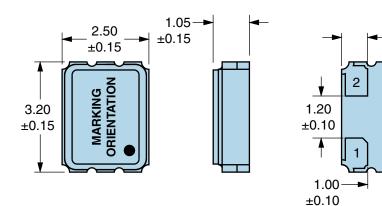
-0.75 ±0.10 (X4)

1.00 ↓ ±0.10

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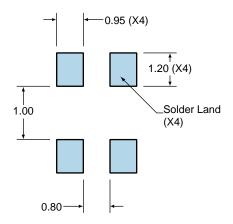
## **MECHANICAL DIMENSIONS (all dimensions in millimeters)**



PIN	CONNECTION
1	Tri-State
2	Case Ground
3	Output
4	Supply Voltage
LINE	MARKING
4	500
1	EPO

### Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are ±0.1

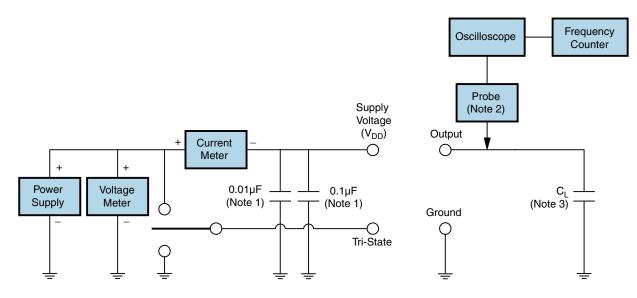
# EH4720ETTS-20.000M



#### **OUTPUT WAVEFORM & TIMING DIAGRAM**



**Test Circuit for CMOS Output** 



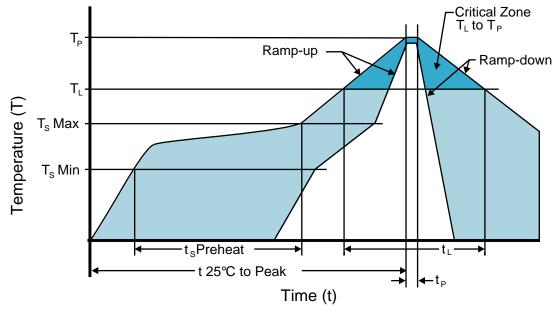
- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.
- Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>300MHz) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance.



# **Recommended Solder Reflow Methods**

EH4720ETTS-20.000M



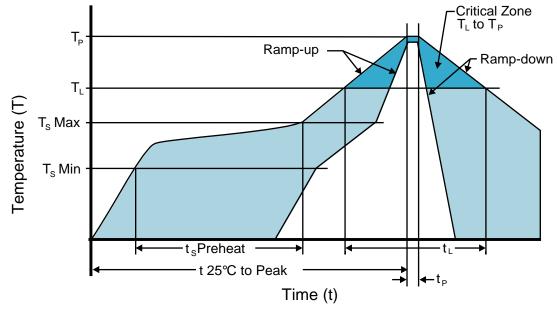
## **High Temperature Infrared/Convection**

$T_s$ MAX to $T_L$ (Ramp-up Rate)	3°C/second Maximum
Preheat	
- Temperature Minimum (T <sub>s</sub> MIN)	150°C
- Temperature Typical (T <sub>s</sub> TYP)	175°C
- Temperature Maximum (T <sub>s</sub> MAX)	200°C
- Time (t <sub>s</sub> MIN)	60 - 180 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	217°C
- Time (t∟)	60 - 150 Seconds
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum
Target Peak Temperature (T <sub>P</sub> Target)	250°C +0/-5°C
Time within 5°C of actual peak (t <sub>P</sub> )	20 - 40 seconds
Ramp-down Rate	6°C/second Maximum
Time 25°C to Peak Temperature (t)	8 minutes Maximum
Moisture Sensitivity Level	Level 1



# **Recommended Solder Reflow Methods**

EH4720ETTS-20.000M



### Low Temperature Infrared/Convection 240°C

T <sub>s</sub> MAX to T <sub>L</sub> (Ramp-up Rate)	5°C/second Maximum
Preheat	
- Temperature Minimum (Ts MIN)	N/A
- Temperature Typical (T <sub>s</sub> TYP)	150°C
- Temperature Maximum (T <sub>s</sub> MAX)	N/A
- Time (t <sub>s</sub> MIN)	60 - 120 Seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/second Maximum
Time Maintained Above:	
- Temperature (T∟)	150°C
- Time (t∟)	200 Seconds Maximum
Peak Temperature (T <sub>P</sub> )	240°C Maximum
Target Peak Temperature (T <sub>P</sub> Target)	240°C Maximum 1 Time / 230°C Maximum 2 Times
Time within 5°C of actual peak (t <sub>p</sub> )	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
Ramp-down Rate	5°C/second Maximum
Time 25°C to Peak Temperature (t)	N/A
Moisture Sensitivity Level	Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

### **High Temperature Manual Soldering**

260°C Maximum for 5 seconds Maximum, 2 times Maximum.