TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74VHC20F,TC74VHC20FT,TC74VHC20FK

### **Dual 4-Input NAND Gate**

The TC74VHC20 is an advanced high speed CMOS 4-INPUT NAND GATE fabricated with silicon gate C2MOS technology.

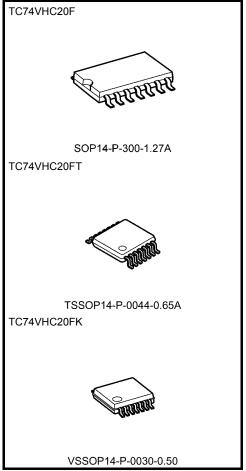
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

The internal circuit is composed of 3 stages including a buffer output, which provide high noise immunity and stable output.

An input protection circuit ensures that 0 to 5.5~V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5~V to 3~V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

#### **Features**

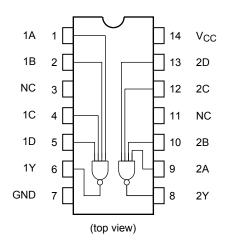
- High speed:  $t_{pd} = 3.3 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 2 \mu A \text{ (max)}$  at  $T_{a} = 25 \text{°C}$
- High noise immunity: V<sub>NIH</sub> = V<sub>NIL</sub> = 28% V<sub>CC</sub> (min)
- Power down protection is provided on all inputs.
- Balanced propagation delays:  $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range:  $V_{CC}$  (opr) = 2 to 5.5 V
- Pin and function compatible with 74ALS20



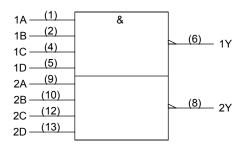
Weight

SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

### **Pin Assignment**



### **IEC Logic Symbol**



**Truth Table** 

	Inp	Output			
Α	В	С	D	Υ	
L	X	X	X	Н	
Х	L	X	X	Н	
Х	X	L	X	Н	
Х	Х	Х	L	Н	
Н	Н	Н	Н	L	

X: Don't care

### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	−0.5 to 7.0	٧
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	lıK	-20	mA
Output diode current	lok	±20	mA
DC output current	I <sub>OUT</sub>	±25	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	PD	180	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



# **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	−40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V <sub>CC</sub> = 3.3 ± 0.3 V)	ns/V	
imput rise and rail tille	ui/uv	0 to 20 (V <sub>CC</sub> = 5 ± 0.5 V)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

### **Electrical Characteristics**

### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Cymbol			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Onit
High-level input voltage	V <sub>IH</sub>	_			1.50 V <sub>CC</sub> × 0.7	1 1	1 1	1.50 V <sub>CC</sub> × 0.7	1 1	V
Low-level input voltage	$V_{IL}$	_				1 1	0.50 V <sub>CC</sub> × 0.3	1 1	0.50 V <sub>CC</sub> × 0.3	٧
High-level output voltage	Voн	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	Ι <sub>ΟΗ</sub> = -50 μΑ	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5	_ _ _	1.9 2.9 4.4	_ _ _	V
			$I_{OH} = -4 \text{ mA}$ $I_{OH} = -8 \text{ mA}$	3.0 4.5	2.58 3.94	_	_	2.48 3.80	_	
Low-level output voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	Ι <sub>ΟL</sub> = 50 μΑ	2.0 3.0 4.5	_ 	0.0 0.0 0.0	0.1 0.1 0.1	_ _ _	0.1 0.1 0.1	V
			$I_{OL} = 4 \text{ mA}$ $I_{OL} = 8 \text{ mA}$	3.0 4.5	_ _	_ _	0.36 0.36	_ _	0.44 0.44	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5	_	_	±0.1	ı	±1.0	μА
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20.0	μА

### AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

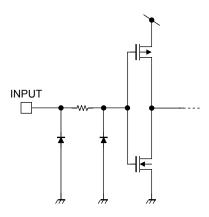
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = −40 to 85°C		Unit	
			V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	
Propagation delay time	<sup>t</sup> pLH t <sub>pHL</sub>	_	3.3 ± 0.3	15	_	4.6	6.6	1.0	8.0	- ns
				50	_	7.1	10.1	1.0	11.5	
			5.0 ± 0.5	15	_	3.3	5.0	1.0	6.0	
				50	_	4.8	7.0	1.0	8.0	
Input capacitance	C <sub>IN</sub>		_		_	4	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>			(Note)	-	19		_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

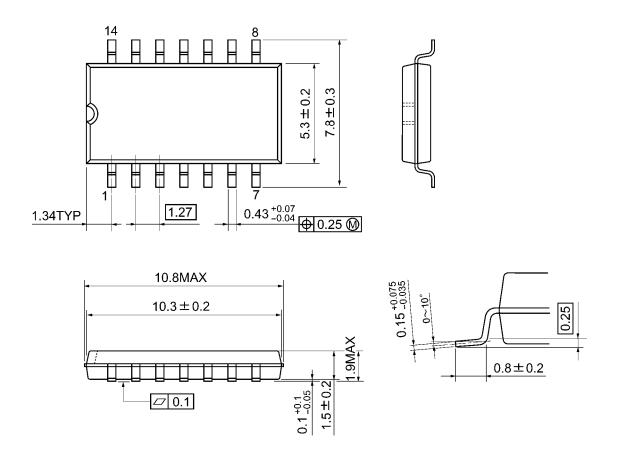
$$I_{CC \text{ (opr)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2 \text{ (per gate)}$$

# **Input Equivalent Circuit**



# **Package Dimensions**

SOP14-P-300-1.27A Unit: mm

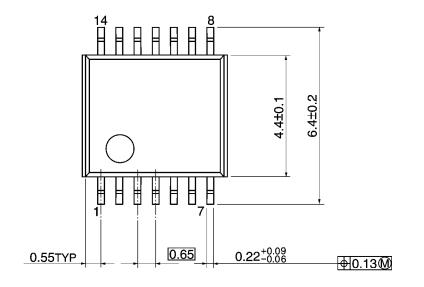


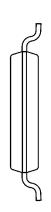
Weight: 0.18 g (typ.)

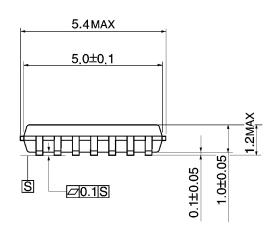
# **Package Dimensions**

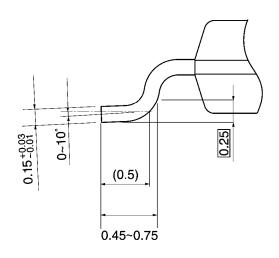
TSSOP14-P-0044-0.65A

Unit: mm





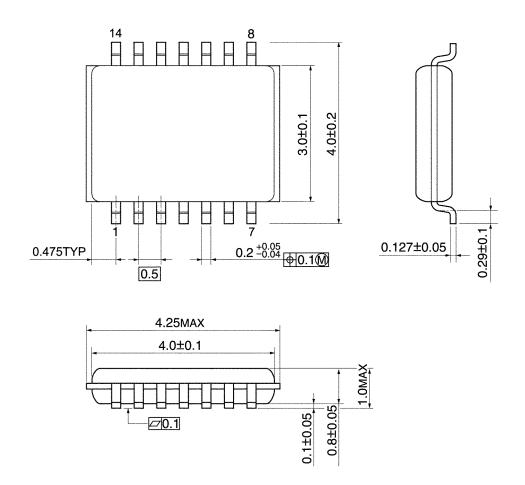




Weight: 0.06 g (typ.)

# **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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