

e-Front runners

#### **FUJI POWER MOSFET**

# Super FAP-E<sup>3</sup> series

### **N-CHANNEL SILICON POWER MOSFET**

#### Features

Maintains both low power loss and low noise Lower R<sub>DS</sub>(on) characteristic

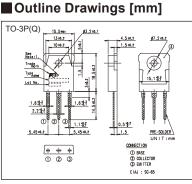
More controllable switching dv/dt by gate resistance Smaller V<sub>GS</sub> ringing waveform during switching Narrow band of the gate threshold voltage  $(3.0\pm0.5V)$ High avalanche durability

#### Applications

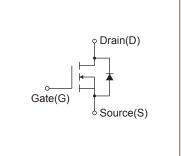
Switching regulators UPS (Uninterruptible Power Supply) **DC-DC converters** 

#### Maximum Ratings and Characteristics

#### • Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Equivalent circuit schematic



| Description   | Symbol | Characteristics | Unit  | Remarks                |
|---|--------|-----------------|-------|------------------------|
| Drain Source Voltone                                    | VDS    | 600             | V     |                        |
| Drain-Source Voltage                                    | VDSX   | 600             | V     | V <sub>GS</sub> = -30V |
| Continuous Drain Current                                | lo     | ±19             | А     |                        |
| Pulsed Drain Current                                    | IDP    | ±76             | А     |                        |
| Gate-Source Voltage                                     | Vgs    | ±30             | V     |                        |
| Repetitive and Non-Repetitive Maximum Avalanche Current | lar    | 19              | А     | Note*1                 |
| Non-Repetitive Maximum Avalanche Energy                 | Eas    | 799             | mJ    | Note*2                 |
| Repetitive Maximum Avalanche Energy                     | Ear    | 31.5            | mJ    | Note*3                 |
| Peak Diode Recovery dV/dt                               | dV/dt  | 6.5             | kV/µs | Note*4                 |
| Peak Diode Recovery -di/dt                              | -di/dt | 100             | A/µs  | Note*5                 |
| Manimum Davies Diagingtian                              | PD     | 2.50            | 10/   | Ta=25°C                |
| Maximum Power Dissipation                               |        | 315             | W     | Tc=25°C                |
|   | Tch    | 150             | °C    |                        |
| Operating and Storage Temperature range                 | Tstg   | -55 to + 150    | °C    |                        |

#### • Electrical Characteristics at Tc=25°C (unless otherwise specified)

| Description                      | Symbol      | Conditions  | Conditions |     | typ. | max.  | Unit |
|----------------------------------|-------------|---|------------|-----|------|-------|------|
| Drain-Source Breakdown Voltage   | BVDSS       | ID=250µA, VGS=0V  |            | 600 | -    | -     | V    |
| Gate Threshold Voltage           | Vgs (th)    | ID=250µA, VDS=VGS   |            | 2.5 | 3.0  | 3.5   | V    |
| Zero Gate Voltage Drain Current  |             | V <sub>DS</sub> =600V, V <sub>GS</sub> =0V                            | Tch=25°C   | -   | -    | 25    | μA   |
|                                  | IDSS        | V <sub>DS</sub> =480V, V <sub>GS</sub> =0V                            | Tch=125°C  | -   | -    | 250   |      |
| Gate-Source Leakage Current      | Igss        | V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V                            |            | -   | 10   | 100   | nA   |
| Drain-Source On-State Resistance | RDS (on)    | ID=9.5A, VGS=10V  |            | -   | 0.31 | 0.365 | Ω    |
| Forward Transconductance         | <b>g</b> fs | ID=9.5A, VDS=25V  |            | 13  | 26   | -     | S    |
| Input Capacitance                | Ciss        | V <sub>DS</sub> =25V<br>V <sub>GS</sub> =0V<br>f=1MHz                 |            | -   | 3600 | 5400  | pF   |
| Output Capacitance               | Coss        |   |            | -   | 310  | 465   |      |
| Reverse Transfer Capacitance     | Crss        |   |            | -   | 23   | 35    |      |
| Turn-On Time                     | td(on)      | Vcc=300V  | -          | 26  | 39   | ns    |      |
|                                  | tr          | V <sub>cs</sub> =10V<br>I <sub>D</sub> =9.5A<br>R <sub>cs</sub> =8.2Ω |            | -   | 13   |       | 20   |
| Turn-Off Time                    | td(off)     |   |            | -   | 150  |       | 225  |
|                                  | tf          |   |            | -   | 20   |       | 30   |
| Total Gate Charge                | QG          | V <sub>cc</sub> =300V<br>I <sub>D</sub> =19A<br>V <sub>GS</sub> =10V  |            | -   | 105  | 160   | nC   |
| Gate-Source Charge               | QGS         |   |            | -   | 23   | 35    |      |
| Gate-Drain Charge                | QGD         |   |            | -   | 30   | 45    |      |
| Avalanche Capability             | lav         | L=1.71mH, T <sub>ch</sub> =25°C                                       |            | 19  | -    | -     | A    |
| Diode Forward On-Voltage         | Vsd         | IF=19A, VGS=0V, Tch=25°C  |            | -   | 0.90 | 1.35  | V    |
| Reverse Recovery Time            | trr         | I <sub>F</sub> =19A, V <sub>GS</sub> =0V                              |            | -   | 0.6  | -     | μs   |
| Reverse Recovery Charge          | Qrr         | -di/dt=100A/µs, Tch=25°C  |            | -   | 10   | -     | μC   |

#### Thermal Characteristics

| Description        | Symbol     | Test Conditions    | min. | typ. | max. | Unit |
|--------------------|------------|--------------------|------|------|------|------|
| Thermal resistance | Rth (ch-c) | Channel to case    |      |      | 0.40 | °C/W |
|                    | Rth (ch-a) | Channel to ambient |      |      | 50.0 | °C/W |

Note \*1 : Tch≤150°C

Note 1 : Italia 50 C, IAs=8A, L=22.9mH, Vcc=60V, R<sub>G</sub>=50Ω EAs limited by maximum channel temperature and avalanche current. See to 'Avalanche Energy' graph.

Note \*3 : Repetitive rating : Pulse width limited by maximum channel temperature.

See to the 'Transient Themal impeadance' graph.

Note \*4 : IFS-ID, -di/dt=100A/µs, VccSBVDss, TchS150°C. Note \*5 : IFS-ID, dv/dt=5.0kV/µs, VccSBVDss, TchS150°C.

t= 1µs

10µs

100µs

1ms

DC

10<sup>3</sup>

9 10

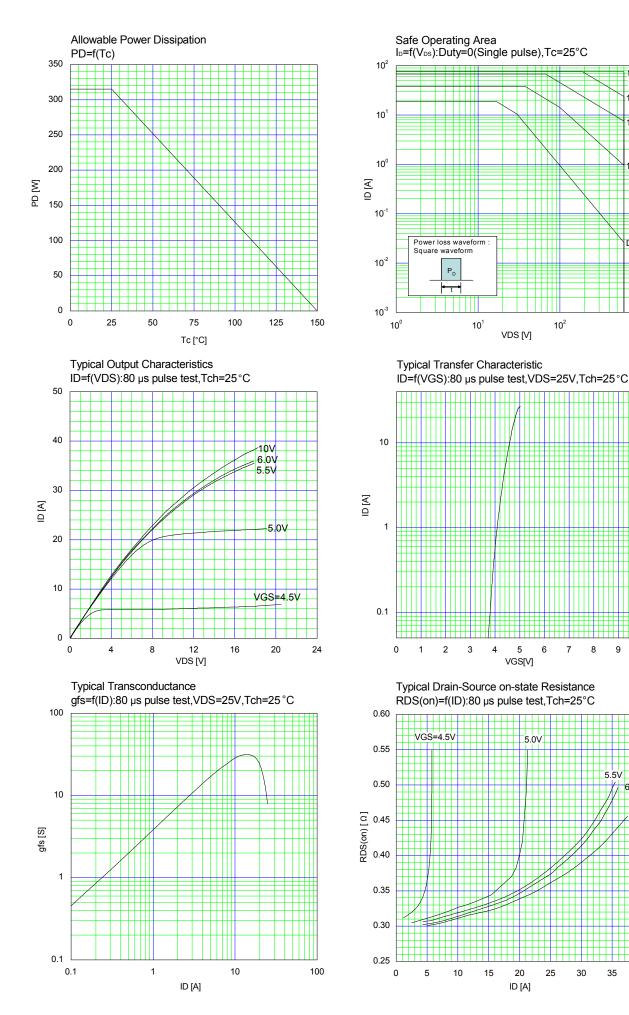
5 5V

35

40

6.0\

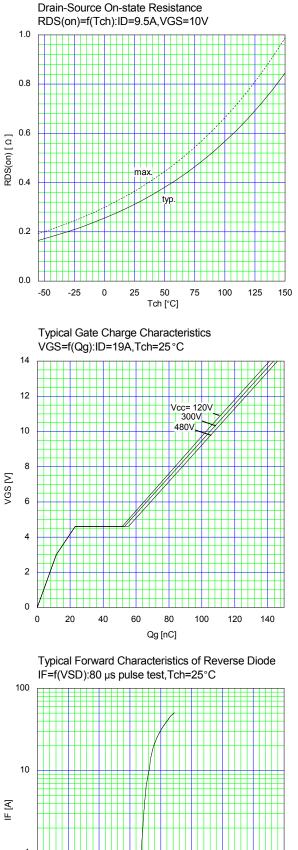
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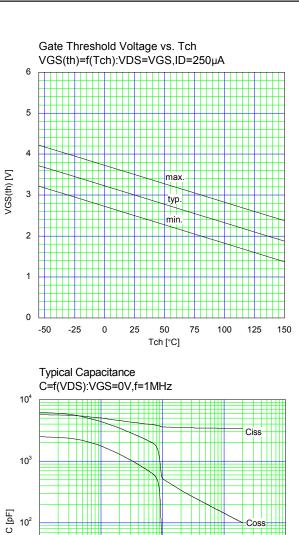


Crss

10<sup>3</sup>

10<sup>2</sup>





Typical Switching Characteristics vs. ID t=f(ID):Vcc=300V,VGS=10V,RG=8.2Ω

10<sup>1</sup>

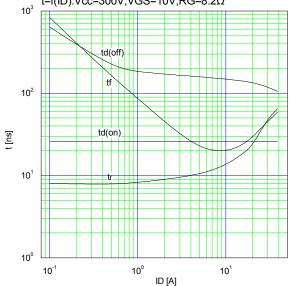
VDS [V]

10<sup>0</sup>

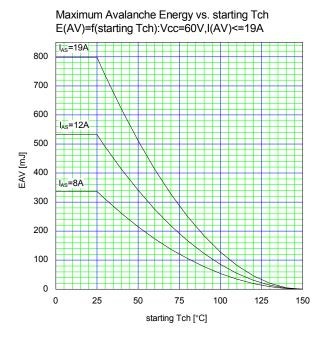
10<sup>1</sup>

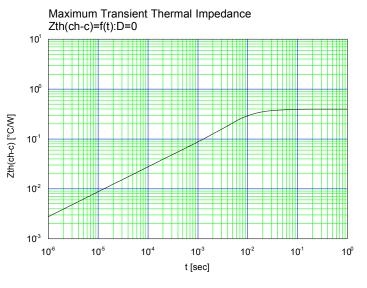
10<sup>°</sup>

10<sup>-1</sup>



1 0.1 0.00 0.25 0.50 0.75 1.00 1.25 1.50 1.75 2.00 VSD [V]





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## WARNING

| WARNING  |
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