TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process)

2SC3303

Industrial Applications

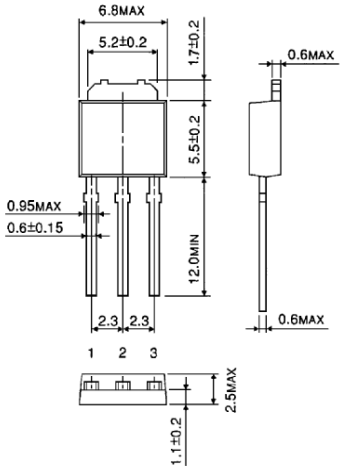
High Current Switching Applications
DC-DC Converter Applications

- Low collector saturation voltage: $V_{CE\ (sat)}$ = 0.4 V (max) (I_C = 3 A)
- High speed switching time: t_{stg} = 1.0 μs (typ.)

Maximum Ratings (Ta = 25°C)

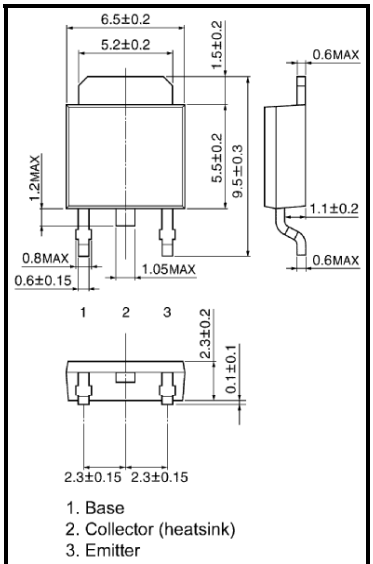
Characteristics		Symbol	Rating	Unit
Collector-base voltage		V_{CBO}	100	V
Collector-emitter voltage		V_{CEO}	80	V
Emitter-base voltage		V_{EBO}	7	V
Collector current	DC	I_C	5	A
	Pulse	I_{CP}	8	
Base current		I_B	1	A
Collector power dissipation	Ta = 25°C	P_C	1.0	W
	Tc = 25°C		20	
Junction temperature		T_j	150	°C
Storage temperature range		T_{stg}	−55 to 150	°C

Unit: mm



JEDEC	—
JEITA	—
TOSHIBA	2-7B1A

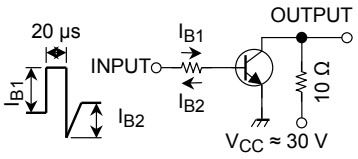
Weight: 0.36 g (typ.)



JEDEC	—
JEITA	—
TOSHIBA	2-7J1A

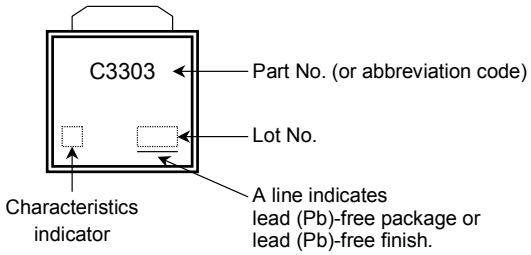
Weight: 0.36 g (typ.)

Electrical Characteristics (Ta = 25°C)

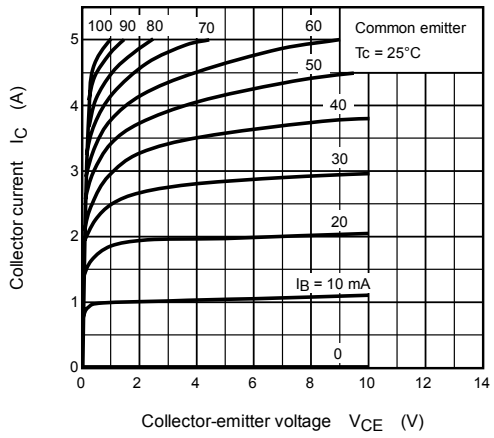
Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Collector cut-off current		I_{CBO}	V_{CB} = 100 V, I_E = 0	—	—	1	μA
Emitter cut-off current		I_{EBO}	V_{EB} = 7 V, I_C = 0	—	—	1	μA
Collector-emitter breakdown voltage		$V_{(BR)\ CEO}$	I_C = 10 mA, I_B = 0	80	—	—	V
DC current gain		$h_{FE\ (1)}$ (Note)	V_{CE} = 1 V, I_C = 1 A	70	—	240	
		$h_{FE\ (2)}$	V_{CE} = 1 V, I_C = 3 A	40	—	—	
Collector-emitter saturation voltage		$V_{CE\ (sat)}$	I_C = 3 A, I_B = 0.15 A	—	0.2	0.4	V
Base-emitter saturation voltage		$V_{BE\ (sat)}$	I_C = 3 A, I_B = 0.15 A	—	0.9	1.2	V
Transition frequency		f_T	V_{CE} = 4 V, I_C = 1 A	—	120	—	MHz
Collector output capacitance		C_{ob}	V_{CB} = 10 V, I_E = 0, f = 1 MHz	—	80	—	pF
Switching time	Turn-on time	t_{on}		—	0.2	—	μs
	Storage time	t_{stg}		—	1.0	—	
	Fall time	t_f	I_{B1} = −I_{B2} = 0.15 A, DUTY CYCLE ≤ 1%	—	0.1	—	

Note: $h_{FE\ (1)}$ classification O: 70 to 140, Y: 120 to 240

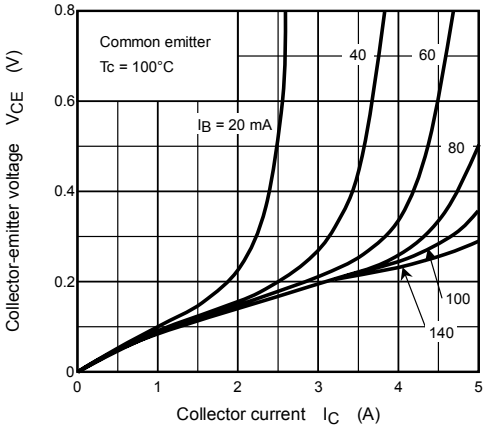
Marking



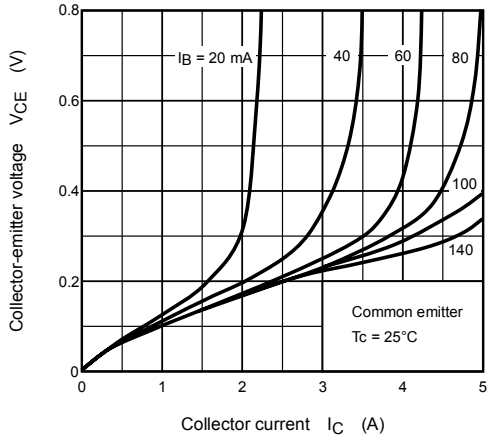
$I_C - V_{CE}$



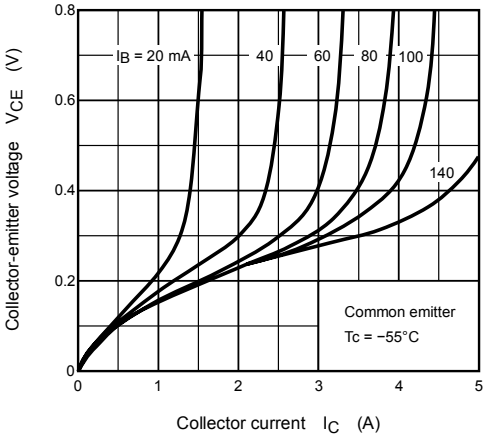
$V_{CE} - I_C$



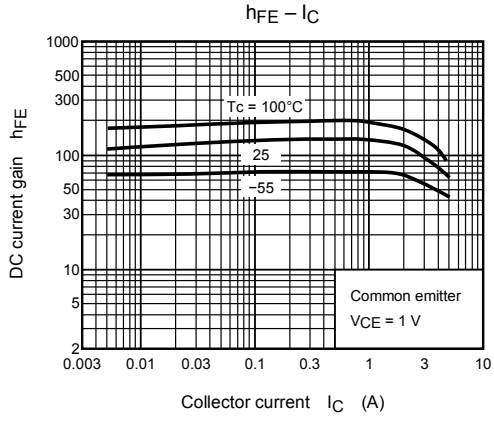
$V_{CE} - I_C$



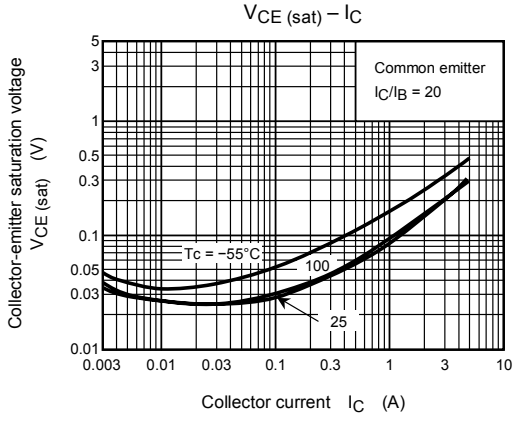
$V_{CE} - I_C$



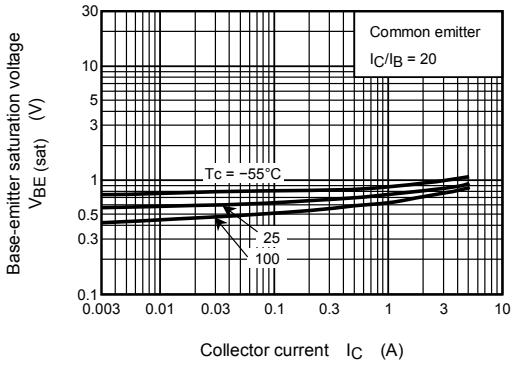
$h_{FE} - I_C$



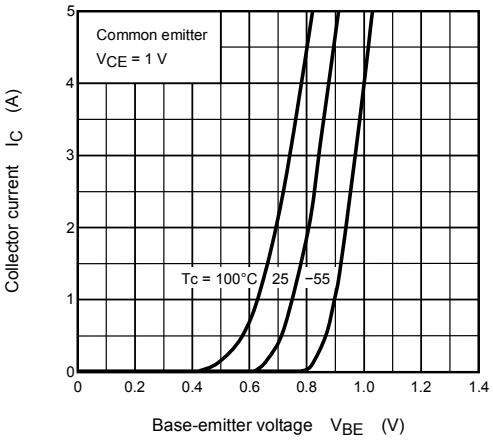
$V_{CE\ (sat)} - I_C$



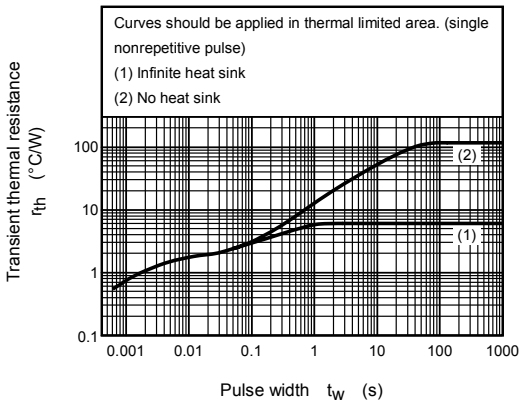
$V_{BE\ (sat)} - I_C$



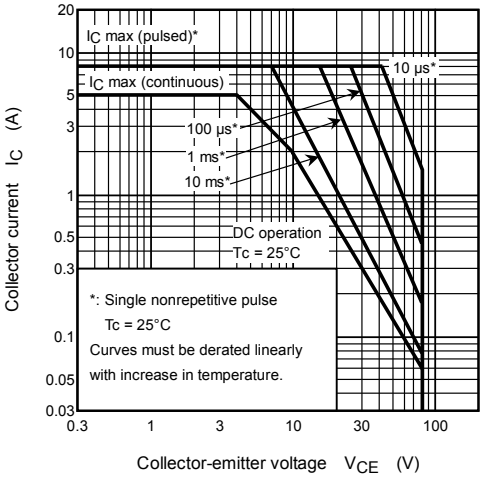
$I_C - V_{BE}$



$r_{th} - t_w$



Safe Operating Area



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