

Silicon NPN Power Transistors

2SC2516

DESCRIPTION

- With TO-220C package
- Low collector saturation voltage
- Wide area of safe operation

APPLICATIONS

- Switching regulators
- DC-DC converters
- High frequency power amplifiers

PINNING

PIN	DESCRIPTION
1	Base
2	Collector;connected to mounting base
3	Emitter

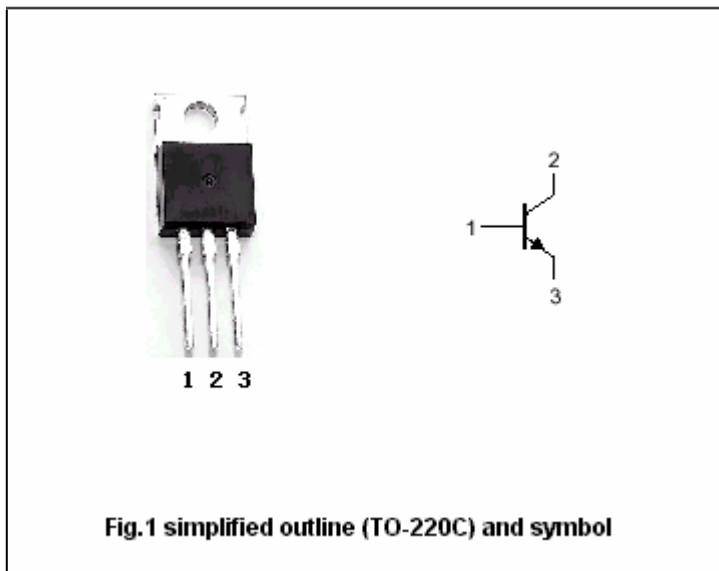


Fig.1 simplified outline (TO-220C) and symbol

Absolute maximum ratings(Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	150	V
V _{CEO}	Collector-emitter voltage	Open base	60	V
V _{EBO}	Emitter-base voltage	Open collector	12	V
I _C	Collector current		5	A
I _{CM}	Collector current-peak		10	A
I _B	Base current		2.5	A
P _T	Total power dissipation	T _a =25°C	1.5	W
		T _C =25°C	30	
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

Silicon NPN Power Transistors

2SC2516

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =3.0A ; I _B =0; L=1mH	60			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =3A; I _B =0.3A			0.6	V
V _{BEsat}	Base-emitter saturation voltage	I _C =3A; I _B =0.3A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =100V ; I _E =0			10	μA
I _{CEx}	Collector cut-off current	V _{CE} =100V; V _{BE} =-1.5V T _a =125°C			10 1	μA mA
I _{EBO}	Emitter cut-off current	V _{EB} =10V ; I _C =0			10	μA
h _{FE-1}	DC current gain	I _C =0.2 A ; V _{CE} =5V	40			
h _{FE-2}	DC current gain	I _C =3 A ; V _{CE} =5V	40		200	

Switching times

t _{on}	Turn-on time	I _C =3A; I _{B1} =-I _{B2} =0.3A R _L =17Ω; V _{CC} =50V			0.5	μs
t _s	Storage time				2.5	μs
t _f	Fall time				0.5	μs

◆ h_{FE-2} Classifications

M	L	K
40-80	60-120	100-200

Silicon NPN Power Transistors

2SC2516

PACKAGE OUTLINE



Fig.2 Outline dimensions (unindicated tolerance: ±0.10 mm)