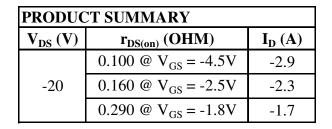
P-Channel 20-V (D-S) MOSFET

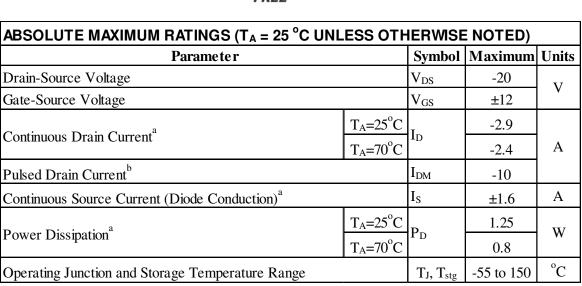
These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology





G



THERMAL RESISTANCE RATINGS								
Parameter	Symbol	Maximum	Units					
Maximum Junction-to-Ambient ^a	t <= 5 sec	D	100	°C/W				
	Steady-State	R _{THJA}	166					

Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

AM2303P

SPECIFICATIONS ($T_A = 25^{\circ}C$ UNLESS OTHERWISE NOTED)									
Parameter	Chl		Limits			T T •4			
	Symbol	Test Conditions	Min	Тур	Max	Unit			
Static									
Gate-Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}, I_D = -250 \text{ uA}$	-0.4			V			
Gate-Body Leakage	Igss	$V_{DS} = 0 V, V_{GS} = +/-12 V$			±100	nA			
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -16 V, V_{GS} = 0 V$			-1	uA			
	IDSS	$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			-10	uA			
On-State Drain Current ^A	I _{D(on)}	$V_{DS} = -5 V$, $V_{GS} = -4.5 V$	-3			Α			
Drain-Source On-Resistance ^A		$V_{GS} = -4.5 \text{ V}, I_D = -2.9 \text{ A}$			0.100	Ω			
	rDS(on)	$V_{GS} = -2.5 \text{ V}, I_D = -2.3 \text{ A}$			0.160				
		$V_{GS} = -1.8 \text{ V}, I_D = -1.7 \text{ A}$			0.290				
Forward Tranconductance ^A	g _{fs}	$V_{DS} = -5 \text{ V}, I_D = -2.8 \text{ A}$		3		S			
Diode Forward Voltage	V _{SD}	$I_S = -1.6 A, V_{GS} = 0 V$		-0.7		V			
Dynamic ^b									
Total Gate Charge	Qg			6		nC			
Gate-Source Charge	Qgs	$V_{DS} = -5 V$, $V_{GS} = -4.5 V$, $I_{D} = -2.6 A$		0.3					
Gate-Drain Charge	Qgd	ID = -2.0 A		1.3					
Input Capacitance	Ciss			395		pF			
Output Capacitance	Coss	P-Channel VDS=-15V, VGS=0V, f=1MHz		130					
Reverse Transfer Capacitance	Crss	1–11112		33					
Turn-On Delay Time	td(on)			6.5		ns			
Rise Time	tr	$V_{DD} = -5 V, R_L = 5 OHM,$		3					
Turn-Off Delay Time	td(off)	$V_{GEN} = -4.5 \text{ V}, R_G = 6 \text{ OHM}$		31					
Fall-Time	tf			4					

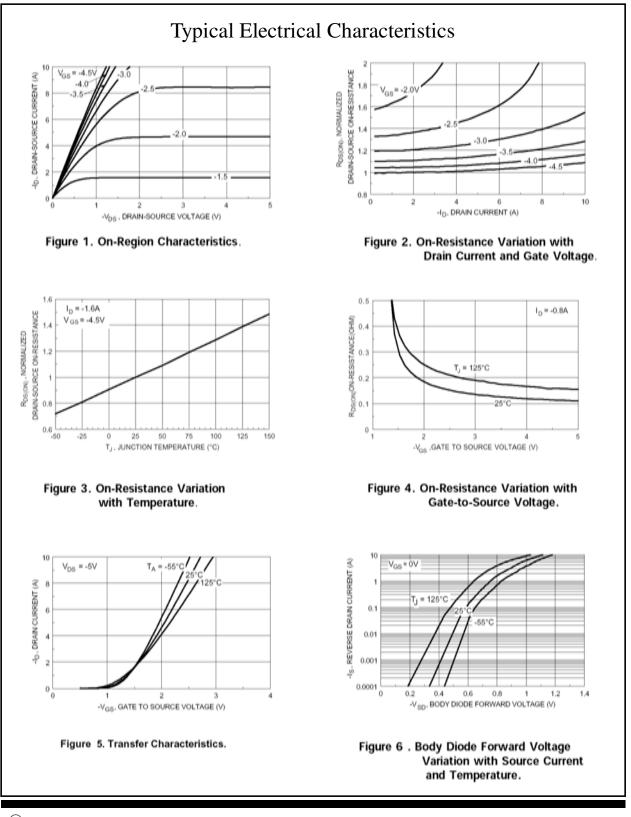
Notes

a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.

b. Guaranteed by design, not subject to production testing.

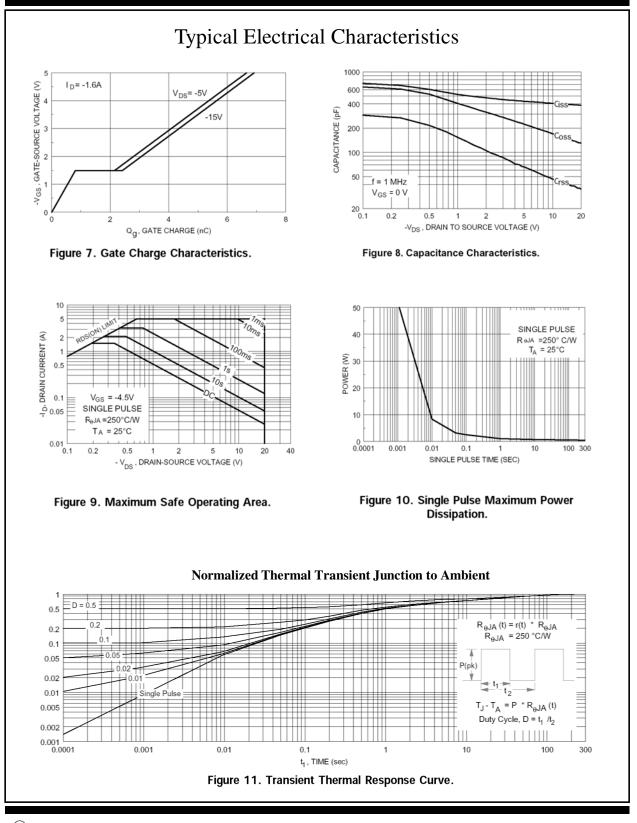
Analog Power (APL) reserves the right to make changes without further notice to any products herein. APL makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does APL assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in APL data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. APL does not convey any license under its patent rights nor the rights of others. APL products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the APL product could create a situation where personal injury or death may occur. Should Buyer purchase or use APL products for any such unintended or unauthorized application, Buyer shall indemnify and hold APL and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that APL was negligent regarding the design or manufacture of the part. APL is an Equal Opportunity/Affirmative Action Employer.

AM2303P



Publication Order Number: DS-AM2303_B

AM2303P



Publication Order Number: DS-AM2303_B

Package Information

