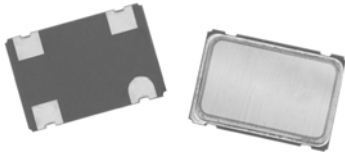


Surface Mount Oscillator



The XOSM-57 series is an ultra miniature package clock oscillator with dimensions 7.0 mm x 5.0 mm x 1.9 mm. It is mainly used in portable PC and telecommunication devices and equipment.

FEATURES

- Size: 7.0 x 5.0 x 1.9 (mm)
- Miniature package
- Tri-state enable/disable
- TTL/HCMOS compatible
- Tape and reel
- I_R re-flow
- 5 V input voltage
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition



RoHS
COMPLIANT
HALOGEN
FREE

STANDARD ELECTRICAL SPECIFICATIONS			
PARAMETER	SYMBOL	CONDITION	VALUE
Frequency range	F_O	-	1.500 MHz to 100.000 MHz
Frequency stability ⁽¹⁾		all conditions	± 25 ppm, ± 50 ppm, ± 100 ppm
Operating temperature range	T_{OPR}	-	0 °C to 70 °C
			- 40 °C to + 85 °C (option)
Storage temperature range	T_{STG}	-	- 55 °C to + 125 °C
Power supply voltage	V_{DD}	-	5.0 V \pm 10 %
Aging (first year)		25 °C \pm 3 °C	± 5 ppm
Supply current	I_{DD}	1.500 MHz to 20.000 MHz	20 mA max.
		20.001 MHz to 50.000 MHz	35 mA max.
		30.001 MHz to 100.000 MHz	45 mA max.
Output symmetry	Sym	at $\frac{1}{2} V_{DD}$	40 %/60 % (45 %/55 % option)
Rise/fall time	t_r/t_f	1.500 MHz to 67.000 MHz	10 ns
		67.001 MHz to 100.000 MHz	3 ns
Output voltage	V_{OH}	-	90 % V_{DD} min.
	V_{OL}	-	10 % V_{DD} max.
Output load		1.500 MHz to 67.000 MHz	10 TTL or 50 pF max.
		67.001 MHz to 100.000 MHz	15 pF max.
Start-up time	t_s	-	10 ms max.
Pin 1, tri-state function		-	pin 1 = H or open (output active at pin 3) pin 1 = L (high impedance at pin 3)

Note

⁽¹⁾ Include: 25 °C tolerance, operating temperature range, input voltage change, aging, load change, shock vibration

DIMENSIONS in inches [millimeters]											
	<table border="1"> <thead> <tr> <th>PIN</th> <th>CONNECTION</th> </tr> </thead> <tbody> <tr> <td>#1</td> <td>TRI-STATE/NC</td> </tr> <tr> <td>#2</td> <td>GND</td> </tr> <tr> <td>#3</td> <td>OUTPUT</td> </tr> <tr> <td>#4</td> <td>V_{DD}</td> </tr> </tbody> </table>	PIN	CONNECTION	#1	TRI-STATE/NC	#2	GND	#3	OUTPUT	#4	V_{DD}
PIN	CONNECTION										
#1	TRI-STATE/NC										
#2	GND										
#3	OUTPUT										
#4	V_{DD}										

Note

- A 0.01 μ F bypass capacitor should be placed between V_{DD} (pin 4) and GND (pin 2) to minimize power supply line noise



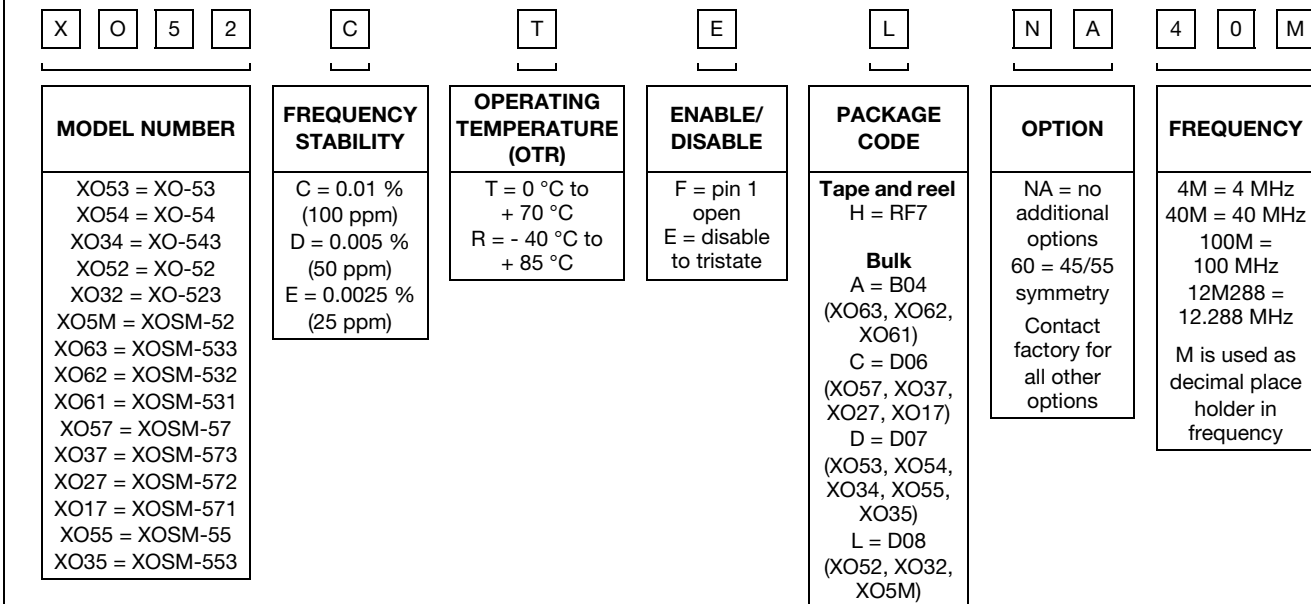
ORDERING INFORMATION

XOSM-57	B	R	E	50M	e4
MODEL	FREQUENCY STABILITY AA = 0.0025 % (25 ppm) A = 0.005 % (50 ppm) B = 0.01 % (100 ppm) standard	OTR blank = standard R = - 40 °C to + 85 °C	ENABLE/DISABLE E = disable to tri-state	FREQUENCY/MHz	JEDEC LEAD (Pb)-FREE standard

GLOBAL PART NUMBER



GLOBAL PART NUMBERING



Example: XO52CTELNA40M

PART MARKING

Line 1: M2804XXXXX (part number)
 Line 2: XX.XXXXX (frequency)
 Line 3: yywwvv (date/factory code)



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