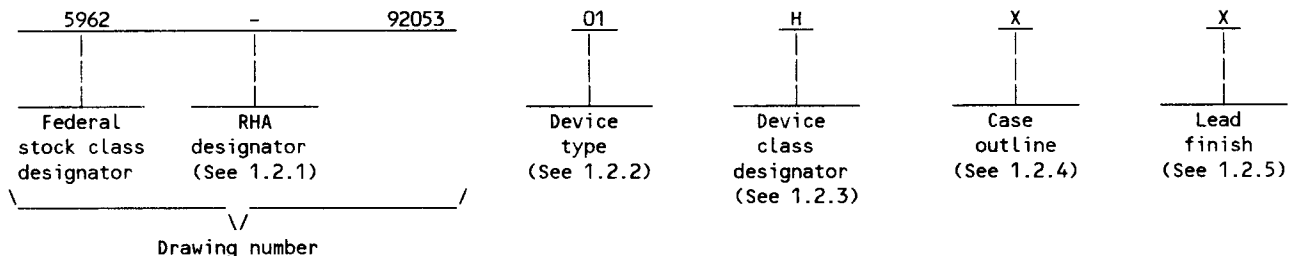


1. SCOPE

1.1 Scope. This drawing forms a part of a one part - one part number documentation system (see 6.6 herein). This drawing describes device requirements for hybrid microcircuits to be processed in accordance with MIL-H-38534. Two product assurance classes, military high reliability (device class H) and space application (device class K) and a choice of case outlines and lead finishes are available and are reflected in the Part or Identifying Number (PIN). When available, a choice of radiation hardness assurance levels are reflected in the PIN.

1.2 PIN. The PIN shall be as shown in the following example:



1.2.1 Radiation hardness assurance (RHA) designator. Device classes H and K RHA marked devices shall meet the MIL-H-38534 specified RHA levels and shall be marked with the appropriate RHA designator. A dash (-) indicates a non-RHA device.

1.2.2 Device type(s). The device type(s) shall identify the circuit function as follows:

Device type	Generic number	Circuit function	Frequency	Accuracy ±1 LSB	BIT/VEL
01	SDC14610-112	14-bit, 11.8 V S/D converter	400 Hz	4 min	VEL
02	SDC14610T-112	14-bit, 11.8 V S/D converter	400 Hz	4 min	BIT
03	SDC14611-112	14-bit, 11.8 V R/D converter	400 Hz	4 min	VEL
04	SDC14611T-112	14-bit, 11.8 V R/D converter	400 Hz	4 min	BIT
05	SDC14615-112	16-bit, 11.8 V S/D converter	400 Hz	4 min	VEL
06	SDC14615T-112	16-bit, 11.8 V S/D converter	400 Hz	4 min	BIT
07	SDC14616-112	16-bit, 11.8 V R/D converter	400 Hz	4 min	VEL
08	SDC14616T-112	16-bit, 11.8 V R/D converter	400 Hz	4 min	BIT
09	SDC14615-114	16-bit, 11.8 V S/D converter	400 Hz	2 min	VEL
10	SDC14615T-114	16-bit, 11.8 V S/D converter	400 Hz	2 min	BIT
11	SDC14616-114	16-bit, 11.8 V R/D converter	400 Hz	2 min	VEL
12	SDC14616T-114	16-bit, 11.8 V R/D converter	400 Hz	2 min	BIT

1.2.3 Device class designator. This device class designator shall be a single letter identifying the product assurance level as follows:

Device class

H or K

Device requirements documentation

Certification and qualification to MIL-H-38534

1.2.4 Case outline(s). The case outline(s) shall be as designated in MIL-STD-1835 and as follows:

Outline letter	Descriptive designator	Terminals	Package style
X	See figure 1	36	Dual-in-line

1.2.5 Lead finish. The lead finish shall be as specified in MIL-H-38534 for classes H and K. Finish letter "X" shall not be marked on the microcircuit or its packaging. The "X" designation is for use in specifications when lead finishes A, B, and C are considered acceptable and interchangeable without preference.

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1.3 Absolute maximum ratings. 1/

Positive supply voltage (V_{CC})	+7.0 V dc
Negative supply voltage (V_{EE})	-7.0 V dc
Reference input voltage	35 V rms
Digital input voltage range	-0.3 V dc to +7.0 V dc
Power dissipation, $T_A = +125^\circ\text{C}$ (P_D)	550 mW
Storage temperature range	-65°C to +150°C
Lead temperature (soldering, 10 seconds)	+300°C
Thermal resistance, junction-to-case (Θ_{JC})	8.0°C/W
Thermal resistance, junction-to-ambient (Θ_{JA})	20°C/W

1.4 Recommended operating conditions.

Positive supply voltage range (V_{CC})	+4.75 V dc to +5.25 V dc
Negative supply voltage range (V_{EE})	-4.75 V dc to -5.25 V dc
Reference input voltage range	2.0 V rms to 35 V rms
Reference input carrier frequency range	360 Hz to 5000 Hz
Signal input voltage range	10.62 V rms to 12.98 V rms
Ambient operating temperature range (T_A)	-55°C to +125°C

2. APPLICABLE DOCUMENTS

2.1 Government specifications, standards, and handbook. Unless otherwise specified, the following specifications, standards, and handbook of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATIONS

MILITARY

- MIL-M-38510 - Microcircuits, General Specification for.
- MIL-H-38534 - Hybrid Microcircuits, General Specification for.

STANDARDS

MILITARY

- MIL-STD-480 - Configuration Control-Engineering Changes, Deviations and Waivers.
- MIL-STD-883 - Test Methods and Procedures for Microelectronics.
- MIL-STD-1835 - Microcircuit Case Outlines.

HANDBOOK

MILITARY

- MIL-HDBK-780 - Standardized Military Drawings.

(Copies of the specifications, standards, and handbook required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

1/ Stresses above the absolute maximum rating may cause permanent damage to the device. Extended operation at the maximum levels may degrade performance and affect reliability.

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3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with MIL-H-38534 and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-H-38534 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.4 herein and figure 1.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 2.

3.2.3 Block diagram. The block diagram shall be as specified on figure 3.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full specified operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-H-38534. The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in QML-38534.

3.6 Manufacturer eligibility. In addition to the general requirements of MIL-H-38534, the manufacturer of the part described herein shall maintain the electrical test data (variables format) from the initial quality conformance inspection group A lot sample, produced on the certified line, for each device type listed herein. The data should also include a summary of all parameters manually tested, and for those which, if any, are guaranteed. This data shall be maintained under document revision level control by the manufacturer and be made available to the preparing activity (DESC-ECT) upon request.

3.7 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to supply to this drawing. The certificate of compliance submitted to DESC-ECT shall affirm that the manufacturer's product meets the requirements of MIL-H-38534 and the requirements herein.

3.8 Certificate of conformance. A certificate of conformance as required in MIL-H-38534 shall be provided with each lot of microcircuits delivered to this drawing.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-H-38534.

4.2 Screening. Screening shall be in accordance with MIL-H-38534. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).

(2) T_A as specified in accordance with table I of method 1015 of MIL-STD-883.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$ $V_{CC} = +5 \text{ V dc}, V_{EE} = -5 \text{ V dc}$ unless otherwise specified	Group A subgroups	Device type	Limits		Unit
					Min	Max	
Resolution <u>1/</u>	RES		7,8A,8B	01 - 04	14		Bits
				05 - 12	16		
Output accuracy <u>2/</u>	AOUT		7,8A,8B	01 - 04	-4	+4	LSB
				05 - 08	-13	+13	
				09 - 12	-7	+7	
Accuracy repeatability <u>2/ 3/</u>	AR		7,8A,8B	ALL	-1.0	+1.0	LSB
Reference input impedance <u>3/</u>	Z_{IN1}	Single ended	4,5,6	ALL	60		k Ω
		Differential			120		
Reference input common mode range <u>3/</u>	V_{CM1}		4,5,6	ALL	-50	+50	V pk
Signal input common mode range <u>3/</u>	V_{CM2}		4,5,6	ALL	-30	+30	V pk
Signal input impedance <u>3/</u>	Z_{IN2}	Line-to-line	4,5,6	01,02, 05,06, 09,10	52		k Ω
		Line-to-ground			34		
		Single ended		03,04, 07,08, 11,12	70		
		Differential			140		
Digital output low voltage	V_{OL}	$I_{OL} = -1.6 \text{ mA}$	1,2,3	ALL		0.4	V
Digital output high voltage	V_{OH}	$I_{OH} = 0.4 \text{ mA}$	1,2,3	ALL	2.8		V

See footnotes at end of table.

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TABLE I. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C ≤ T _A ≤ +125°C V _{CC} = +5 V dc, V _{EE} = -5 V dc unless otherwise specified		Group A subgroups	Device type	Limits		Unit
						Min	Max	
Output leakage current	I _{OZ}			1,2,3	ALL	-60	+60	μA
Digital input high voltage	V _{IH}	<u>Digital inputs</u> INH, EL, and EM	V _{IN} = 2.0 V	1,2,3	ALL	pass/ fail		
Digital input low voltage	V _{IL}		V _{IN} = 0.8 V	1,2,3	ALL		pass/ fail	
Digital input current	I _{IN}	Internal pull-up		4,5,6	ALL		-10	μA
Inhibit voltage <u>1/</u>	V _{INH}	No digital angles change while INH is logic 0 and analog input is rotating		7,8A,8B	ALL		0.8	V
Enable voltage <u>1/</u>	V _E	EM controls output bits 1 - 8 and EL controls output bits 9 - 14 for (device types 01 - 04) and bits 9 - 16 for device types 05 - 12)		7,8A,8B	ALL		0.8	V
Disable voltage (high impedance) <u>1/</u>	V _D			7,8A,8B	ALL	2.0		V
Positive supply current	I _{CC}	V _{CC} = +5.0 V		1,2,3	ALL		+51	mA
Negative supply current	I _{EE}	V _{EE} = -5.0 V		1,2,3	ALL		-51	mA
Analog velocity output voltage	V _{OUT}	<u>4/</u>		7,8A,8B	ALL	3.24	4.00	V
Bandwidth <u>1/</u>	BW			7,8A,8B	ALL	72	134	Hz

1/ These parameters are tested on a go-no-go basis only or in conjunction with other measured parameters and are not directly testable.

2/ Output accuracy is measured at angles from 0° to 180°, in 15° increments, and at 225°, 270°, and 315°.

3/ Parameters shall be tested as part of device initial characterization and after design and process changes. Parameters shall be guaranteed to the limits specified in table I for all lots not specifically tested.

4/ Analog output voltage is tested at 8 revolutions per second for device types 01 through 04 and at 2 revolutions per second for device types 05 through 12.

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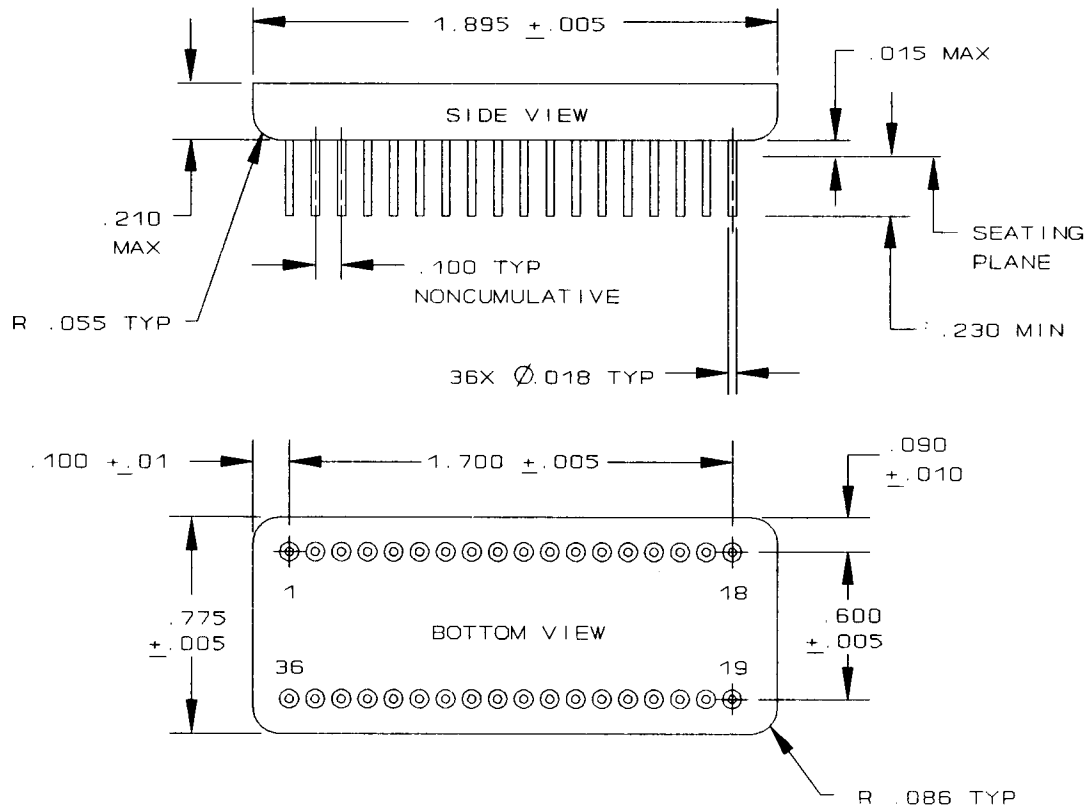
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Case outline X



Inches	mm	Inches	mm
.015	0.38	.210	5.33
.018	0.46	.230	5.84
.055	1.40	.600	15.24
.086	2.18	.775	19.69
.090	2.29	1.700	43.18
.100	2.54	1.895	48.13

NOTES:

1. Dimensions are in inches.
2. Metric equivalents are given for information only.
3. Lead identification numbers are for reference only.
4. Lead spacing dimensions apply only at seating plane.

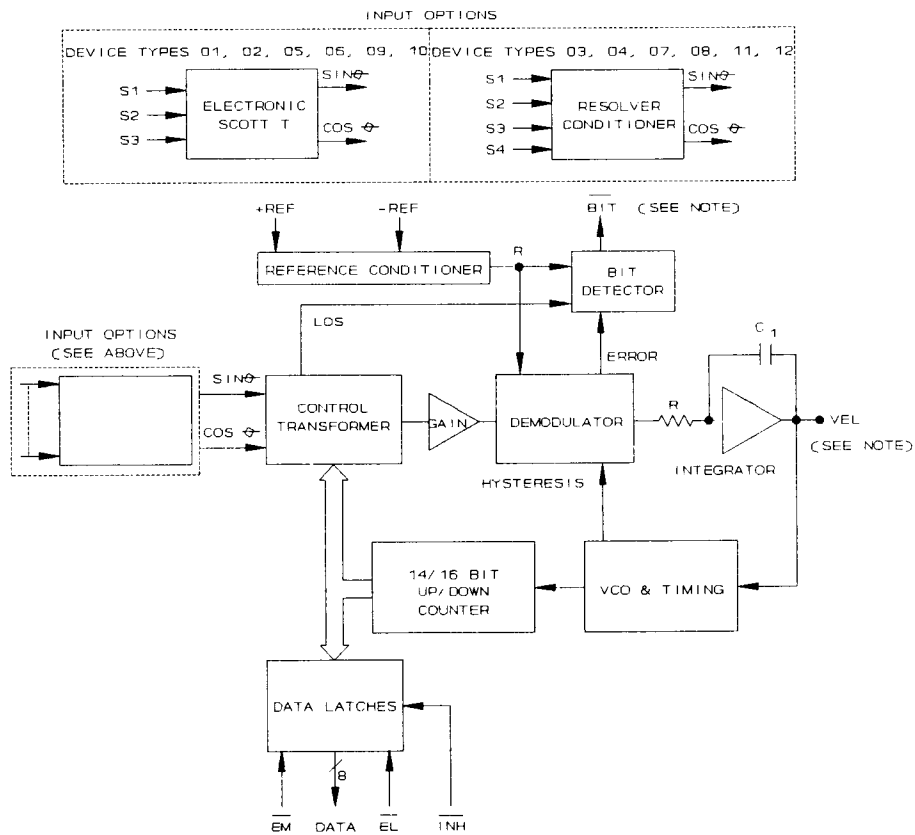
FIGURE 1. Case outline.

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Device type	01, 05, 09	02, 06, 10	03, 07, 11	04, 08, 12
Case outline	X			
Terminal number	Terminal symbol			
1	S1 A	S1 A	S1 A	S1 A
2	S2 A	S2 A	S2 A	S2 A
3	S3 A	S3 A	S3 A	S3 A
4	N/C	N/C	S4 A	S4 A
5	GND	GND	GND	GND
6	A GND	A GND	A GND	A GND
7	S1 B	S1 B	S1 B	S1 B
8	S2 B	S2 B	S2 B	S2 B
9	S3 B	S3 B	S3 B	S3 B
10	N/C	N/C	S4 B	S4 B
11	V _{CC}	V _{CC}	V _{CC}	V _{CC}
12	V _{EE}	V _{EE}	V _{EE}	V _{EE}
13	S1 C	S1 C	S1 C	S1 C
14	S2 C	S2 C	S2 C	S2 C
15	S3 C	S3 C	S3 C	S3 C
16	N/C	N/C	S4 C	S4 C
17	-REF	-REF	-REF	-REF
18	+REF	+REF	+REF	+REF
19	EM C	EM C	EM C	EM C
20	EL C	EL C	EL C	EL C
21	VEL C	BIT C	VEL C	BIT C
22	BIT 1/9	BIT 1/9	BIT 1/9	BIT 1/9
23	BIT 2/10	BIT 2/10	BIT 2/10	BIT 2/10
24	BIT 3/11	BIT 3/11	BIT 3/11	BIT 3/11
25	BIT 4/12	BIT 4/12	BIT 4/12	BIT 4/12
26	BIT 5/13	BIT 5/13	BIT 5/13	BIT 5/13
27	BIT 6/14	BIT 6/14	BIT 6/14	BIT 6/14
28	BIT 7/15	BIT 7/15	BIT 7/15	BIT 7/15
29	BIT 8/16	BIT 8/16	BIT 8/16	BIT 8/16
30	EL B	EL B	EL B	EL B
31	EM B	EM B	EM B	EM B
32	VEL B	BIT B	VEL B	BIT B
33	INH	INH	INH	INH
34	EL A	EL A	EL A	EL A
35	EM A	EM A	EM A	EM A
36	VEL A	BIT A	VEL A	BIT A

FIGURE 2. Terminal connections.

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NOTE: $\overline{\text{BIT}}$ is not connected on device types 01, 03, 05, 07, 09, and 11. VEL is not connected on device types 02, 04, 06, 08, 10, and 12.

FIGURE 3. Block diagram.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5008, group A test table)
Interim electrical parameters	1, 4, 7, 9
Final electrical test parameters	1*, 2, 3, 4, 5, 6, 7, 8A, 8B
Group A test requirements	1, 2, 3, 4, 5, 6, 7, 8A, 8B
Group C end-point electrical parameters	1, 2, 3, 4, 5, 6, 7, 8A, 8B
Group E end-point electrical parameters for RHA devices	Subgroups ** (per method 5005, group A test table)

* PDA applies to subgroup 1.

** When applicable to this standardized military drawing,
the subgroups shall be defined.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-H-38534 and as specified herein.

4.3.1 Group A inspection. Group A inspection shall be in accordance with MIL-H-38534 and as follows:

- a. Tests shall be as specified in table II herein.
- b. Subgroups 9, 10, and 11 shall be omitted.

4.3.2 Group B inspection. Group B inspection shall be in accordance with MIL-H-38534.

4.3.3 Group C inspection. Group C inspection shall be in accordance with MIL-H-38534 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.7 herein).
 - (2) T_A as specified in accordance with table I of method 1005 of MIL-STD-883.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.3.4 Group D inspection. Group D inspection shall be in accordance with MIL-H-38534.

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4.3.5 Group E inspection. Group E inspection is required only for parts intended to be marked as radiation hardness assured (see 3.5 herein). RHA levels for device classes H and K shall be M, D, R, and H. RHA quality conformance inspection sample tests shall be performed at the RHA level specified in the acquisition document.

- a. RHA tests for device classes H and K for levels M, D, R, and H shall be performed through each level to determine at what levels the devices meet the RHA requirements. These RHA tests shall be performed for initial qualification and after design or process changes which may affect the RHA performance of the device.
- b. End-point electrical parameters shall be as specified in table II herein.
- c. Prior to total dose irradiation, each selected sample shall be assembled in its qualified package. It shall pass the specified group A electrical parameters in table I for subgroups specified in table II herein.
- d. For device classes H and K, the devices shall be subjected to radiation hardness assured tests as specified in MIL-H-38534 for RHA level being tested, and meet the postirradiation end-point electrical parameter limits as defined in table I at $T_A = +25^{\circ}\text{C} \pm 5$ percent, after exposure.
- e. Prior to and during total dose irradiation testing, the devices shall be biased to establish a worst case condition as specified in the radiation exposure circuit.
- f. For device classes H and K, subgroups 1 and 2 in table V, method 5005 of MIL-STD-883 shall be tested as appropriate for device construction.
- g. When specified in the purchase order or contract, a copy of the RHA delta limits shall be supplied.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-H-38534.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use for Government microcircuit applications (original equipment), design applications, and logistics purposes.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronic devices (FSC 5962) should contact DESC-ECT, telephone (513) 296-6047.

6.5 Comments. Comments on this drawing should be directed to DESC-ECT, Dayton, Ohio 45444, or telephone (513) 296-5374.

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6.6 One part - one part number system. The one part - one part number system described below has been developed to allow for transitions between identical generic devices covered by the four major microcircuit requirements documents (MIL-M-38510, MIL-H-38534, MIL-I-38535, and 1.2.1 of MIL-STD-883) without the necessity for the generation of unique PIN's. The four military requirements documents represent different class levels, and previously when a device manufacturer upgraded military product from one class level to another, the benefits of the upgraded product were unavailable to the Original Equipment Manufacturer (OEM), that was contractually locked into the original unique PIN. By establishing a one part number system covering all four documents, the OEM can acquire to the highest class level available for a given generic device to meet system needs without modifying the original contract parts selection criteria.

<u>Military documentation format</u>	<u>Example PIN under new system</u>	<u>Manufacturing source listing</u>	<u>Document listing</u>
New MIL-M-38510 Military Detail Specifications (in the SMD format)	5962-XXXXXZZ(B or S)YY	QPL-38510 (Part 1 or 2)	MIL-BUL-103
New MIL-H-38534 Standardized Military Drawings	5962-XXXXXZZ(H or K)YY	QML-38534	MIL-BUL-103
New MIL-I-38535 Standardized Military Drawings	5962-XXXXXZZ(Q or V)YY	QML-38535	MIL-BUL-103
New 1.2.1 of MIL-STD-883 Standardized Military Drawings	5962-XXXXXZZ(M)YY	MIL-BUL-103	MIL-BUL-103

6.7 Sources of supply for device classes H and K. Sources of supply for device classes H and K are listed in QML-38534. The vendors listed in QML-38534 have submitted a certificate of compliance (see 3.7 herein) to DESC-ECT and have agreed to this drawing.

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