Bulletin I27134 rev. E 10/02

International
IOR Rectifier

IRKU/V41, 56 SERIES

THYRISTOR/ THYRISTOR **ADD-A-pak™ GEN V Power Modules**

Features

- High Voltage
- Industrial Standard Package
- Thick Al metal die and double stick bonding
- Thick copper baseplate
- UL E78996 approved
- 3500V_{RMS} isolating voltage

Benefits

- Up to 1600V
- Full compatible TO-240AA
- High Surge capability
- Easy Mounting on heatsink
- Al_20_3 DBC insulator
- Heatsink grounded

45 A
60 A

Mechanical Description

The Generation V of Add-A-pak module combine the excellent thermal performance obtained by the usage of Direct Bonded Copper substrate with superior mechanical ruggedness, thanks to the insertion of a solid Copper baseplate at the bottom side of the device. The Cu baseplate allow an easier mounting on the majority of heatsink with increased tolerance of surface roughness and improve thermal spread.
The Generation V of AAP module is manufactured without hard mold, eliminating in this way any possible direct stress on the leads.

The electrical terminals are secured against axial pull-out: they are fixed to the module housing via a click-stop feature already tested and proved as reliable on other IR modules.

Electrical Description

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

Major Ratings and Characteristics

Parameters		IRKU/V41	IRKU/V56	Units
$I_{T(AV)}$	@ 85°C	45	60	A
$I_{T(RMS)}$		70	95	A
I_{TSM}	@ 50Hz	850	1310	A
	@ 60Hz	890	1370	A
I^2t	@ 50Hz	3.61	8.50	KA^2s
	@ 60Hz	3.30	7.82	KA^2s
$I^2\sqrt{t}$		36.1	85.0	$KA^2\sqrt{s}$
V_{RRM} range		400 to 1600		V
T_{STG}		-40 to 125		°C
T_J		-40 to125		°C

ELECTRICAL SPECIFICATIONS

Voltage Ratings

Type number	Voltage Code -	V_{RRM}, maximum repetitive peak reverse voltage V	V_{RSM}, maximum non-repetitive peak reverse voltage V	V_{DRM}, max. repetitive peak off-state voltage, gate open circuit V	I_{RRM} I_{DRM} 125°C mA
IRKU/V41, 56	04	400	500	400	15
	08	800	900	800	
	12	1200	1300	1200	
	16	1600	1700	1600	

On-state Conduction

	Parameters	IRKU/V41	IRKU/V56	Units	Conditions		
$I_{T(AV)}$	Max. average on-state current	45	60	A	180° conduction, half sine wave, T_C = 85°C		
$I_{T(RMS)}$	Max. RMS on-state current @ T_C	70	95	A	DC		
		82	80	°C			
I_{TSM}	Max. peak, one cycle non-repetitive on-state current	850	1310	A	t=10ms	No voltage reapplied	Sinusoidal half wave, Initial T_J = T_J max.
		890	1370		t=8.3ms		
		715	1100		t=10ms	100% V_{RRM} reapplied	
		750	1150		t=8.3ms		
		940	1450		t=10ms	T_J = 25°C, no voltage reapplied	
		985	1520		t=8.3ms		
I^2t	Max. I^2t for fusing	3.61	8.56	KA^2s	t=10ms	No voltage reapplied	Initial T_J = T_J max.
		3.30	7.82		t=8.3ms		
		2.56	6.05		t=10ms	100% V_{RRM} reapplied	
		2.33	5.53		t=8.3ms		
		4.42	10.05		t=10ms	T_J = 25°C, no voltage reapplied	
		4.03	9.60		t=8.3ms		
$I^2\sqrt{t}$	Max. $I^2\sqrt{t}$ for fusing (1)	36.1	85.6	$KA^2\sqrt{s}$	t=0.1 to 10ms, no voltage reapplied		
$V_{T(TO)}$	Max. value of threshold voltage (2)	0.88	0.85	V	Low level (3)	T_J = T_J max	
		0.91	0.88		High level (4)		
r_t	Max. value of on-state slope resistance (2)	5.90	3.53	mΩ	Low level (3)	T_J = T_J max	
		5.74	3.41		High level (4)		
V_{TM}	Max. peak on-state voltage	1.81	1.54	V	I_{TM} = π x $I_{T(AV)}$	T_J = 25°C	
					I_{FM} = π x $I_{F(AV)}$		
di/dt	Max. non-repetitive rate of rise of turned on current	150		A/µs	T_J = 25°C, from 0.67 V_{DRM}, I_{TM} = π x $I_{T(AV)}$, I_g = 500mA, t_r < 0.5 µs, t_p > 6 µs		
I_H	Max. holding current	200		mA	T_J = 25°C, anode supply = 6V, resistive load, gate open circuit		
I_L	Max. latching current	400			T_J = 25°C, anode supply = 6V, resistive load		

(1) I^2t for time t_x = $I^2\sqrt{t}$ x $\sqrt{t_x}$. (2) Average power = $V_{T(TO)}$ x $I_{T(AV)}$ + r_t x $(I_{T(RMS)})^2$
(3) 16.7% x π x I_{AV} < I < π x I_{AV} (4) I > π x I_{AV}

Triggering

	Parameters	IRKU/V41	IRKU/V56	Units	Conditions	
P_{GM}	Max. peak gate power	10	10	W		
$P_{G(AV)}$	Max. average gate power	2.5	2.5			
I_{GM}	Max. peak gate current	2.5	2.5	A		
$-V_{GM}$	Max. peak negative gate voltage	10		V		
V_{GT}	Max. gate voltage required to trigger	4.0			T_J = -40°C	Anode supply = 6V resistive load
		2.5			T_J = 25°C	
		1.7			T_J = 125°C	
I_{GT}	Max. gate current required to trigger	270		mA	T_J = -40°C	Anode supply = 6V resistive load
		150			T_J = 25°C	
		80			T_J = 125°C	
V_{GD}	Max. gate voltage that will not trigger	0.25		V	T_J = 125°C, rated V_{DRM} applied	
I_{GD}	Max. gate current that will not trigger	6		mA	T_J = 125°C, rated V_{DRM} applied	

Blocking

	Parameters	IRKU/V41,56	Units	Conditions
I_{RRM} I_{DRM}	Max. peak reverse and off-state leakage current at V_{RRM}, V_{DRM}	15	mA	T_J = 125 °C, gate open circuit
V_{INS}	RMS isolation voltage	2500 (1 min) 3500 (1 sec)	V	50 Hz, circuit to base, all terminals shorted
dv/dt	Max. critical rate of rise of off-state voltage (5)	500	V/µs	T_J = 125°C, linear to 0.67 V_{DRM}, gate open circuit

(5) Available with dv/dt = 1000V/µs, to complete code add S90 i.e. IRKU41/16AS90.

Thermal and Mechanical Specifications

	Parameters	IRKU/V41	IRKU/V56	Units	Conditions
T_J	Junction operating temperature range	- 40 to 125		°C	
T_{stg}	Storage temper. range	- 40 to 125			
R_{thJC}	Max. internal thermal resistance, junction to case	0.23	0.20	K/W	Per module, DC operation
R_{thCS}	Typical thermal resistance case to heatsink	0.1			Mounting surface flat, smooth and greased
T	Mounting torque ± 10% to heatsink	5		Nm	A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound
	busbar	3			
wt	Approximate weight	110 (4)		g (oz)	
	Case style	TO-240AA		JEDEC	

ΔR Conduction (per Junction)

(The following table shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC)

Devices	Sine half wave conduction					Rect. wave conduction					Units
	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	
IRKU/V41	0.11	0.13	0.17	0.23	0.34	0.09	0.14	0.18	0.23	0.34	°C/W
IRKU/V56	0.09	0.11	0.13	0.18	0.27	0.07	0.11	0.14	0.19	0.28	

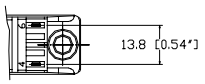
Ordering Information Table

Device Code

IRK	U	56	/	16	A	S90
1	2	3		4	5	6

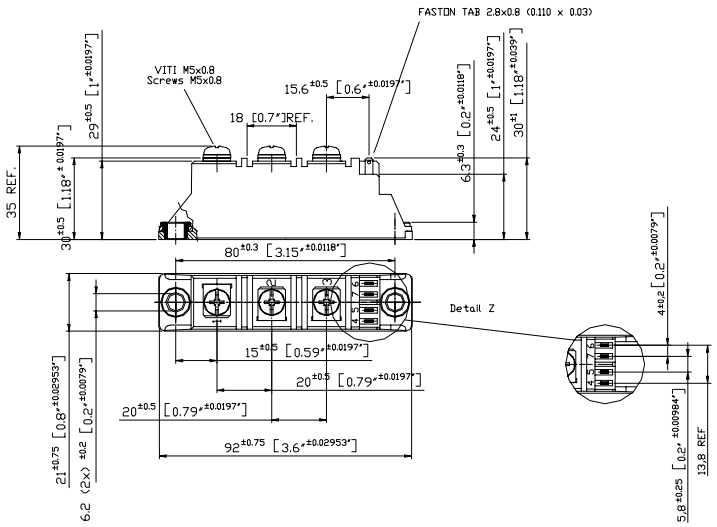
1 - Module type
2 - Circuit configuration (See Circuit Configuration table below)
3 - Current code *****
4 - Voltage code (See Voltage Ratings table)
5 - A : Gen V
6 - dv/dt code: S90 = dv/dt 1000 V/µs
No letter = dv/dt 500 Vµs

IRK.57 types
With no auxiliary cathode



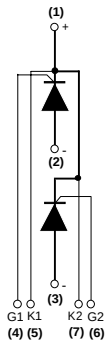
***** Available with no auxiliary cathode.
To specify change: 56 to 57
41 to 42
e.g. : IRKU57/16A etc.

Outline Table

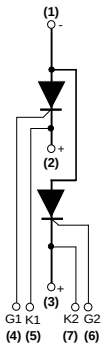


Dimensions are in millimeters and [inches]

IRKU



IRKV



NOTE: To order the Optional Hardware see Bulletin I27900

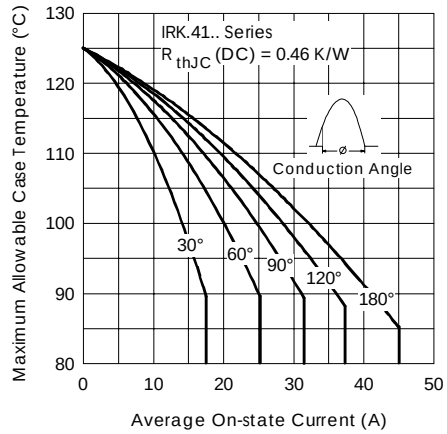


Fig. 1 - Current Ratings Characteristics

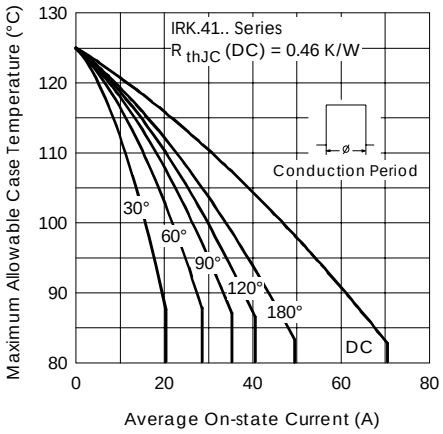


Fig. 2 - Current Ratings Characteristics

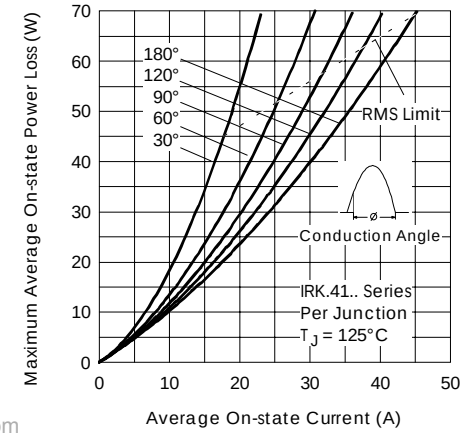


Fig. 3 - On-state Power Loss Characteristics

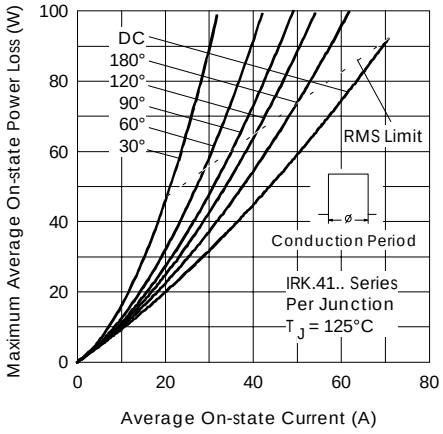


Fig. 4 - On-state Power Loss Characteristics

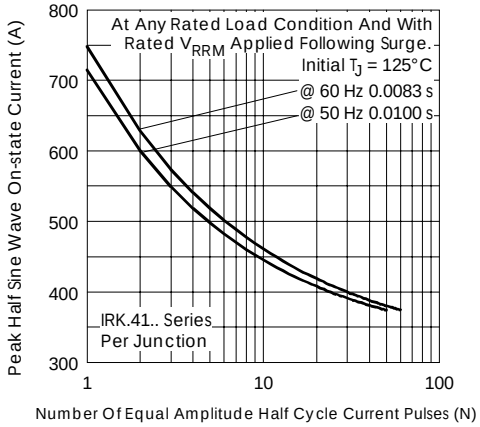


Fig. 5 - Maximum Non-Repetitive Surge Current

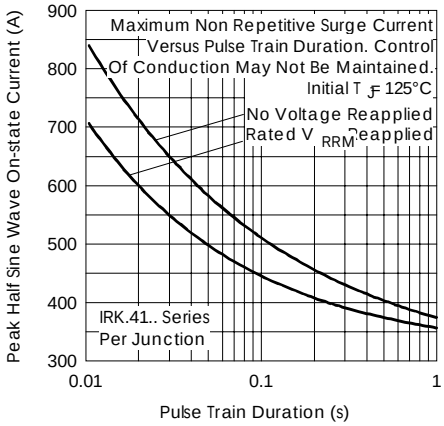


Fig. 6 - Maximum Non-Repetitive Surge Current

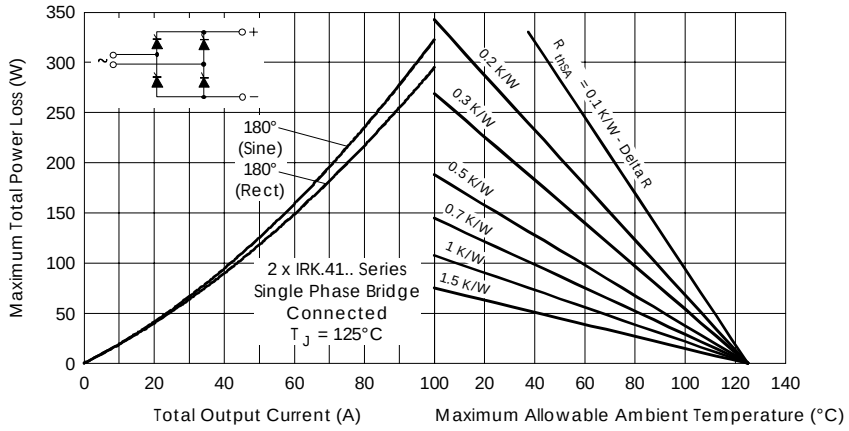


Fig. 7 - On-state Power Loss Characteristics (Single Phase Bridge IRKU+IRKV)

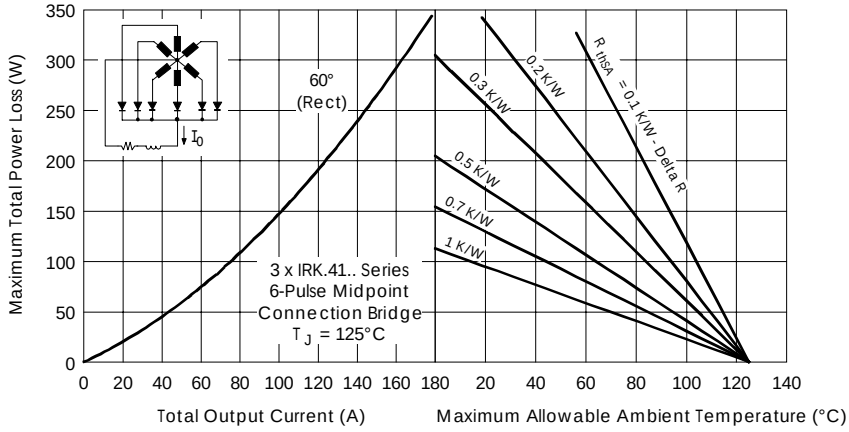


Fig. 8 - On-state Power Loss Characteristics

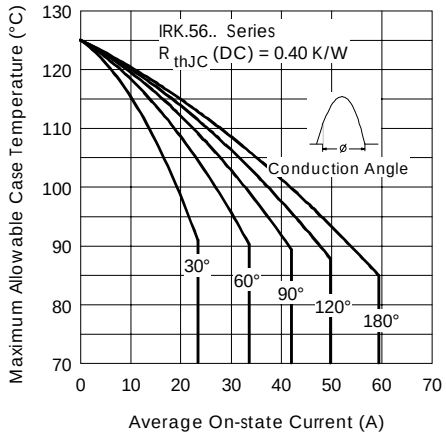


Fig. 9 - Current Ratings Characteristics

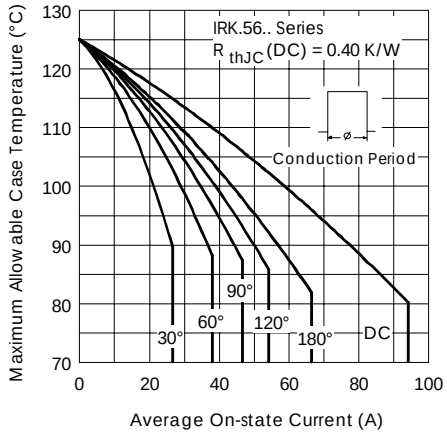


Fig. 10 - Current Ratings Characteristics

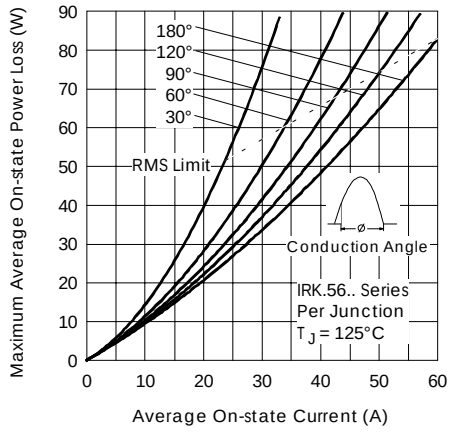


Fig. 11 - On-state Power Loss Characteristics

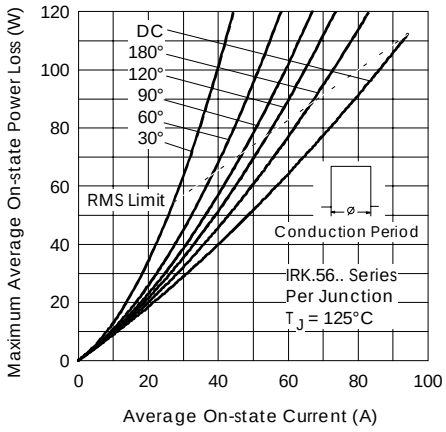


Fig. 12 - On-state Power Loss Characteristics

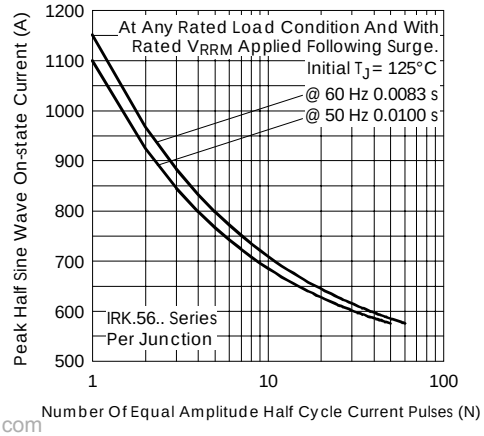


Fig. 13 - Maximum Non-Repetitive Surge Current

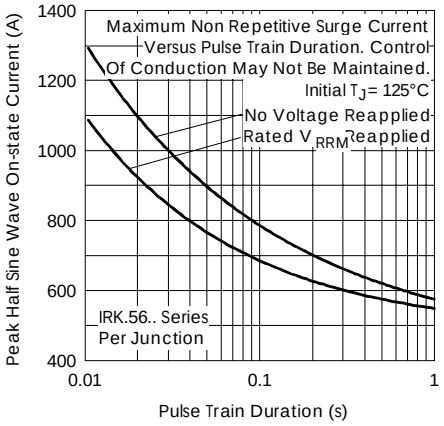


Fig. 14 - Maximum Non-Repetitive Surge Current

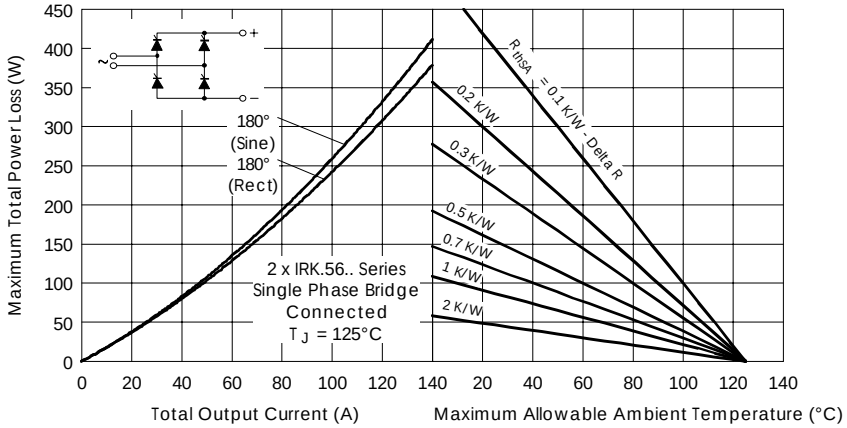


Fig. 15 - On-state Power Loss Characteristics (Single Phase Bridge IRKU+IRKV)

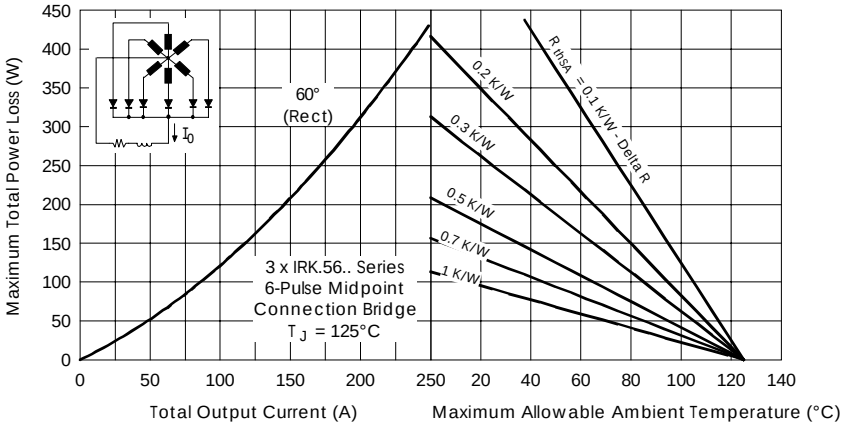


Fig. 16 - On-state Power Loss Characteristics

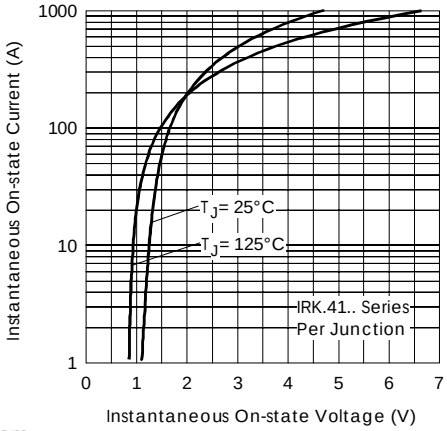


Fig. 17 - On-state Voltage Drop Characteristics

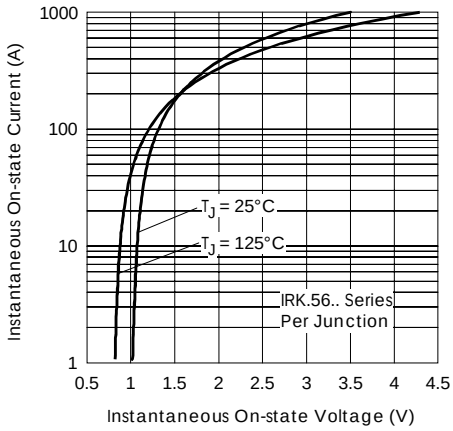


Fig. 18 - On-state Voltage Drop Characteristics

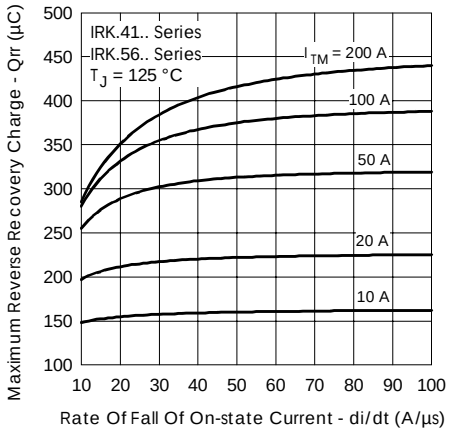


Fig. 19 - Recovery Charge Characteristics

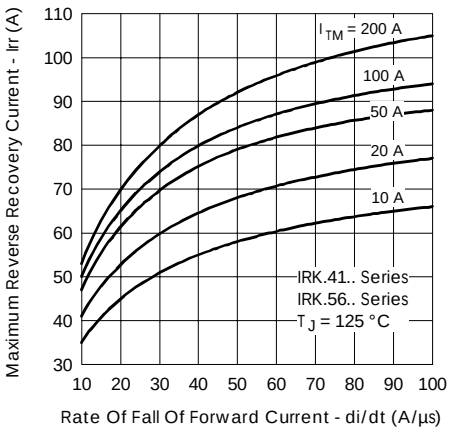


Fig. 20 - Recovery Current Characteristics

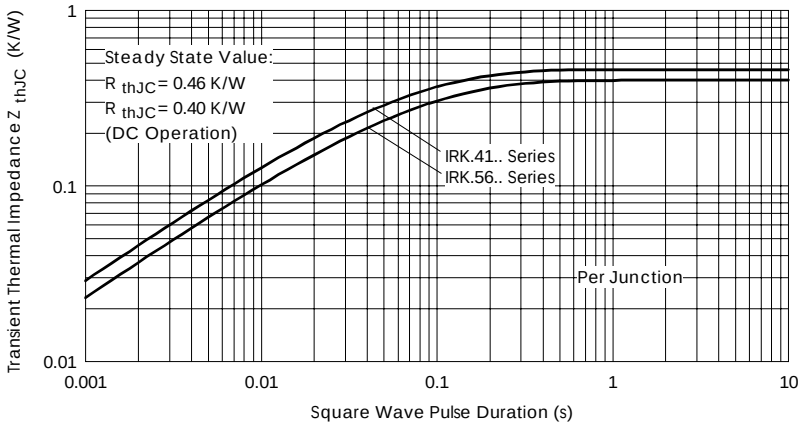


Fig. 21 - Thermal Impedance Z_{thJC} Characteristics

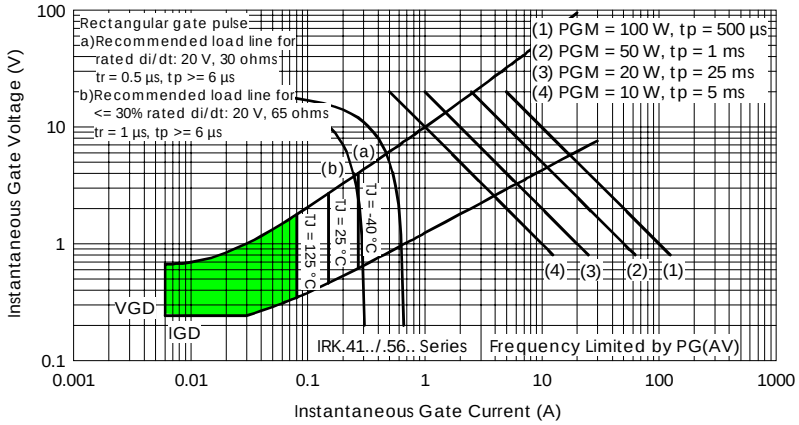


Fig. 22 - Gate Characteristics

Data and specifications subject to change without notice.
This product has been designed and qualified for Industrial Level.
Qualification Standards can be found on IR's Web site.

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IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7309
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