

SANYO Semiconductors

DATA SHEET

LV8052GP___ LV8052LP

Bi-CMOSIC _ For Digital Still Camera Single-Chip Motor Driver IC

Overview

The LV8052GP, LV8052LP is a single-chip motor driver IC for digital still camera.

Functions

- DSC actuator driver incorporated in a single chip
- Photo sensor driving transistor incorporated
- Various actuator applications possible
- Reduction of the current drain by MOS output

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage 1	V _M max		6	V
Supply voltage 2	V _{CC} max		6	V
Output peak current	I _O peak	OUT1 to 8	600	mA
Output continuous current	I _O max1	OUT1 to 8	400	mA
	IO max2	PI	50	mA
Allowable power dissipation	Pd max	Mounted on a circuit board*	1.05	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

* Standard circuit board : 40×50×0.8mm³ glass epoxy four-layer board

Recommended Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage range 1	VM		2.7 to 5.5	V
Supply voltage range 2	V _{CC}		2.7 to 5.5	V
Logic input voltage	VIN		0 to V _{CC} +0.3	V
Input frequency	fIN	EN, MD1 to 3, IN1 to 2, INA to B, SWPI	to 100	kHz

- Any and all SANYO Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO Semiconductor representative nearest you before usingany SANYO Semiconductor products described or contained herein in such applications.
- SANYO Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO Semiconductor products described or contained herein.

SANYO Semiconductor Co., Ltd. TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Electrical Characteristics at Ta = 25°C, $V_M = 5.0V$, $V_{CC} = 3.3V$

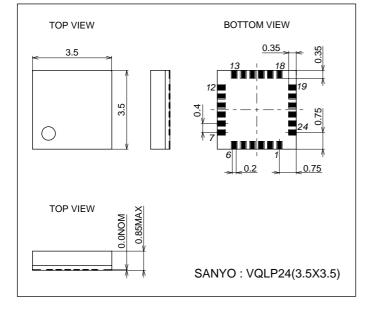
Parameter	Symbol	Conditions		Ratings		Unit	
T diameter	Cymbol	Conditions	min	typ	max		
Input H-level voltage	VINH	EN, MD1 to 3, IN1 to 2, INA to B, SWPI	2.5			V	
Input L-level voltage	VINL	EN, MD1 to 3, IN1 to 2, INA to B, SWPI			1.0	V	
Input pin current	I _{IN} L	$V_{IN} = 0V$			1.0	μA	
	I _{IN} H	$V_{IN} = 3.3V$	20	33	50	μA	
Current drain at standby	ICCO	EN, INA to B = "L"			1.0	μA	
Current drain 1	۱ _M	EN = "H", MD1 to 3, IN1 to 2, INA to $B = $ "H", no load	40	80	160	μA	
Current drain 2	ICC	EN = "H", MD1 to 3, IN1 to 2, INA to $B = "H"$, no load	0.5	1.0	1.8	mA	
V _{CC} low-voltage cut voltage	V _{th} V _{CC}		2.1	2.35	2.6	V	
Low-voltage hysteresis voltage	V _{th} HYS		100	150	200	mV	
Thermal shutdown temperature	TSD	Design guarantee	150	180	200	°C	
Thermal hysteresis width	∆TSD	Design guarantee	20	40	60	°C	
Motor driver for SH (OUT1-2)		· · · · ·	•		·		
Output ON resistance	Ronu	I _O = 400mA, upper ON resistance		0.65	0.80	Ω	
	Rond	I _O = 400mA, lower ON resistance		0.45	0.60	Ω	
Output leak current	lOleak				1.0	μA	
Diode forward voltage	VD	I _D = -400mA	0.7	0.9	1.2	V	
Output constant current	I _O 1	OUT2 \rightarrow OUT1, RRFS = 1 Ω , 3.0V \leq V _M \leq 5.0V	117.5	125.0	132.5	mA	
	1 ₀ 2	OUT1 \rightarrow OUT2, RRFS = 1 Ω , 3.0V \leq V _M \leq 5.0V	117.5	125.0	132.5	mA	
	IO3	OUT2 \rightarrow OUT1, RRFS = 1 Ω , 2.9V \leq V _M \leq 3.1V	116.9	123.0	129.1	mA	
	I _O 4	OUT1 \rightarrow OUT2, RRFS = 1 Ω , 2.9V \leq V _M \leq 3.1V	116.9	123.0	129.1	mA	
Stepping motor driver for AF (OUT	2-3, OUT6-7)		I	I			
Output ON resistance	Ronu	I _O = 400mA, upper ON resistance		0.65	0.80	Ω	
	Rond	I _O = 400mA, lower ON resistance		0.45	0.60	Ω	
Output leak current	lOleak				1.0	μA	
Diode forward voltage	VD	I _D = -400mA	0.7	0.9	1.2	V	
Motor driver for ZOOM (OUT4-8)							
Output ON resistance	Ronu	I _O = 400mA, upper ON resistance		0.65	0.80	Ω	
	Rond	I _O = 400mA, lower ON resistance		0.45	0.60	Ω	
Output leak current	lOleak				1.0	μA	
Diode forward voltage	VD	I _D = -400mA	0.7	0.9	1.2	V	
Motor driver for AE (OUT5-6)							
Output ON resistance	Ronu	I _O = 400mA, upper ON resistance		0.65	0.80	Ω	
	Rond	I _O = 400mA, lower ON resistance		0.45	0.60	Ω	
Output leak current	l _O leak				1.0	μA	
Diode forward voltage	VD	I _D = -400mA	0.7	0.9	1.2	V	
Photo sensor driving transistor (PI)	_	1 - 1					
Output ON resistance	Ron	I _O = 30mA	[3.0	6.0	Ω	
Output leak current	lOleak	-			1.0	μA	

Package Dimensions

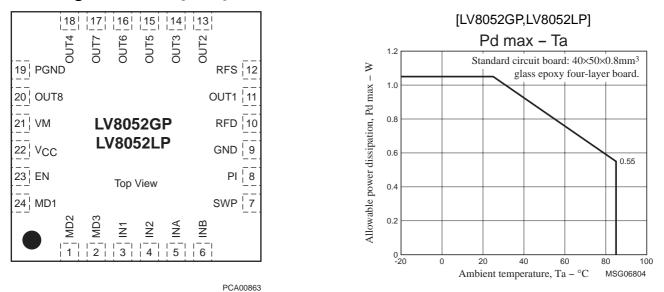
unit : mm (typ) 3322 [LV8052GP] SIDE VIEW TOP VIEW BOTTOM VIEW (0.125) 3.5 ŝ 12 (C0.116) 3.5 \bigcirc (0.5)SIDE VIEW 0.83 (0.035) 0.25 SANYO : VCT24(3.5X3.5)X01

3321



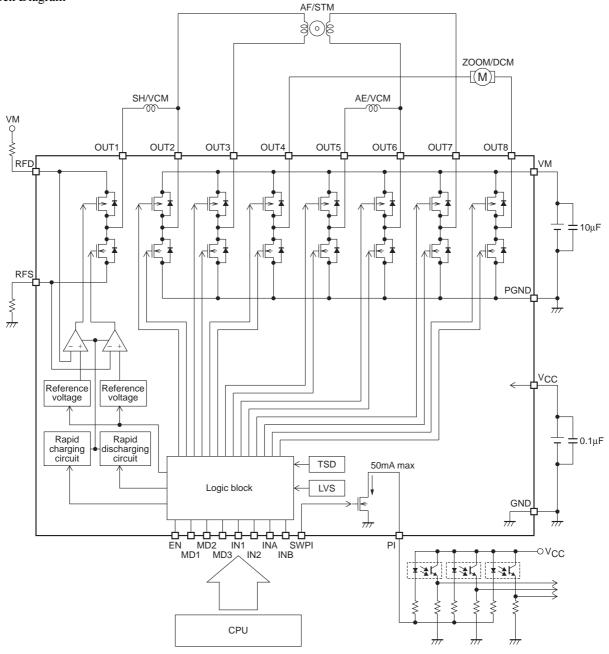


Pin Assignment * The pin assignment is the same as LV8052GP and LV8052LP.



LV8052GP,LV8052LP

Block Diagram



LV8052GP,LV8052LP

Pin Function Pin No. Pin name Function Equivalent circuit MD2 1 Control signal input pin VCC 2 MD3 \bigcirc 3 IN1 4 IN2 5 INA > 6 INB SWPI -7 Control signal input pin (photo sensor driving transistor) $10k\Omega$ 23 EN Control signal input pin \sim 24 MD1 100kΩ 10 RFD OUT1→OUT2 Current detection (10)resistance connection pin OUT1 11 Output pin 12 RFS OUT1 \rightarrow OUT2 Current detection resistance connection pin (11) 12 OUT2 13 Output pin VM OUT3 14 OUT5 15 OUT6 16 17 OUT7 18 OUT4 20 OUT8 PGND

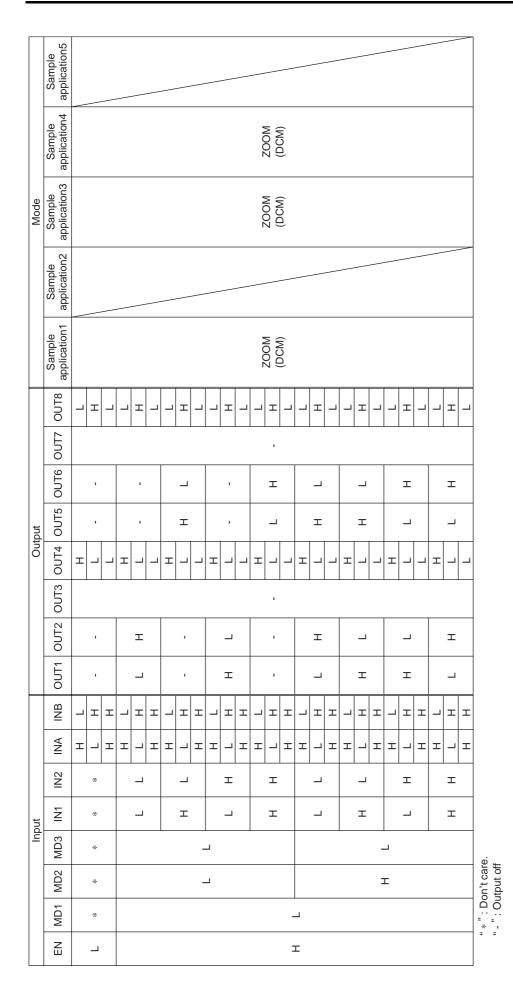
Continued on next page.

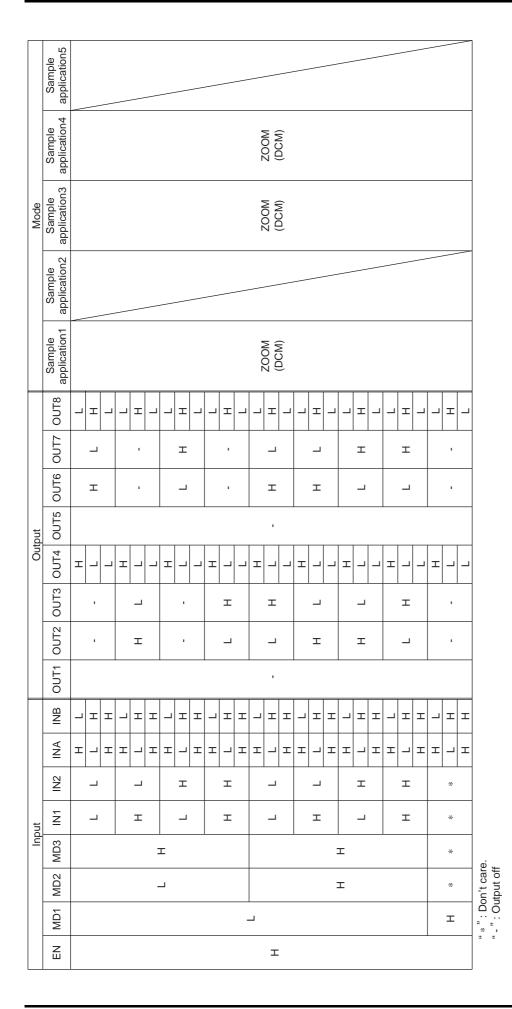
LV8052GP,LV8052LP

Continued f	from precedin	g page.	
Pin No.	Pin name	Function	Equivalent circuit
8	PI	Photo sensor driving transistor output pin	GND GND
10	RFD	OUT1→OUT2 Current detection resistance connection pin	VM $VREF$ $VM-0.125V$ $VREF$ $VM-0.125V$ $VKEF$ $VM-0.125V$ $VKEF$ $VM-0.125V$ $VKEF$ $VK-0$ $VK-0$ $VKEF$ $VK-0$ $VK-$
12	RFS	OUT1→OUT2 Current detection resistance connection pin	VCC VREF 0.125V GND
0		Signal CND	
9 19	GND PGND	Signal GND Power GND	
19 21	V _M	Motor power connection pin	
21	V _{CC}	Logic power connection pin	
~~~	101		

# **Truth Table**

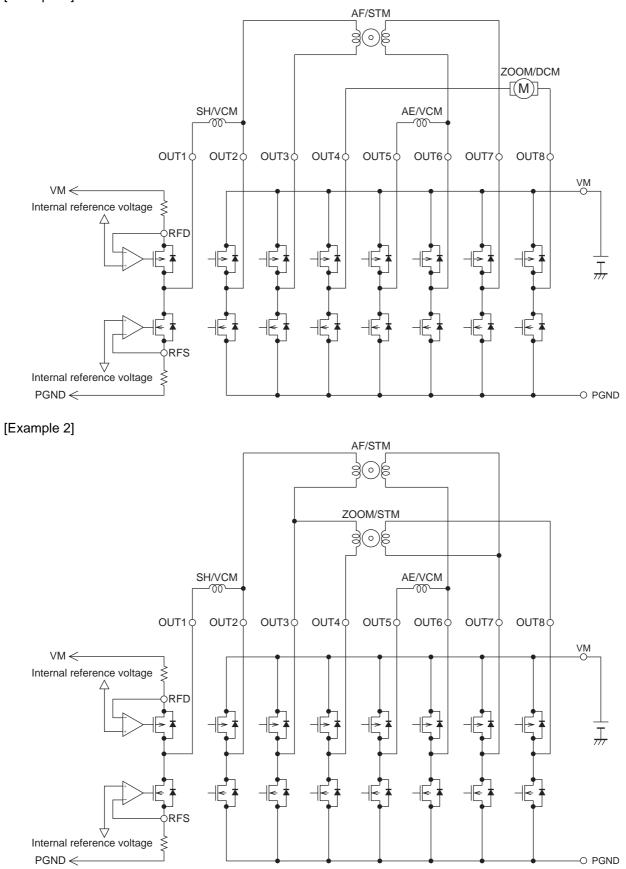
	Sample application1		L	AE Overtetion)			L	AE											ZOOM	(Single-phase	excitation)	(STM)	ZOOM	(Two-phase	excitation)	(INI C)			/	/	/	/	/	~					
	Sample application1			Single choose ov	(oirigie-priase excitation) (STM)		- - -		(I WU-PIIASE EXULATION)	)		ation)				tion)								_	/		/	/					/	/	/	/		<b>_</b>	
Mode	Sample application1	Standby	τ		*-			/	/		AF AF (Single-phase excitation) (STM)					AF (Turo aboac avaitation)	U-pridase excitat (STM)	(	_	/	/	/	/		/		AE	(Single-phase	excitation)	(STM)	AE	(Two-phase	excitation)	(STM)					
	Sample application1		SH (VCM) "Close"	AE (VCM)	SH (VCM) "Open"	AE (VCM)		/	/							Ë			ZOOM	(Single-phase	excitation)	(NIS)	ZOOM	(Two-phase	excitation)	(MI C)	_	/	/	/	/								
	Sample application1		S	AE (	S	AE (/		/	/											/	/	/	/	/	/				/	/	/	/	/						
	OUT8	ı					ı								·				_	ı	т	·		Г	т	т					·								
	OUT7	ı					ı				_	ı	т	ı		_	т	т	т	ı	_	ı	н	н	_	_					ı								
	OUT6	ı	ı	_	1	т	_	_	т	Т	т		_		Т	Т	_	_									_		н	ı	_	_	т	т					
put	OUT5	ı		т		_	т	т	_	Γ													ı				т		Γ	ı	т	т	_	_					
Output	OUT4	ı					ı								·				,	_	·	т	н	Г	_	т		_		Т	т	_	_	т					
	OUT3	ı					ı					_		т	Т		_	т		т	,	_		н	т	_		т		Γ	_	т	т	_					
	OUT2	ı	т	ı	_	ı	т	_	_	т	ı	н		_		Т	т	_	1			1																	
	OUT1	ı	_		т	ı	_	т	т	_													ı								ı								
	INB		<b>I</b>	1	<u> </u>	<u> </u>	1	1	1									_																					
	INA																	-	J																				
	IN2	*	_	_	т	т	_	_	т	т	_	_	т	т	_		т	т	_	_	т	т			т	т	_	_	н	т	-	_	т	т					
Input	IN1	*	_	Т	-	т	_	т	_	Т	_	Т	-	Т		T	_	I	_	т		т	_	т	_	т	_	т		Т	-	Т	_	Т					
1	MD3	*		-				-				-	E			-	E			_	I			-	1			2				-	E		re.				
	MD2	*		-				-	E			-	_			-	E			_	I			]	Ξ			-				-	E		Jon't ca				
	MD1	*								-																	:								" * ": Don't care.				
	N	_																=	C																				



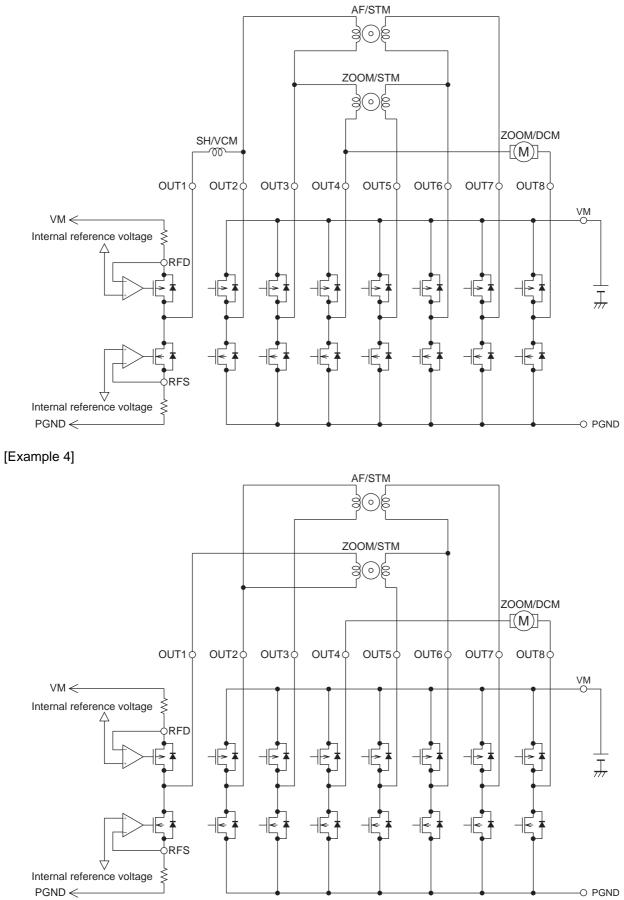


## **Sample Application Circuit**

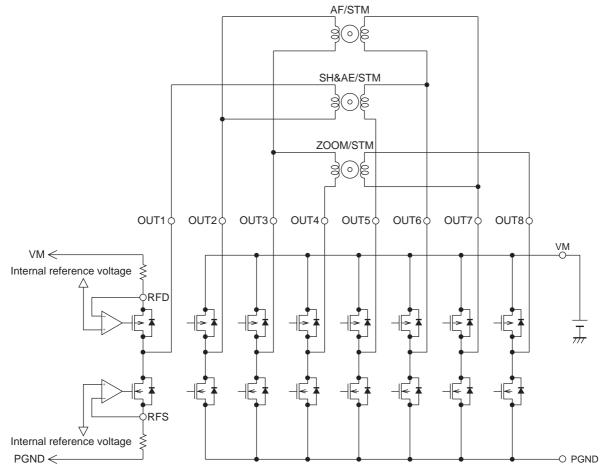












#### **Design Considerations**

1. Method to calculate the set current value for shutter control

The output current can be set from the internal reference voltage and the detection resistors, each connected between VM and RFD pins and between RFS pin and GND.

 $I_{OUT}$  = Internal reference voltage 0.125V ÷ detection resistor

From the above equation, the current value to flow from OUT2 to OUT1 when the detection resistor  $1\Omega$  is connected between the RFS pin and GND can be determined to be about 125mA. Similarly, the current value to flow from OUT1 to OUT2 when the detection resistor  $1\Omega$  is connected between VM and RFD can be determined to be about 125mA.

- Changeover between the constant current and saturation drive Saturation drive is made by deleting the detection resistors between VM and RFD pins and between the RFS pin and GND.
- 3. OUT4 and OUT8 independent control with INA and INB pins When the INA or INB pin is set at "H", OUT4 and OUT8 are activated regardless of the input conditions of MD1 to MD3 and IN1 to IN2.
- 4. Photo sensor driving transistor

By setting the SWPI pin to "H", the photo sensor driving transistor is activated. When thermal shutdown and  $V_{CC}$  low-voltage cut circuits are activated, OUT1 through OUT8 are turned OFF under control of the internal circuit. But the output (PI) of photo sensor driving transistor continues operation.

- Specifications of any and all SANYO Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- SANYO Semiconductor Co., Ltd. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all SANYO Semiconductor products (including technical data,services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of SANYO Semiconductor Co., Ltd.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the SANYO Semiconductor product that you intend to use.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

This catalog provides information as of December, 2006. Specifications and information herein are subject to change without notice.