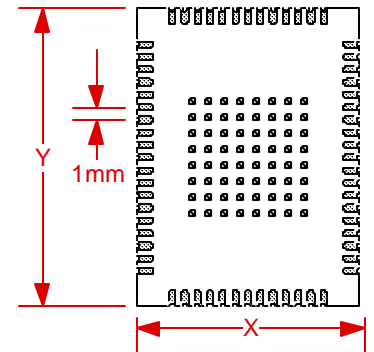
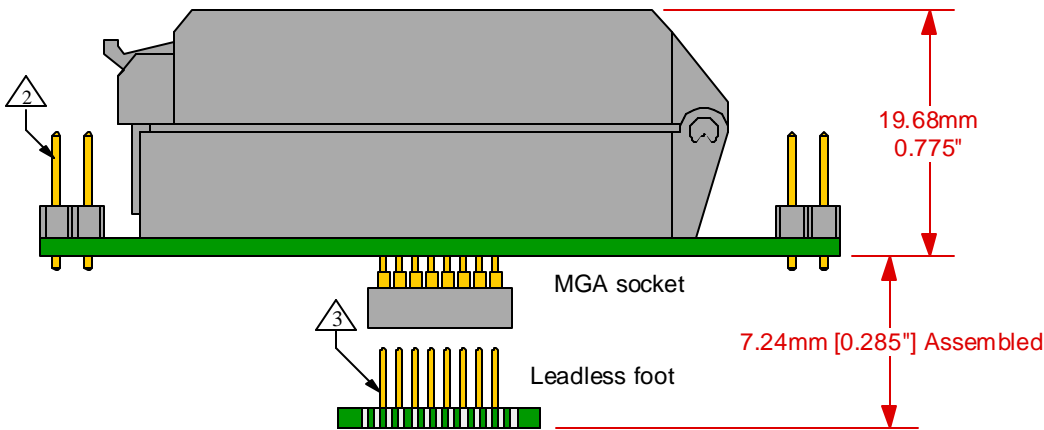


Top View

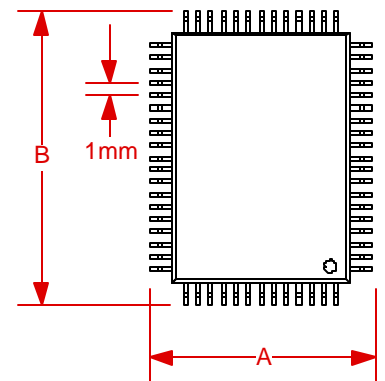
- 1 Substrate: 1.59mm ± 0.18 mm [0.0625" ± 0.007 "] FR4/G10 or equivalent high temp material. 17 μ m [1/2 oz.] Cu clad. SnPb plating.
- 2 Pins: material- Brass Alloy 360 1/2 hard; finish- 0.25 μ m [10 μ "] Au over 1.27 μ m [50 μ "] Ni (min.).
- 3 Test points: material- Phosphor Bronze; plating- Sn over 1.27 μ m [50 μ "] Ni. Gold flash on contact end.



Leadless foot dimensions



Side View




QFP package dimensions

* Part Number	Footprint	Dim "A"	Dim "B"	Dim "X"	Dim "Y"
CA-QFE64RA-L-Z-T-01	QFE64RA	19.6mm [0.772"]	25.5 mm [1.004"]	17.65mm [0.695"]	23.62mm [0.930"]
CA-QFE64RB-L-Z-T-01	QFE64RB	17.2mm [0.677"]	23.2mm [0.913"]	16.13mm [0.635"]	22.10mm [0.870"]
CA-QFE64RC-L-Z-T-01	QFE64RC	17.9mm [0.705"]	23.9mm [0.941"]	16.89mm [0.665"]	22.86mm [0.900"]
CA-QFE64RD-L-Z-T-01	QFE64RD	18.8mm [0.740"]	24.8mm [0.976"]	17.65mm [0.695"]	23.62mm [0.930"]

Description: Carrier Adaptor (CA)

64 position (1.00mm pitch) QFP zero insertion force (ZIF) socket with test points to 64 position surface mountable QFP emulator foot. The 2 piece adaptor interconnects via a gold plated (1.27mm [0.05"] center) Mini-grid socket interface. Emulator foot is leadless and design to attached to gull-wing QFP pattern.

Tolerance: diameters ± 0.03 mm [± 0.001 "], PCB perimeters ± 0.13 mm [± 0.005 "], PCB thicknesses ± 0.18 mm [± 0.007 "], pitches (from true position) ± 0.08 mm [± 0.003 "], all other tolerances ± 0.13 mm [± 0.005 "] unless stated otherwise.

CA-QFE64R(x)-L-Z-T-01* Drawing		Status: Released	Scale 3:2	Rev: G
 <p>© 1995 IRONWOOD ELECTRONICS, INC. PO BOX 21151 ST. PAUL, MN 55121 Tele: (651) 452-8100 www.ironwoodelectronics.com</p>	Drawing: G. Nelson		Date: 3/23/95	
	File: CA-QFE064RA-L-Z-T-01 Dwg		Modified: 1/28/05, MT	