

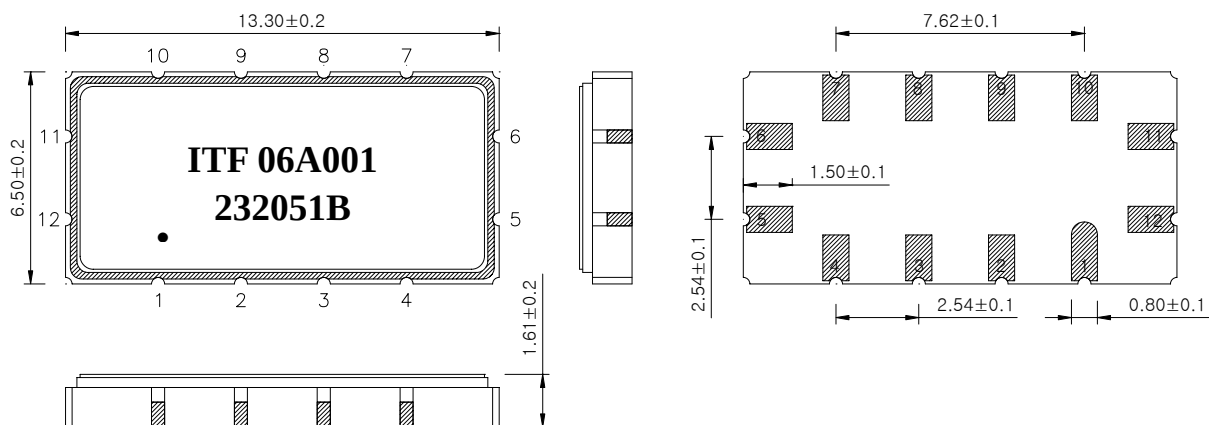
Bandpass Filter 232051B



1. Features

- IF bandpass filter
- Low-Loss Filter
- Single-ended operation
- Ceramic Surface Mount Device(SMD) Package
- Maximum Storage Temperature Range : -40℃ ~ 85℃
- Electrostatics Sensitive Device (ESD)

2. Package Dimension



Package : S1365

Dimensions shown are nominal in millimeters

Body : Al₂O₃

Lid : Kovar, Ni Plated

Termination : Au plating 0.3 ~ 1.0um, over a 1.27 ~ 8.89um Ni Plating

Pin Configuration	
11	Input
5	Output
6, 12	Ground
Other	Case ground

 Integrated Technology Future	ITF Co., Ltd. 102-901, Bucheon Technopark 364, Samjeong-Dong, Ojeong-Gu, Bucheon-City, Gyeonggi-Do, Korea 421-809	Part No.	232051B	
		Rev. Date	2006-04-17	
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3. Specifications

Fo = 75 MHz

Terminating source impedance : 50Ω and matching network

Terminating load impedance : 50Ω and matching network

Operating temperature range : -30℃ ~ +80℃		Minimum	Typical	Maximum
Center Frequency	MHz	74.5	75.0	75.5
Insertion Loss	dB	-	12.0	14.0
1dB Bandwidth	MHz	19.2	19.7	-
3dB Bandwidth	MHz	19.9	20.4	-
40dB Bandwidth	MHz	-	23.7	24.2
Amplitude Ripple (Fo +/- 8.92 MHz)	dB	-	0.5	1.0
Group Delay Variation (Fo +/- 8.92 MHz)	nsec	-	60	100
Absolute Delay	usec	-	1.1	-
Relative Attenuation				
DC ~ 60.00 MHz	dB	45	50	-
90.00MHz ~ 190.00MHz	dB	40	45	-
190.00MHz ~ 255.00MHz	dB	20	25	-
255.00MHz ~ 300.00MHz	dB	50	52	-
Temperature Coefficient of Frequency	ppm/℃	-	-86	-

Room temperature : + 25 ℃		Minimum	Typical	Maximum
Center Frequency	MHz	74.85	75.0	75.15
Insertion Loss	dB	-	12.0	13.5
Amplitude Ripple (Fo +/- 9.42 MHz)	dB	-	0.5	1.0
Group Delay Variation (Fo +/- 9.42 MHz)	nsec	-	60	100
Relative Attenuation				
DC ~ 60.00 MHz	dB	45	50	-
90.00MHz ~ 190.00MHz	dB	40	45	-
190.00MHz ~ 255.00MHz	dB	20	25	-
255.00MHz ~ 300.00MHz	dB	50	52	-

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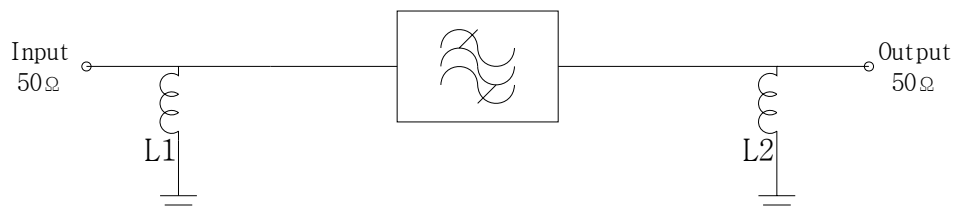


Notes :

- 1) All specifications are based on the matching schematic shown below
- 2) All specifications are measured by Agilent Network analyzer and full 2 port calibration
- 3) Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
- 4) All attenuation measurements are measured relative to insertion loss

4. Matching Schematic

(Actual matching values may vary due to PCB layout and parasitics)



$$L1 = 82\text{nH}, \quad L2 = 68\text{nH}$$

5. Marking Configuration

ITF¹⁾ 06A001²⁾

232051B³⁾

●⁴⁾

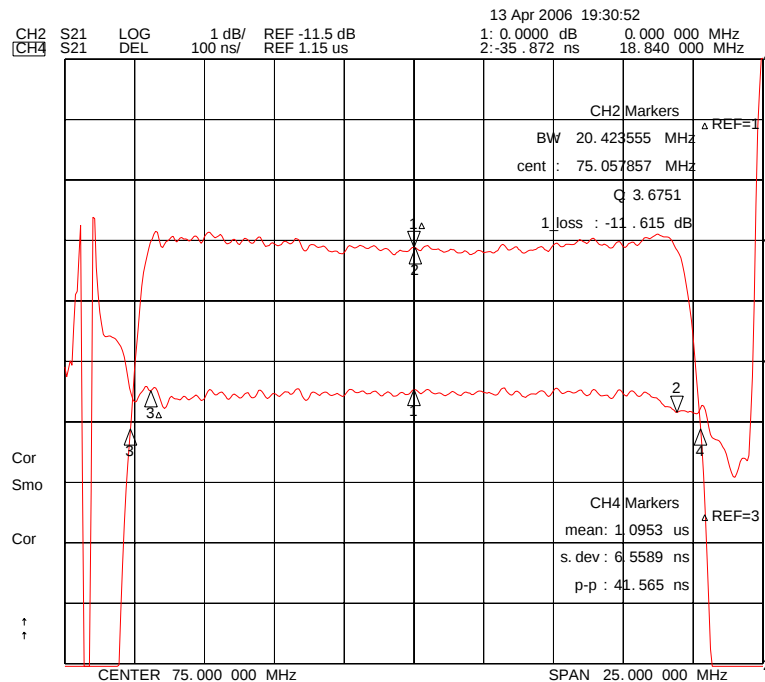
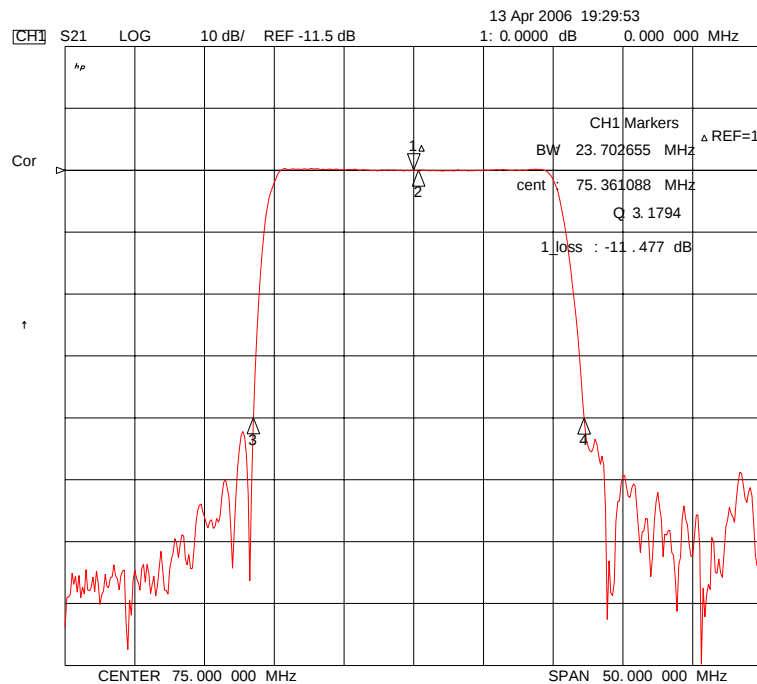
- 1) Manufacturer name
- 2) Lot Number
- 3) Part Number
- 4) Pad Number 1 Index

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6. Typical Performance (at +25°C)



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