TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ14AFS

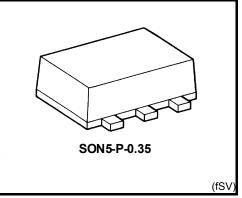
Schmitt Inverter

Features

- High-level output current
- : ± 24 mA (min) at V_{CC} = 3.0VSuper high speed operation : t_{pd} = 3.7 ns (typ.) at V_{CC} = 5V, 50pF

: V_{CC(opr)} = 1.65 to 5.5V

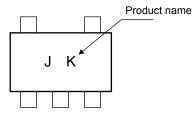
- Operation voltage range
- 5.5-V tolerant input

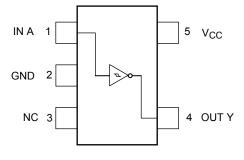


Weight : 0.001 g (typ.)

Marking

Pin Assignment (top view)





Absolute Maximum Ratings (Ta = 25°C)

	=• • • /		
Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 6	V
DC input voltage	VIN	–0.5 to 6	V
DC output voltage	V _{OUT}	-0.5 to Vcc+0.5V	V
Input diode current	I _{IK}	-20	mA
Output diode current	IOK	±20 (Note1)	mA
DC output current	IOUT	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	–65 to 150	°C

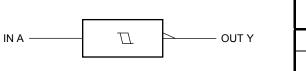
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND,V_{OUT} > V_{CC}

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IEC Logic Symbol



A Y L H H L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	Vee	1.65 to 5.5	V
Supply voltage	V _{CC}	1.5 to 5.5 (Note 2)	
Input voltage	VIN	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Note 2: Data retention only

Electrical Characteristics

DC Characteristics

Characteristics		Symbol Test Condition		Ta = 25°C			2	Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Ма	
High level				1.65	0.6	1.0	1.4	0.6	1.4	
				1.8	0.7	1.1	1.5	0.7	1.5	
	VP	—	2.3	1.0	1.4	1.8	1.0	1.8		
			3.0	1.3	1.75	2.2	1.3	2.2		
				4.5	1.9	2.45	3.1	1.9	3.1	- V
Threshold				5.5	2.2	2.9	3.6	2.2	3.6	
voltage		V _N	_	1.65	0.2	0.5	0.8	0.2	0.8	
				1.8	0.25	0.55	0.9	0.25	0.9	
	Low level			2.3	0.4	0.75	1.15	0.4	1.15	
LO	LOW level			3.0	0.6	1.0	1.5	0.6	1.5	
				4.5	1.0	1.43	2.0	1.0	2.0	
				5.5	1.2	1.7	2.4	1.2	2.4	
				1.65	0.1	0.48	0.9	0.1	1.0	
Hysteresis voltage	V _H		1.8	0.15	0.54	1.0	0.15	1.0	- V	
		_	2.3	0.25	0.65	1.1	0.25	1.1		
			3.0	0.4	0.77	1.2	0.4	1.2		
				4.5	0.6	1.01	1.5	0.6	1.5	1
				5.5	0.7	1.18	1.7	0.7	1.7	1

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Characteristics Sy		Symbol Test Condition		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
						Min	Тур.	Max	Min	Ma	
					1.65	1.55	1.65		1.55		-
					1.8	1.7	1.8	—	1.7	—	
				$I_{OH} = -100 \ \mu A$	2.3	2.2	2.3		2.2		
					3.0	2.9	3.0	_	2.9		
	High level	V _{OH}	$V_{IN} = V_N$		4.5	4.4	4.5	_	4.4		
	riigirievei	VОН	VIN = VN	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52	_	1.29		- V - V
				I _{OH} = -8 mA	2.3	1.9	2.15	_	1.9	_	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
Output				I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
				I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	_	
voltage	Low level	Vol	VIN = VP	I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	
					1.8	_	0	0.1	_	0.1	
					2.3	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
					4.5	_	0	0.1	_	0.1	
				I _{OL} = 4 mA	1.65	_	0.08	0.24		0.24	
				I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	1
				I _{OL} = 24 mA	3.0	_	0.22	0.55		0.55	
				I _{OL} = 32 mA	4.5		0.22	0.55		0.55	
Input leakage current		V _{IN} = 5.5 V d	V _{IN} = 5.5 V or GND				±1		±10	μA	
Power OFF leakage current		IOFF	V_{IN} or $V_{OUT} = 5.5 V$		0.0			1		10	μA
Quiescent sup	ply current	ICC	$V_{IN} = 5.5 V c$	V _{IN} = 5.5 V or GND 1				1		10	μA

AC Electrical Characteristics (Unless otherwise specified Input, $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Canditian		Ta = 25°C			Ta = -4	Unit	
	Symbol	Test Condition	$V_{CC}(V)$	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH t _{pHL}	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8±0.15	2.0	9.1	15.0	2.0	15.6	ns
			2.5 ± 0.2	1.0	5.0	9.0	1.0	9.5	
			$\textbf{3.3}\pm\textbf{0.3}$	1.0	3.7	6.3	1.0	6.5	
			5.0 ± 0.5	0.5	3.1	5.2	0.5	5.5	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.5	4.4	7.2	1.5	7.5	
			5.0 ± 0.5	0.5	3.7	5.9	0.5	6.2	
Input capacitance	C _{IN}	—	0 to 5.5	_	4	_	_	—	pF
Power dissipation capacitance	0	(Note 3)	3.3	_	15	_		_	pF
	C _{PD}		5.5	_	20	_		_	pF

Note 3: CPD is defined as the value of the internal equivalent capacitance which is Calculated from the operating current consumption without load.

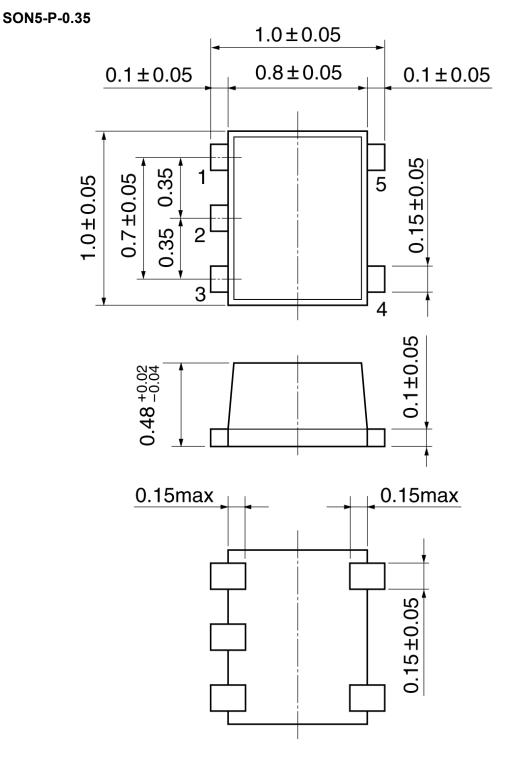
Average operating current can be obtained by the equation.

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

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Package Dimensions

Unit: mm



Weight: 0.001 g (typ.)

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