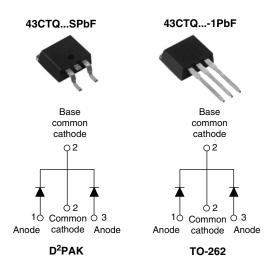


Vishay High Power Products

Schottky Rectifier, 2 x 20 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 20 A			
V_{R}	80/100 V			

FEATURES

- 175 °C T_J operation
- Center tap configuration

· Low forward voltage drop



- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for Q101 level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES		
I _{F(AV)}	Rectangular waveform	40	A	
V_{RRM}		80/100	V	
I _{FSM}	$t_p = 5 \mu s sine$	850	Α	
V _F	20 Apk, T _J = 125 °C (per leg)	0.67	V	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	43CTQ080SPbF 43CTQ080-1PbF	43CTQ100SPbF 43CTQ100-1PbF	UNITS
Maximum DC reverse voltage	V_{R}	80	100	V
Maximum working peak reverse voltage	V_{RWM}	60	100	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	per leg	1	50 % duty cycle at T _C = 135 °C, rectangular waveform		20	
See fig. 5	per device	I _{F(AV)}			40	A
Maximum peak one cycle non-repetitive			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850] ^
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	275	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ
Repetitive avalanche current per leg I _{AR}		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.50	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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43CTQ...SPbF/43CTQ...-1PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.81	V
Maximum forward voltage drop per leg		40 A		0.98	
See fig. 1		20 A	T _J = 125 °C	0.67	
		40 A		0.81	
Maximum reverse leakage current per leg	. (1)	T _J = 25 °C	V _R = Rated V _R	1	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C		11	IIIA
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.71	V
Forward slope resistance	r _t			0.43	mΩ
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		1480	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg Maximum thermal resistance, junction to case per package		D	DC operation	2.0	
		R _{thJC}		1.0	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight				2	g
Approximate weight				0.07	OZ.
Mounting torque minimum maximum				6 (5)	kgf · cm
				12 (10)	$(lbf \cdot in)$
Marking device			Case style D ² PAK Case style TO-262	43CTQ080S	
				43CTQ100S	
				43CTQ080-1	
				43CTQ100-1	



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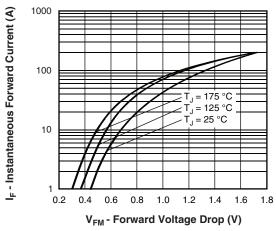


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

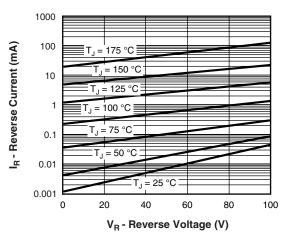


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

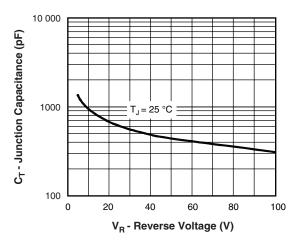


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

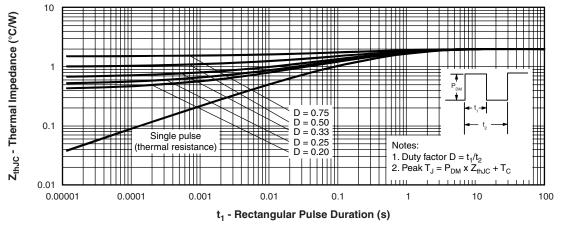


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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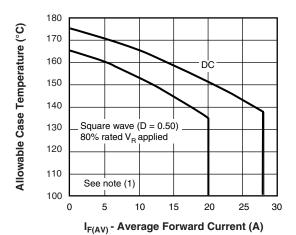


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

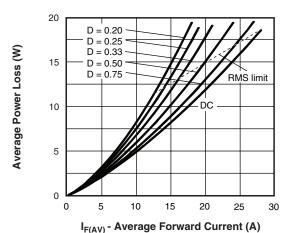


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

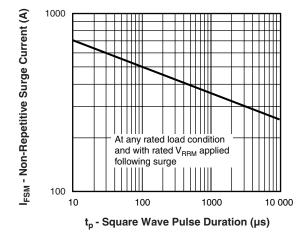


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

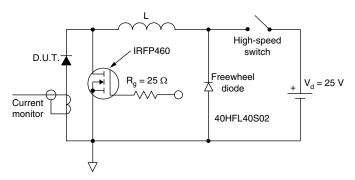


Fig. 8 - Unclamped Inductive Test Circuit

Note

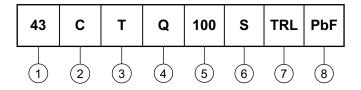
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (40 A)

2 - Circuit configuration:

C = Common cathode

3 - T = TO-220

4 - Schottky "Q" series

5 - Voltage ratings - 080 = 80 V 100 = 100 V

6 - • S = D²PAK

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented - for D²PAK only)

• TRR = Tape and reel (right oriented - for D²PAK only)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95014					
Part marking information	http://www.vishay.com/doc?95008				
Packaging information	http://www.vishay.com/doc?95032				

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