

ADSL Transformer

EPD1017G

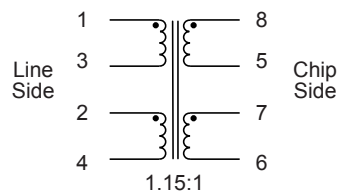
- Designed to meet Supplementary Insulation Requirements of IEC950 for 250 VAC working voltage
- Excellent THD Characteristics
- Dielectric Rating : 1 mA Max. @ 1.5K Vrms, for 1 minute

Electrical Parameters @ 25°C

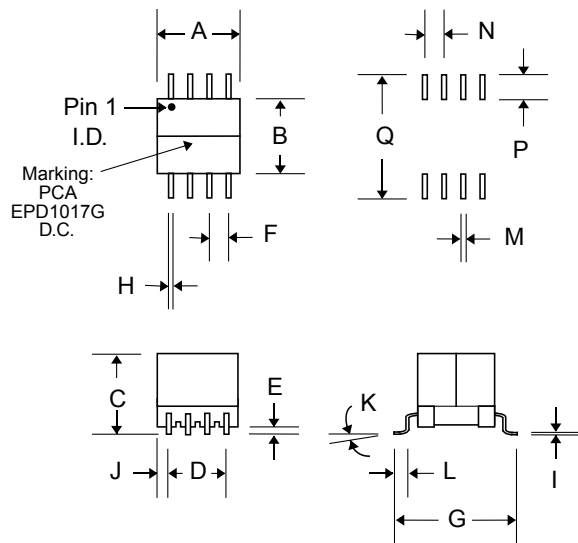
Line Side OCL ($\mu\text{H} \pm 6\%$)	Leakage Inductance (μH Max.)	Total Harmonic Distortion (dB Min.)	Longitudinal Balance (dB Min.)	DCR ($\Omega \pm 10\%$)			
				Line		Chip	
@ 10 KHz, 0.01 Vrms	@ 100 KHz, 0.01 Vrms	@ 100 KHz, 4.4 Vrms	20 KHz - 1.1 MHz				
474	15	-80	-45	1.0	1.0	0.99	1.15
Pins 1-4 with 3-2 shorted	Pins 1-4 with 3-2, 8-6 & 5-7 shorted	Line Side		Pins 1-3	Pins 2-4	Pins 7-6	Pins 8-5

- Characteristic Line Side Impedance : 100 Ω
- Characteristic Chip Side Impedance : 75 Ω

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	---	.385	---	---	9.78	---
B	---	.310	---	---	7.87	---
C	---	.415	---	---	10.54	---
D	---	---	.300	---	---	7.62
E	.010	.015	.012	.254	.381	.305
F	---	---	.100	---	---	2.54
G	---	---	.350	---	---	8.89
H	.026	.030	.028	.660	.762	.711
I	.010	.014	.012	.254	.356	.305
J	---	.043	---	---	1.09	---
K	0°	8°	4°	0°	8°	4°
L	.030	.050	.040	.762	1.27	1.02
M	---	---	.046	---	---	1.17
N	---	---	.100	---	---	2.54
P	---	---	.082	---	---	2.08
Q	---	---	.380	---	---	9.65